

EE101: BJT basics



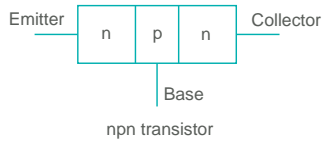
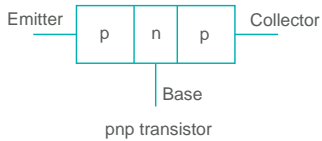
M. B. Patil

mbpatil@ee.iitb.ac.in

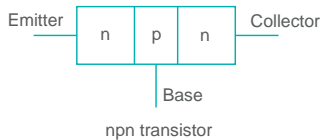
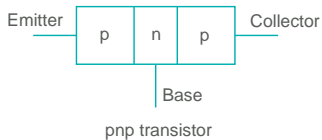
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Department of Electrical Engineering
Indian Institute of Technology Bombay

Bipolar Junction Transistors

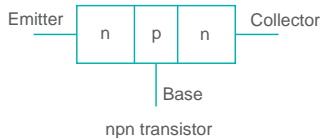
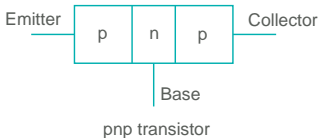


Bipolar Junction Transistors



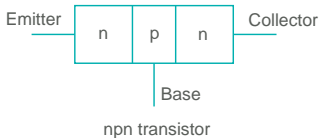
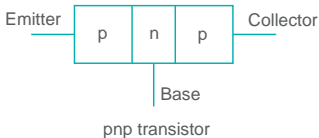
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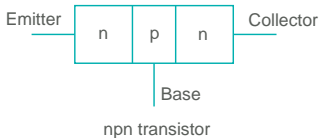
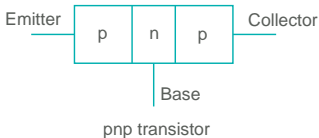


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- * Junction: device includes two p - n junctions (as opposed to a “point-contact” transistor, the first transistor)
- * Transistor: “transfer resistor”

When Bell Labs had an informal contest to name their new invention, one engineer pointed out that it acts like a resistor, but a resistor where the voltage is transferred across the device to control the resulting current.

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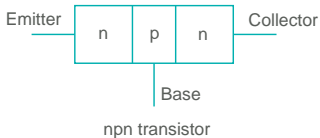
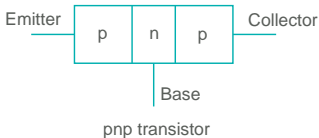
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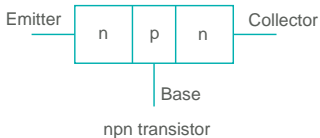
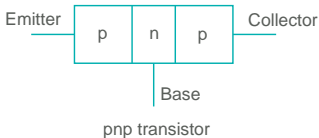
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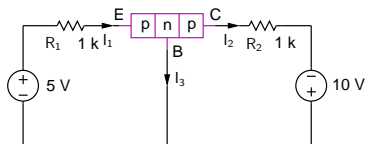
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WRONG! Let us see why.

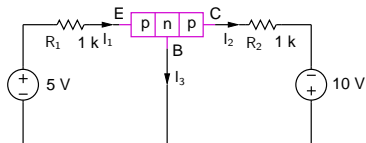
Bipolar Junction Transistors

Consider a *pnp* BJT in the following circuit:

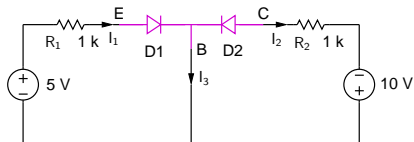


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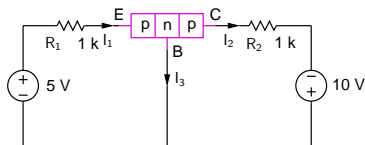


If the transistor is replaced with two diodes connected back-to-back, we get,

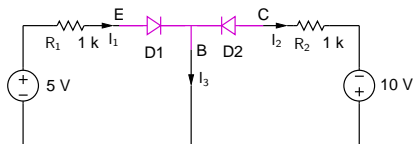


Bipolar Junction Transistors

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Assuming $V_{on} = 0.7 \text{ V}$ for D1, we get

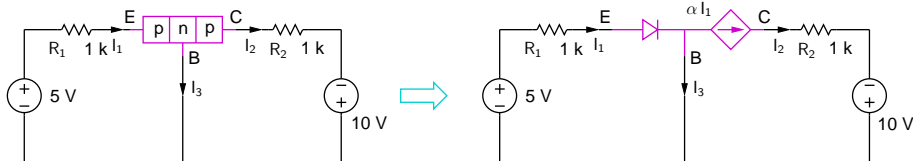
$$I_1 = \frac{5 \text{ V} - 0.7 \text{ V}}{R_1} = 4.3 \text{ mA},$$

$I_2 = 0$ (since D2 is reverse biased), and

$$I_3 \approx I_1 = 4.3 \text{ mA}.$$

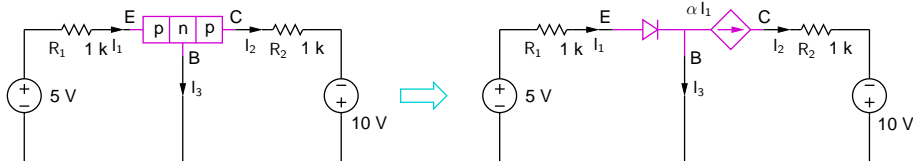
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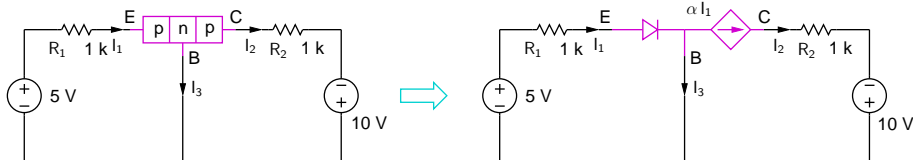


We now get,

$$I_1 = \frac{5\text{ V} - 0.7\text{ V}}{R_1} = 4.3\text{ mA (as before),}$$

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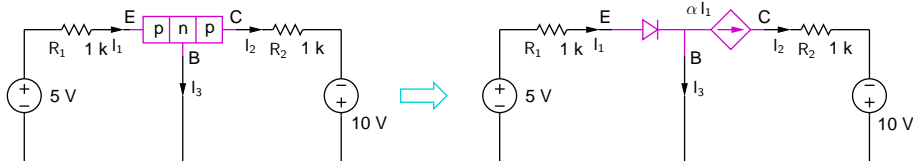
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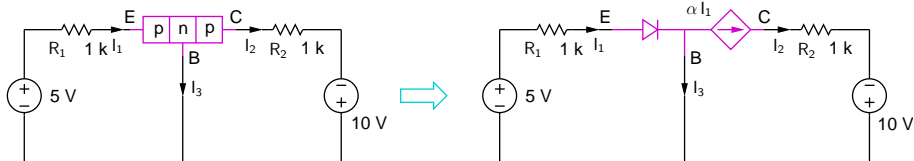
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$$I_3 = I_1 - I_2 = (1 - \alpha) I_1 \approx 0\text{ A.}$$

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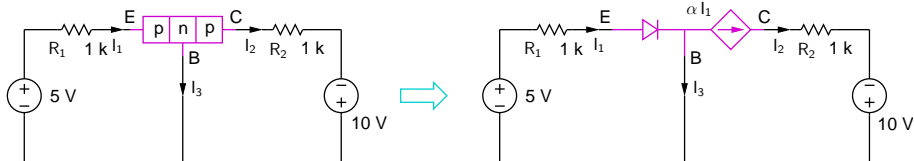
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Conclusion: A BJT is NOT the same as two diodes connected back-to-back (although it does have two p - n junctions).

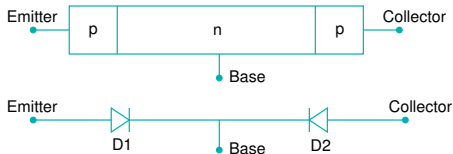
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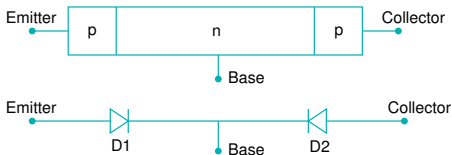
- * When we replace a BJT with two diodes, we assume that there is no interaction between the two diodes, which may be expected if they are “far apart.”



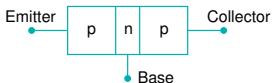
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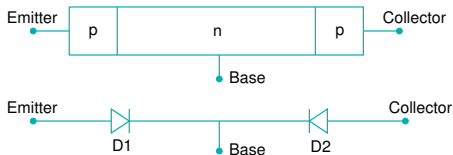
- * However, in a BJT, exactly the opposite is true. For a higher performance, the base region is made as short as possible (subject to certain constraints), and the two diodes therefore cannot be treated as independent devices.



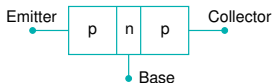
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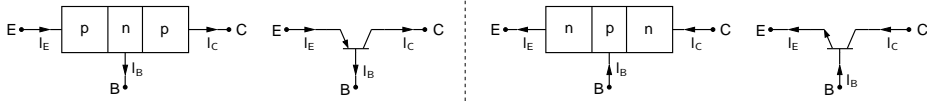


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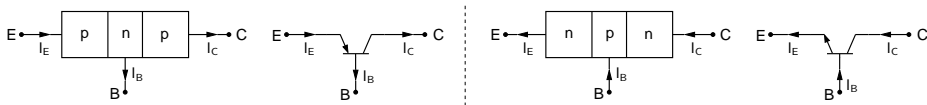


- * Later, we will look at the “Ebers-Moll model” of a BJT, which is a fairly accurate representation of the transistor action.

BJT in active mode

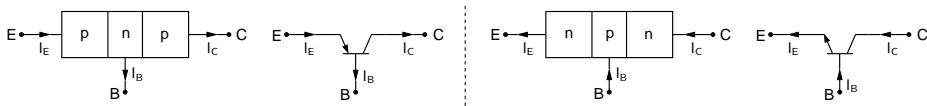


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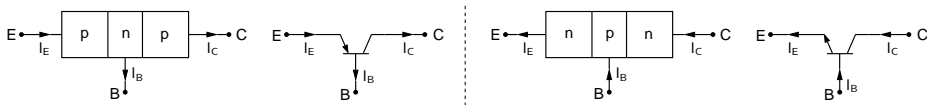
- * In the active mode of a BJT, the B-E junction is under forward bias, and the B-C junction is under reverse bias.
 - For a *pnp* transistor, $V_{EB} > 0$ V, and $V_{CB} < 0$ V.
 - For an *npn* transistor, $V_{BE} > 0$ V, and $V_{BC} < 0$ V.

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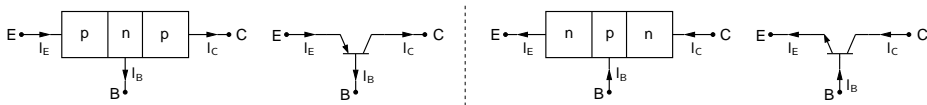
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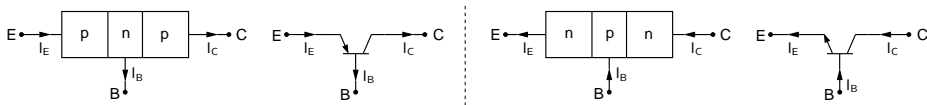
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- * Since the B-E junction is under forward bias, the voltage (magnitude) is typically 0.6 to 0.75 V.
- * The B-C voltage can be several Volts (or even hundreds of Volts), and is limited by the breakdown voltage of the B-C junction.

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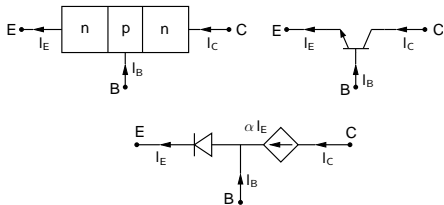
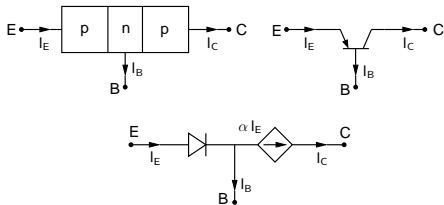
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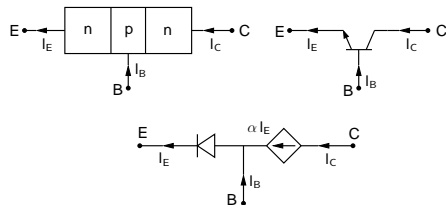
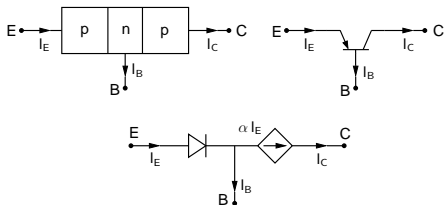


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- * The symbol for a BJT includes an arrow for the emitter terminal, its direction indicating the current direction when the transistor is in active mode.
- * Analog circuits, including amplifiers, are generally designed to ensure that the BJTs are operating in the active mode.

BJT in active mode

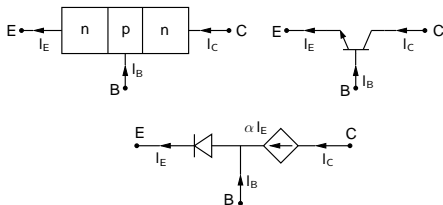
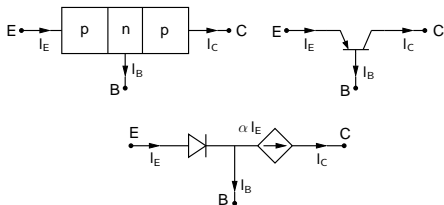


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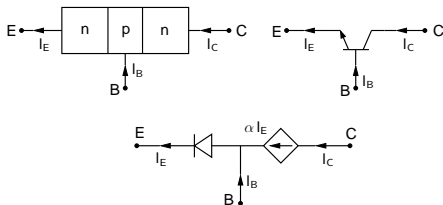
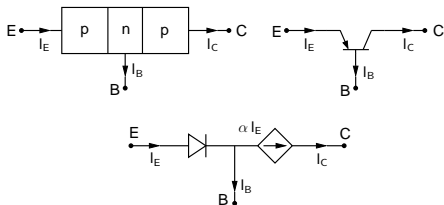
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BJT in active mode



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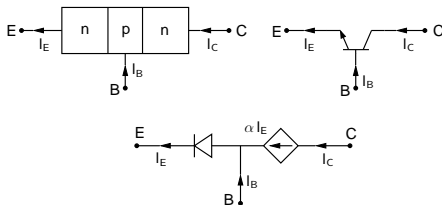
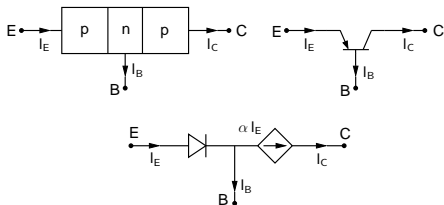
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- * $I_B = I_E - I_C = I_E (1 - \alpha)$.
- * The ratio I_C / I_B is defined as the current gain β of the transistor.

$$\beta = \frac{I_C}{I_B} = \frac{\alpha}{1 - \alpha}.$$

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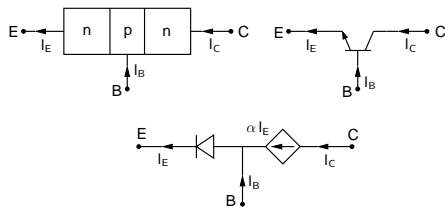
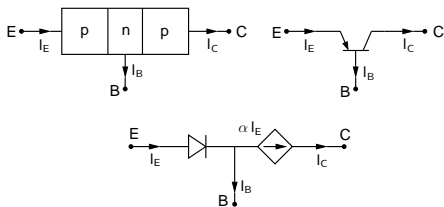


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- * The ratio I_C / I_B is defined as the current gain β of the transistor.

$$\beta = \frac{I_C}{I_B} = \frac{\alpha}{1 - \alpha}.$$

- * β is a function of I_C and temperature. However, we will generally treat it as a constant, a useful approximation to simplify things and still get a good insight.

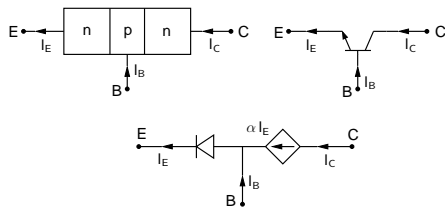
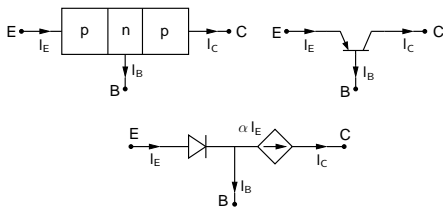
BJT in active mode



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α	β
0.9	9
0.95	19
0.99	99
0.995	199

BJT in active mode

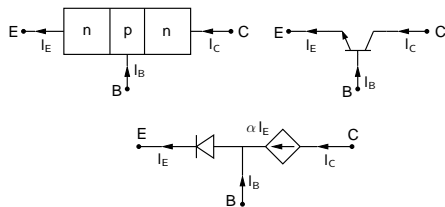
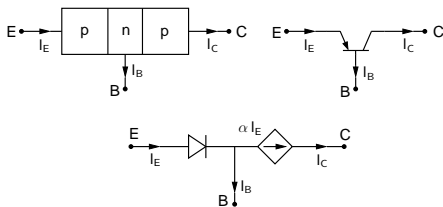


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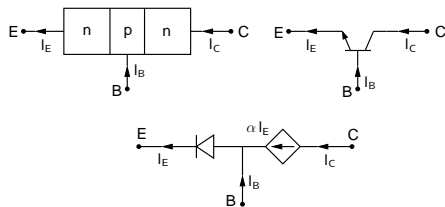
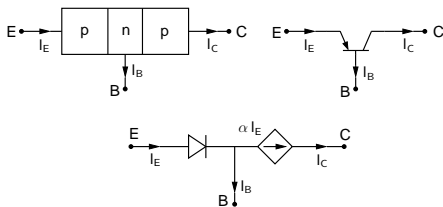


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- * β is a sensitive function of α .
- * Transistors are generally designed to get a high value of β (typically 100 to 250, but can be as high as 2000 for “super- β ” transistors).

BJT in active mode

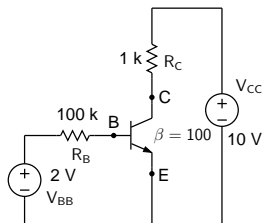


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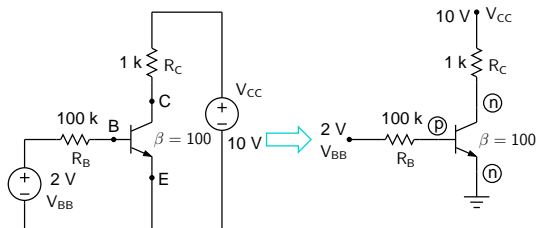
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- * β is a sensitive function of α .
- * Transistors are generally designed to get a high value of β (typically 100 to 250, but can be as high as 2000 for “super- β ” transistors).
- * A large $\beta \Rightarrow I_B \ll I_C$ or I_E when the transistor is in the active mode.

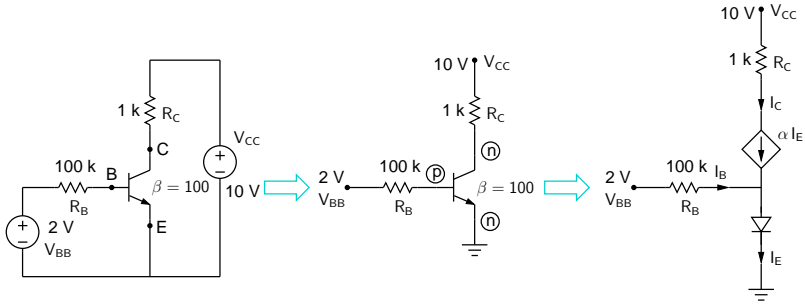
A simple BJT circuit



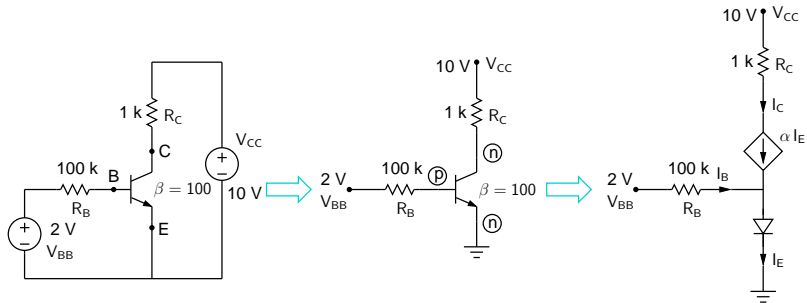
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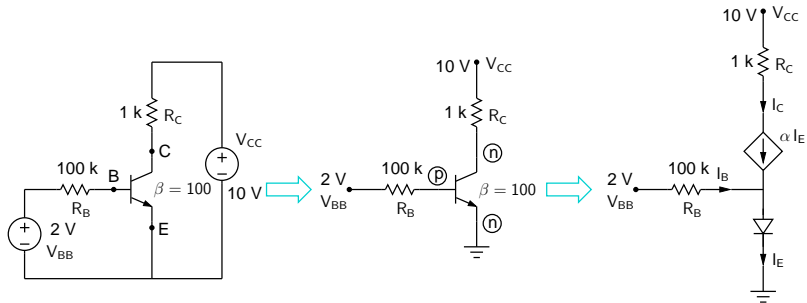


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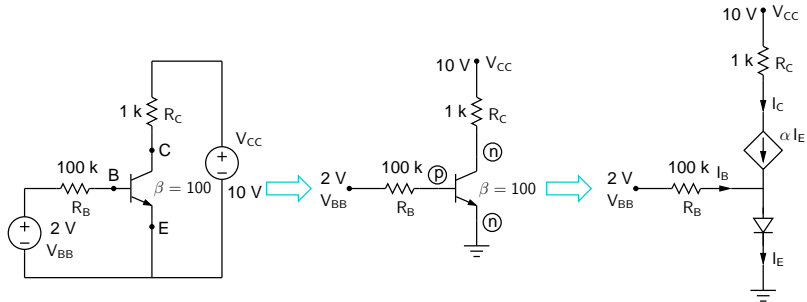
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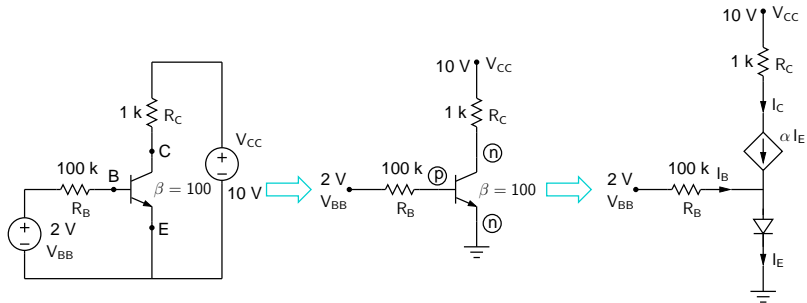


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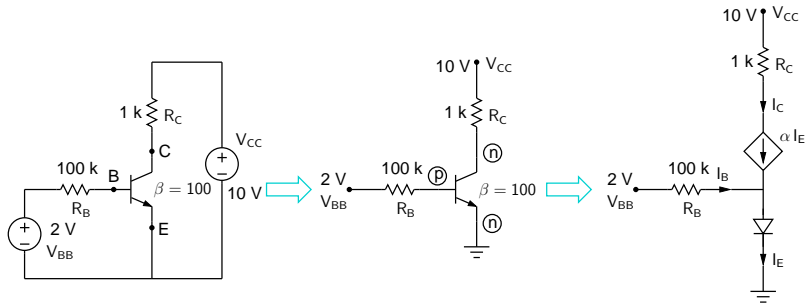
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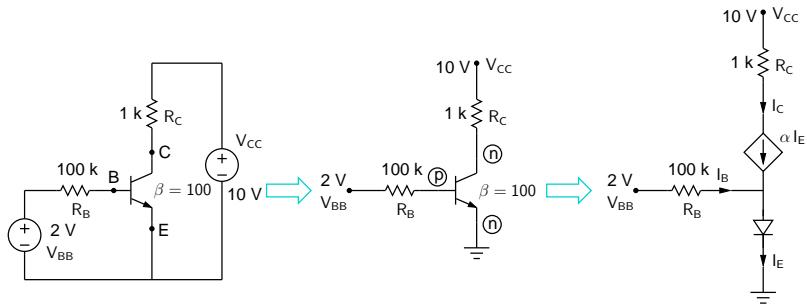
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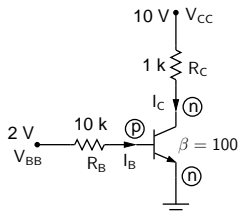
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$$V_{BC} = V_B - V_C = 0.7 \text{ V} - 8.7 \text{ V} = -8.0 \text{ V},$$

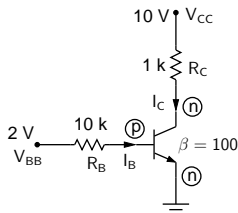
i.e., the B-C junction is indeed under reverse bias.

A simple BJT circuit (continued)



What happens if R_B is changed from 100 k to 10 k ?

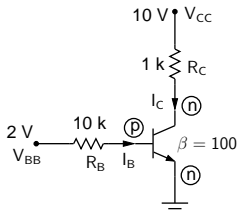
A simple BJT circuit (continued)



What happens if R_B is changed from 100 k to 10 k ?

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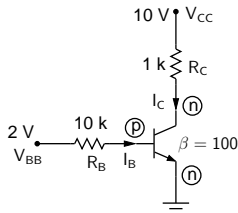
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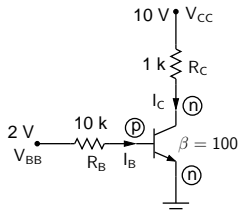
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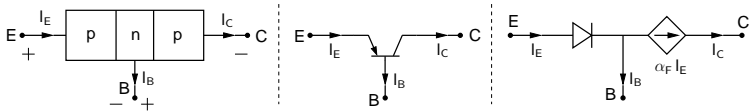
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V_{BC} is not only positive, it is *huge*!

The BJT cannot be in the active mode, and we need to take another look at the circuit.

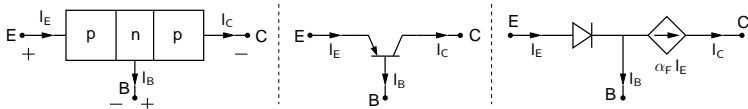
Ebers-Moll model for a *pnp* transistor

Active mode ("forward" active mode): B-E in f. b., B-C in r. b.

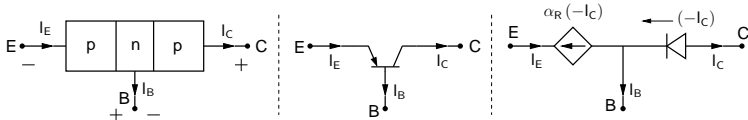


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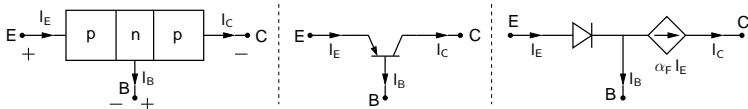


Reverse active mode: B-E in r. b., B-C in f. b.

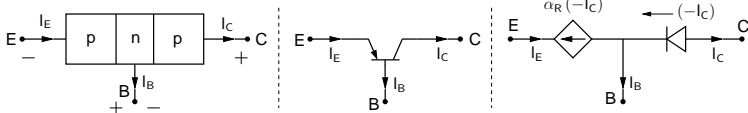


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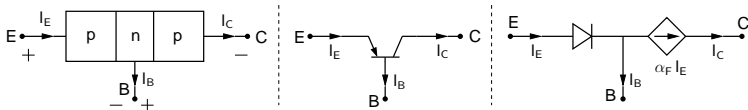
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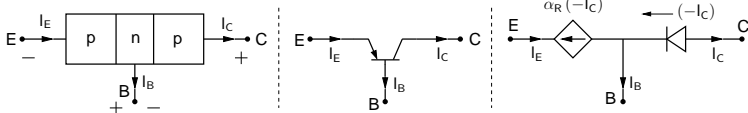
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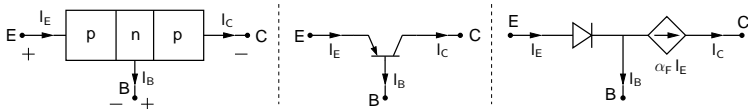


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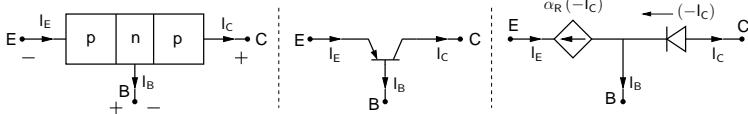
The two α 's, α_F ("forward" α) and α_R ("reverse" α) are generally quite different.

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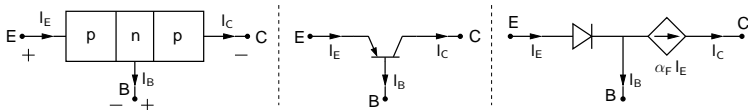
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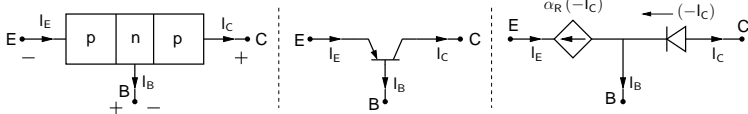
Typically, $\alpha_F > 0.98$, and α_R is in the range from 0.02 to 0.5.

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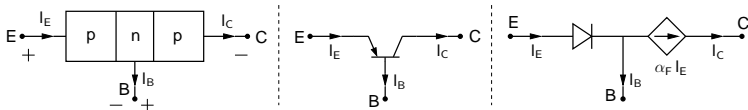
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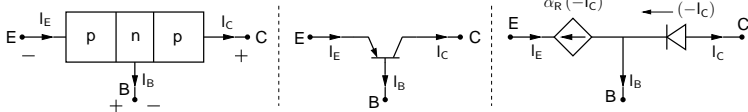
The corresponding current gains (β_F and β_R) differ significantly, since $\beta = \alpha/(1 - \alpha)$.

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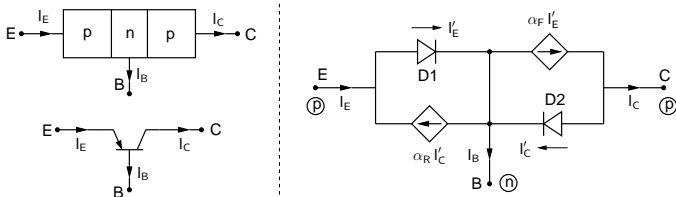
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In amplifiers, the BJT is biased in the forward active mode (simply called the "active mode") in order to make use of the higher value of β in that mode.

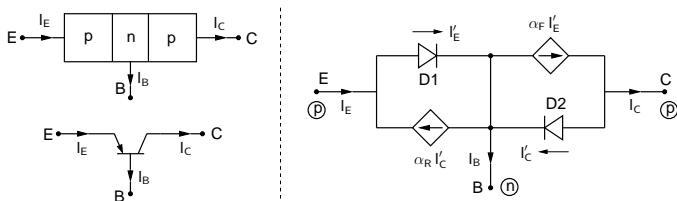
Ebers-Moll model for a *pnp* transistor

The Ebers-Moll model combines the forward and reverse operations of a BJT in a single comprehensive model.



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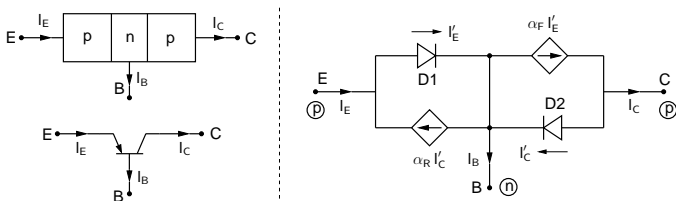


The currents I'_E and I'_C are given by the Shockley diode equation:

$$I'_E = I_{ES} \left[\exp \left(\frac{V_{EB}}{V_T} \right) - 1 \right], \quad I'_C = I_{CS} \left[\exp \left(\frac{V_{CB}}{V_T} \right) - 1 \right].$$

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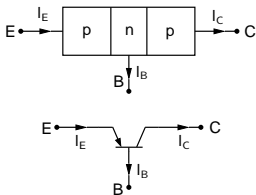


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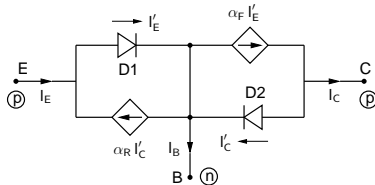
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Mode	B-E	B-C	
Forward active	forward	reverse	$I'_E \gg I'_C$
Reverse active	reverse	forward	$I'_C \gg I'_E$
Saturation	forward	forward	I'_E and I'_C are comparable.
Cut-off	reverse	reverse	I'_E and I'_C are negligbl.

Ebers-Moll model

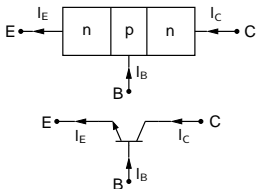


pnp transistor

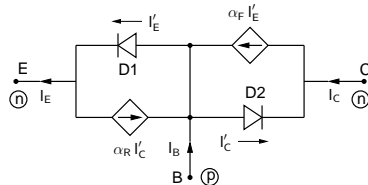


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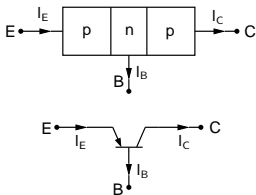
npn transistor



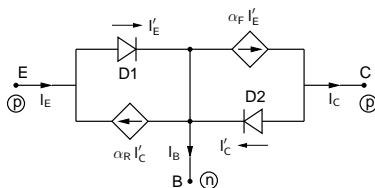
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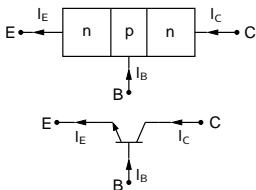


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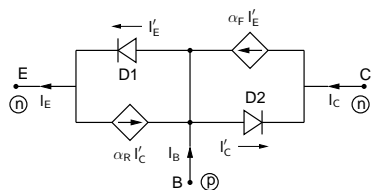


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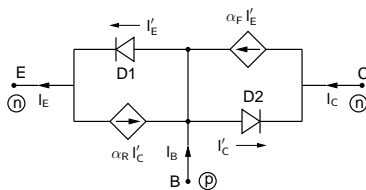
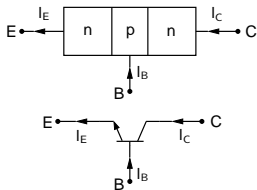


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For an *n*pn transistor, the same model holds with current directions and voltage polarities suitably changed.

I_C - V_{CE} characteristics



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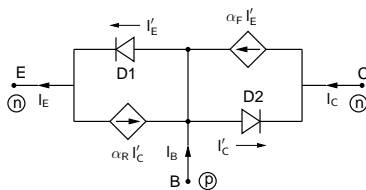
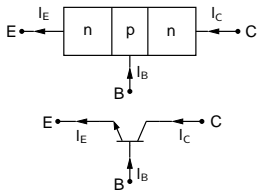
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A BJT is a three-terminal device, and its I - V characteristics can therefore be represented in several different ways. The I_C versus V_{CE} characteristics are very useful in amplifiers.

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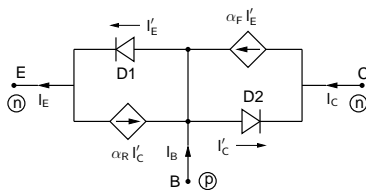
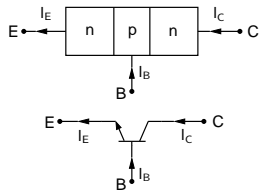
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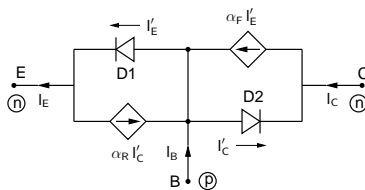
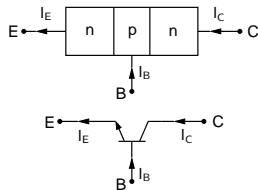
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There are several ways to assign V_{BE} and V_{CB} so that they satisfy the constraint:

$$V_{CB} + V_{BE} = (V_C - V_B) + (V_B - V_E) = V_{CE} = 5 \text{ V}.$$

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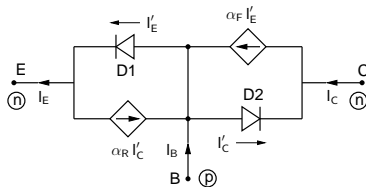
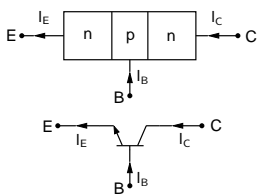
To start with, we consider a single point, $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.

There are several ways to assign V_{BE} and V_{CB} so that they satisfy the constraint:

$$V_{CB} + V_{BE} = (V_C - V_B) + (V_B - V_E) = V_{CE} = 5 \text{ V}.$$

Let us consider some of these possibilities.

I_C - V_{CE} characteristics



$$I'_E = I_{ES} [\exp(V_{BE}/V_T) - 1]$$

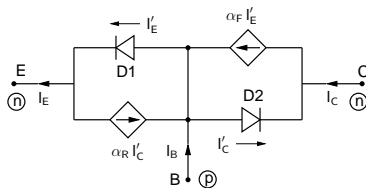
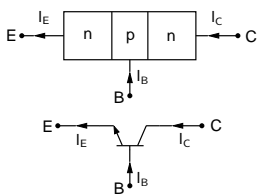
$$I'_C = I_{CS} [\exp(V_{BC}/V_T) - 1]$$

$$\alpha_F = 0.99, \quad I_{SE} = 1 \times 10^{-14} \text{ A}$$

$$\alpha_R = 0.50, \quad I_{SC} = 2 \times 10^{-14} \text{ A}$$

Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.

I_C - V_{CE} characteristics



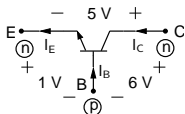
$$I'_E = I_{ES} [\exp(V_{BE}/V_T) - 1]$$

$$I'_C = I_{CS} [\exp(V_{BC}/V_T) - 1]$$

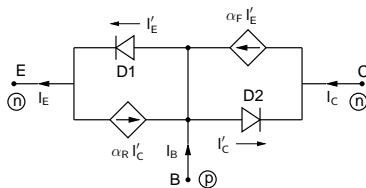
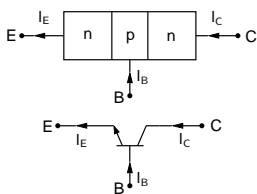
$$\alpha_F = 0.99, \quad I_{SE} = 1 \times 10^{-14} \text{ A}$$

$$\alpha_R = 0.50, \quad I_{SC} = 2 \times 10^{-14} \text{ A}$$

Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.



I_C - V_{CE} characteristics



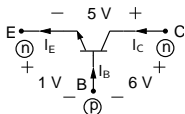
$$I'_E = I_{ES} [\exp(V_{BE}/V_T) - 1]$$

$$I'_C = I_{CS} [\exp(V_{BC}/V_T) - 1]$$

$$\alpha_F = 0.99, \quad I_{SE} = 1 \times 10^{-14} \text{ A}$$

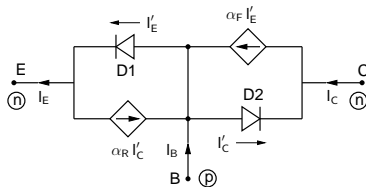
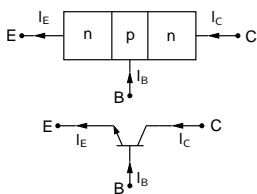
$$\alpha_R = 0.50, \quad I_{SC} = 2 \times 10^{-14} \text{ A}$$

Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.



D1 and D2 are both off, and we cannot satisfy the condition, $I_B = 10 \mu\text{A}$, since all currents are much smaller than $10 \mu\text{A}$.

I_C - V_{CE} characteristics



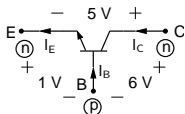
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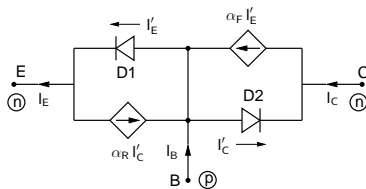
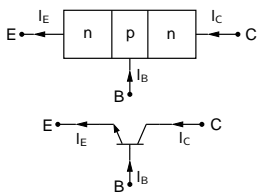
Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.



D1 and D2 are both off, and we cannot satisfy the condition, $I_B = 10 \mu\text{A}$, since all currents are much smaller than $10 \mu\text{A}$.

\Rightarrow This possibility (and similarly others with both junctions reverse biased) is ruled out.

I_C - V_{CE} characteristics



$$I'_E = I_{ES} [\exp(V_{BE}/V_T) - 1]$$

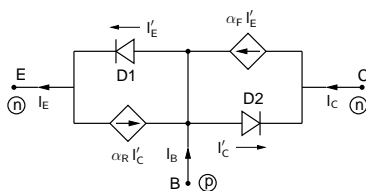
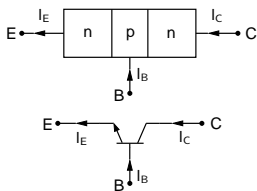
$$I'_C = I_{CS} [\exp(V_{BC}/V_T) - 1]$$

$$\alpha_F = 0.99, \quad I_{SE} = 1 \times 10^{-14} \text{ A}$$

$$\alpha_R = 0.50, \quad I_{SC} = 2 \times 10^{-14} \text{ A}$$

Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.

I_C - V_{CE} characteristics



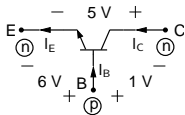
$$I'_E = I_{ES} [\exp(V_{BE}/V_T) - 1]$$

$$I'_C = I_{CS} [\exp(V_{BC}/V_T) - 1]$$

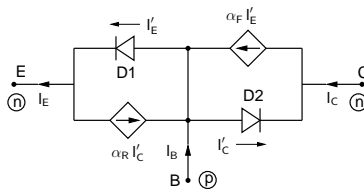
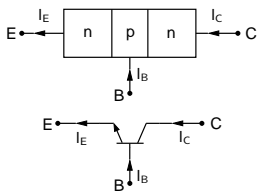
$$\alpha_F = 0.99, \quad I_{SE} = 1 \times 10^{-14} \text{ A}$$

$$\alpha_R = 0.50, \quad I_{SC} = 2 \times 10^{-14} \text{ A}$$

Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.



I_C - V_{CE} characteristics



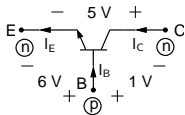
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$$\alpha_F = 0.99, \quad I_{SE} = 1 \times 10^{-14} \text{ A}$$

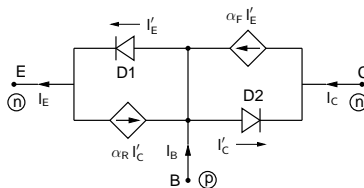
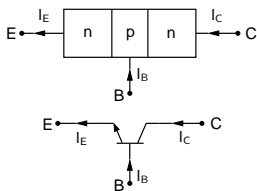
$$\alpha_R = 0.50, \quad I_{SC} = 2 \times 10^{-14} \text{ A}$$

Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.



D1 and D2 are both conducting; however, the forward bias for the B-E junction is impossibly large.

I_C - V_{CE} characteristics



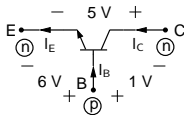
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$$\alpha_F = 0.99, \quad I_{SE} = 1 \times 10^{-14} \text{ A}$$

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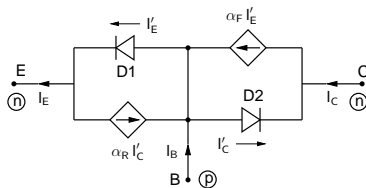
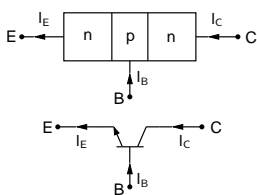
Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.



D1 and D2 are both conducting; however, the forward bias for the B-E junction is impossibly large.

⇒ This possibility is also ruled out.

I_C - V_{CE} characteristics



$$I'_E = I_{ES} [\exp(V_{BE}/V_T) - 1]$$

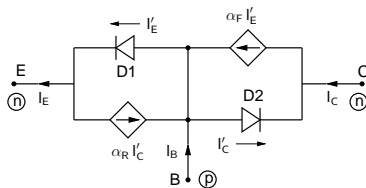
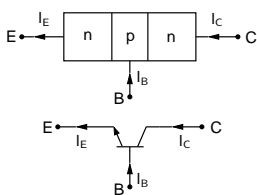
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$$\alpha_R = 0.50, \quad I_{SC} = 2 \times 10^{-14} \text{ A}$$

Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.

I_C - V_{CE} characteristics



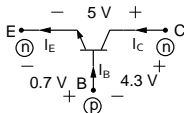
$$I'_E = I_{ES} [\exp(V_{BE}/V_T) - 1]$$

$$I'_C = I_{CS} [\exp(V_{BC}/V_T) - 1]$$

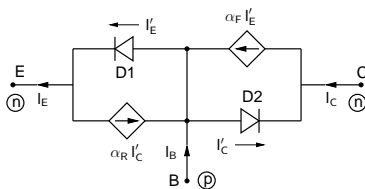
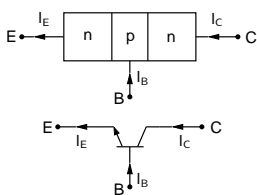
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Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.



I_C - V_{CE} characteristics



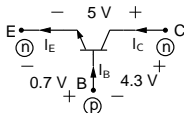
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$$\alpha_F = 0.99, \quad I_{SE} = 1 \times 10^{-14} \text{ A}$$

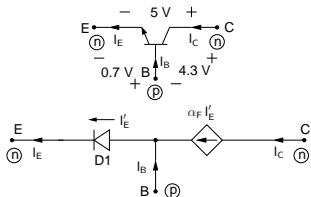
$$\alpha_R = 0.50, \quad I_{SC} = 2 \times 10^{-14} \text{ A}$$

Constraints: $I_B = 10 \mu\text{A}$, $V_{CE} = 5 \text{ V}$.



D1 is on, D2 is off. This is a realistic possibility. Since the B-C junction is under reverse bias, I'_C and $\alpha_R I'_C$ are much smaller than I'_E , and therefore the lower branches in the Ebers-Moll model can be dropped (see next slide).

I_C - V_{CE} characteristics

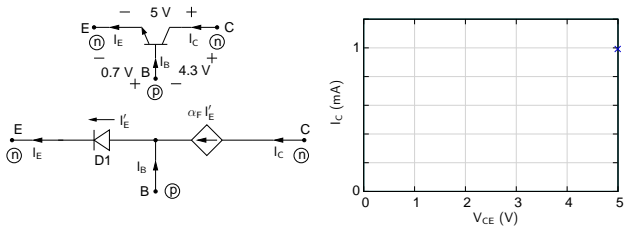


(The actual values for V_{BE} and V_{CB} obtained by solving the Ebers-Moll equations are $V_{BE} = 0.656$ V and $V_{CB} = 4.344$ V.)

The BJT is in the active mode, and therefore

$$I_C = \beta I_B = \frac{\alpha_F}{1 - \alpha_F} I_B = 99 \times 10 \mu A = 0.99 \text{ mA}.$$

I_C - V_{CE} characteristics

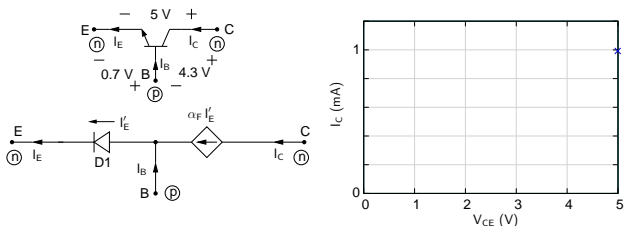


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I_C - V_{CE} characteristics



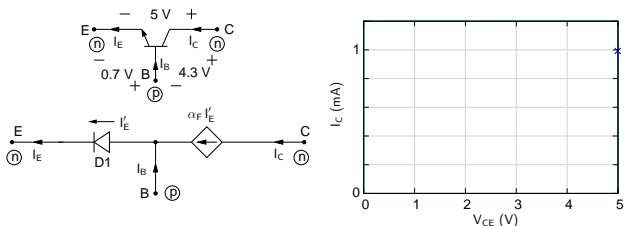
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If V_{CE} is reduced to, say, 4 V, and I_B kept at $10 \mu A$, our previous argument holds, and once again, we find that $I_C = \beta I_B = 0.99$ mA.

I_C - V_{CE} characteristics



(The actual values for V_{BE} and V_{CB} obtained by solving the Ebers-Moll equations are $V_{BE} = 0.656$ V and $V_{CB} = 4.344$ V.)

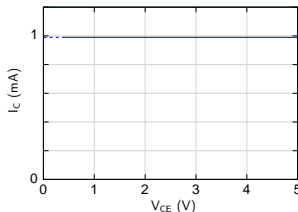
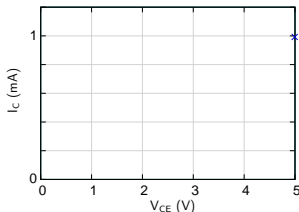
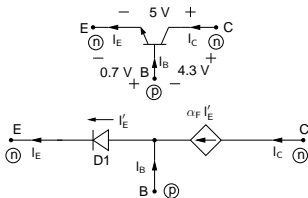
The BJT is in the active mode, and therefore

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If V_{CE} is reduced to, say, 4 V, and I_B kept at $10 \mu A$, our previous argument holds, and once again, we find that $I_C = \beta I_B = 0.99$ mA.

Thus, the plot of I_C versus V_{CE} is simply a horizontal line.

I_C - V_{CE} characteristics



(The actual values for V_{BE} and V_{CB} obtained by solving the Ebers-Moll equations are $V_{BE} = 0.656$ V and $V_{CB} = 4.344$ V.)

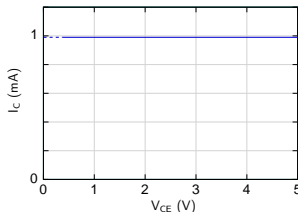
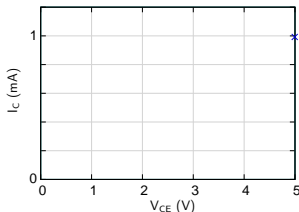
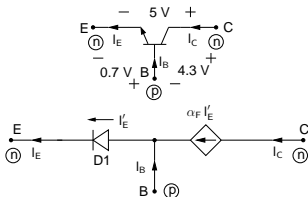
The BJT is in the active mode, and therefore

$$I_C = \beta I_B = \frac{\alpha_F}{1 - \alpha_F} I_B = 99 \times 10 \mu A = 0.99 \text{ mA}.$$

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Thus, the plot of I_C versus V_{CE} is simply a horizontal line.

I_C - V_{CE} characteristics



(The actual values for V_{BE} and V_{CB} obtained by solving the Ebers-Moll equations are $V_{BE} = 0.656$ V and $V_{CB} = 4.344$ V.)

The BJT is in the active mode, and therefore

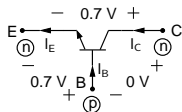
$$I_C = \beta I_B = \frac{\alpha_F}{1 - \alpha_F} I_B = 99 \times 10 \mu A = 0.99 \text{ mA}.$$

If V_{CE} is reduced to, say, 4 V, and I_B kept at $10 \mu A$, our previous argument holds, and once again, we find that $I_C = \beta I_B = 0.99$ mA.

Thus, the plot of I_C versus V_{CE} is simply a horizontal line.

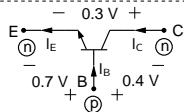
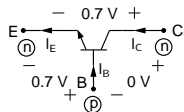
However, as $V_{CE} \rightarrow 0$ V, things change (see next slide).

I_C - V_{CE} characteristics



When $V_{CE} \approx 0.7 \text{ V}$ (and I_B kept at $10 \mu\text{A}$), the B-C drop is about 0 V .

I_C - V_{CE} characteristics

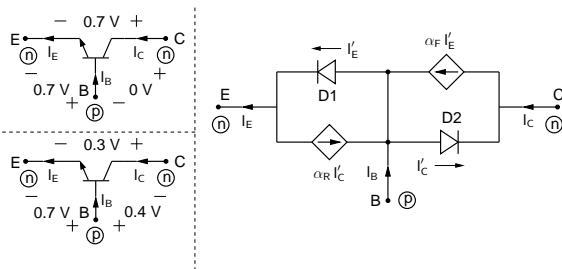


When $V_{CE} \approx 0.7 \text{ V}$ (and I_B kept at $10 \mu\text{A}$), the B-C drop is about 0 V .

As V_{CE} is reduced further, the B-C junction gets forward biased. For example, with $V_{CE} = 0.3 \text{ V}$, we may have a voltage distribution shown in the figure.

(The numbers are only representative; the actual V_{BE} and V_{BC} values can be obtained by solving the E-M equations.)

I_C - V_{CE} characteristics



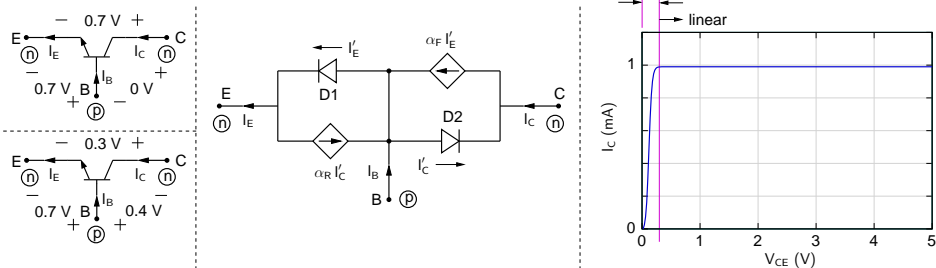
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(The numbers are only representative; the actual V_{BE} and V_{BC} values can be obtained by solving the E-M equations.)

Now, the component I'_C in the E-M model becomes significant, $I_C = \alpha_F I'_E - I'_C$ reduces, and I_C becomes smaller than βI_B .

I_C - V_{CE} characteristics



When $V_{CE} \approx 0.7\text{ V}$ (and I_B kept at $10\text{ }\mu\text{A}$), the B-C drop is about 0 V .

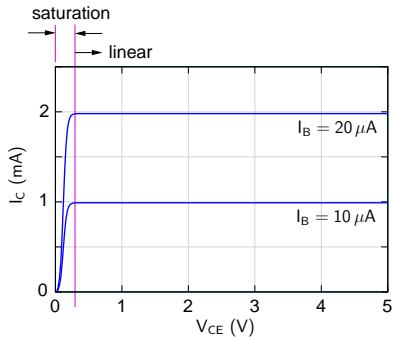
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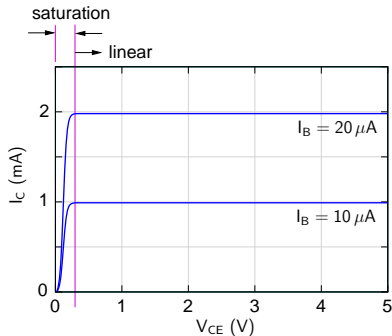
The region where $I_C < \beta I_B$ is called the "saturation region."

I_C - V_{CE} characteristics



If I_B is doubled (from $10 \mu\text{A}$ to $20 \mu\text{A}$), $I_C = \beta I_B$ changes by a factor of 2 in the linear region. Apart from that, there is no qualitative change in the $I_C - V_{CE}$ plot.

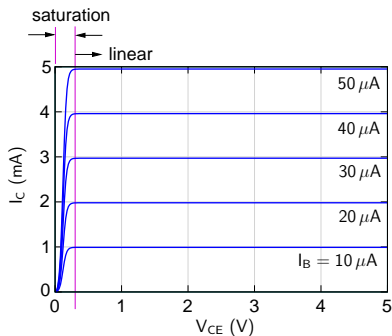
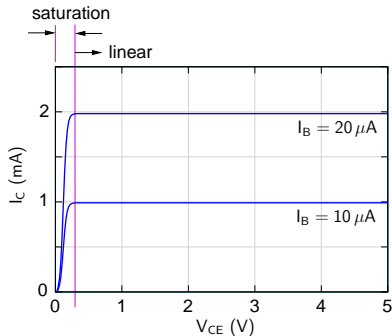
I_C - V_{CE} characteristics



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Clearly, the $I_C - V_{CE}$ behaviour of a BJT is not represented by a single curve but by a *family* of curves, known as the “ $I_C - V_{CE}$ characteristics.”

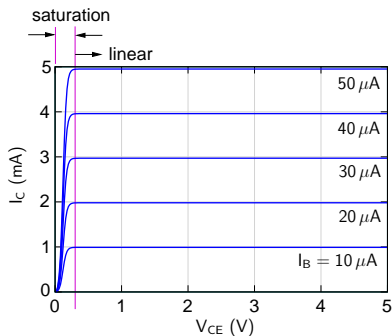
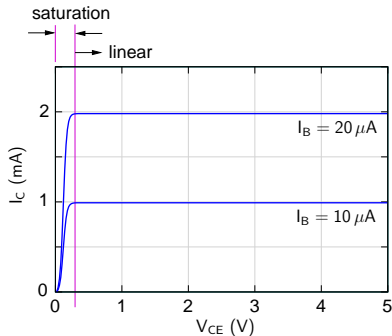
I_C - V_{CE} characteristics



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$I_C - V_{CE}$ characteristics

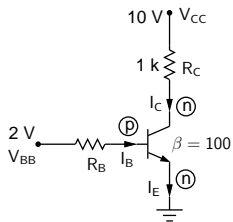


If I_B is doubled (from $10 \mu A$ to $20 \mu A$), $I_C = \beta I_B$ changes by a factor of 2 in the linear region. Apart from that, there is no qualitative change in the $I_C - V_{CE}$ plot.

Clearly, the $I_C - V_{CE}$ behaviour of a BJT is not represented by a single curve but by a *family* of curves, known as the “ $I_C - V_{CE}$ characteristics.”

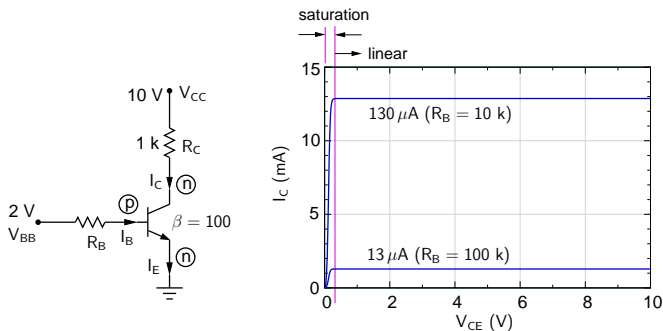
The $I_E - V_{CB}$ and $I_C - V_{BE}$ characteristics of a BJT are also useful in understanding BJT circuits.

A simple BJT circuit (revisited)



We are now in a position to explain what happens when R_B is decreased from 100 k to 10 k in the above circuit.

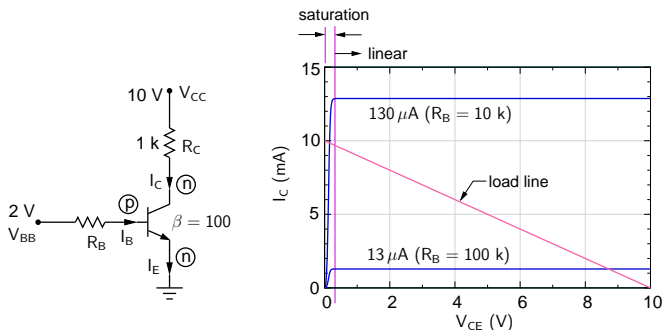
A simple BJT circuit (revisited)



We are now in a position to explain what happens when R_B is decreased from 100 k to 10 k in the above circuit.

Let us plot $I_C - V_{CE}$ curves for $I_B \approx \frac{V_{BB} - 0.7 \text{ V}}{R_B}$ for the two values of R_B .

A simple BJT circuit (revisited)

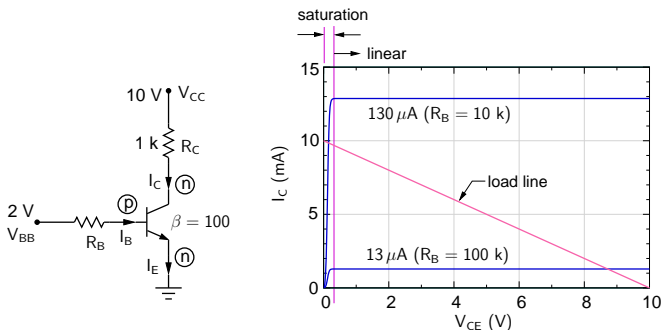


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In addition to the BJT $I_C - V_{CE}$ curve, the circuit variables must also satisfy the constraint, $V_{CC} = V_{CE} + I_C R_C$, a straight line in the $I_C - V_{CE}$ plane.

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The intersection of the load line and the BJT characteristics gives the solution for the circuit. For $R_B = 10 \text{ k}$, note that the BJT operates in the saturation region, leading to $V_{CE} \approx 0.2 \text{ V}$, and $I_C = 9.8 \text{ mA}$.