

Anukool Rajoriya

Research Scholar

Center of Excellence in Nanoelectronics

Department of Electrical Engineering

Indian Institute of Technology Bombay

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INTEREST: Physics and Modeling of Nanoscale devices

EDUCATION: **Ph.D** in Electrical Engineering, Indian Institute of Technology Bombay. India **2014 (expected)**

Thesis: Super steep subthreshold Nano devices

Master of Technology in Electrical Engineering, Indian Institute of Technology Kanpur. India. **July 06-June 08**

Thesis: A study of various encapsulations method and its effect on Organic Light Emitting Diode (OLED) degradation.

Bachelor of Engineering in Electronics and Communications Engineering, RGTU Bhopal. India **June 02-June 06**

Project: Moving Message Display using 8051 microcontroller

PUBLICATION:

Journal:

1. Seena V, **Anukool Rajoriya**, Parita Pant, Soumya Mukherji and V Ramgopal Rao, "Polymer Microcantilever Biochemical Sensors with Integrated Polymer Composites for Electrical Detection", July 2009, Solid State Sciences, Elsevier.

Conference Proceedings:

1. **Anukool Rajoriya**, J Narain, Deepak, "Effect of Thermal Annealing on the Dark Spots Reduction in Organic Light Emitting Diode", International Workshop on Physics of Semiconductor Devices, IWPSD Dec 2009, New Delhi, India.
2. Seena V, **Anukool Rajoriya**, Avil Fernaundus, Karuna Dhale, Prita Pant, Soumyo Mukherji, V Ramgopal Rao, "Fabrication and Characterization of a Novel Polymer Composite Microcantilever Sensors for Explosive Detection", IEEE MEMS 2010.
3. Ramesh Navan, K. Prashanthi, **Anukool Rajoriya**, M. Shojaei Baghini, V. R. Palkar, V. Ramgopal Rao, "A Novel High-k ($k > 40$) Gate Dielectric for Pentacene Organic Thin Film Transistors", Seventeenth Annual International Conference on Composites/Nano Engineering, ICCE-17 July 26-31, 2009 in Hawaii, USA.

RESEARCH EXPERIENCE: Research Assistant, Department of Electrical Engineering, IIT Bombay, India **July 08-July 09**
Guide: Prof. V. Ramgopal Rao

As a graduate student, SAMTEL Center for Display Technologies, IIT Kanpur, India. **May 08-June 08**
Guides: Dr. J. Narain, Dr. Deepak and Dr. SSK Iyer.

TEACHING EXPERIENCE: Teaching Assistant, Department of Electrical Engineering. Indian Institute of Technology Kanpur **July 06-June 08**

Teaching Assistant, Department of Electrical Engineering. Indian Institute of Technology Bombay **July 09-present**

SKILLS:

Technology CAD: Sentaurus TCAD
Fabrication: Working experience in class 100/1000 clean room both at IIT Kanpur and IIT Bombay, Optical Lithography, and thin film deposition

Characterization: SEM, Four Probe measurement, Electrical Characterization, Profilometer
Programming: C, C++

Language:

Software Packages: MATLAB, Microcap, Ngspice, Mentor Graphics IC Suite, Tanner EDA Tool, AutoCAD

Language: English, Hindi

ACHIEVEMENTS:

1. Got **All India Rank 307** (top 1%) in Graduate Aptitude Test in Engineering (GATE) 2006.
2. Department Rank 1st in B.E. second year.

RELEVANT COURSES:

- Solid State Devices
- Physics of Nanostructures
- Quantum Mechanics
- Organic Electronics
- Physical Electronics
- VLSI Technology
- VLSI Design
- Lab in VLSI

Personal Information:

Date of Birth: June 05 1985

Nationality: Indian

Present Address: Room No. 153, Hostel 1, IIT Bombay, Powai, Mumbai-400076. India

Permanent Address: Dashmesh Colony, SurajGanj, Itarsi-461111. India