

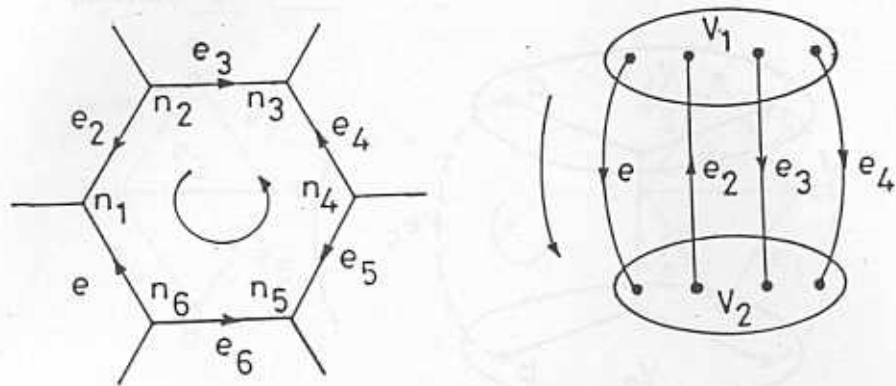


Figure 3.6: Circuit subgraph and Crossing Edge Set with Orientations