

Figure 3.2: A Path Graph

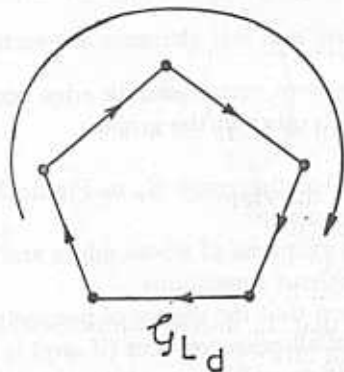
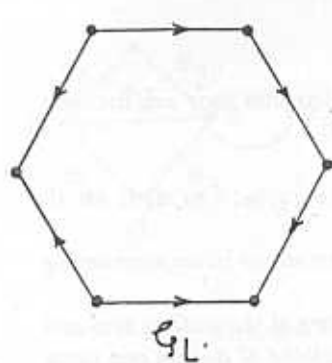


Figure 3.3: A Circuit Graph and a Strongly Directed Circuit Graph