

Day 0 (Nov 22, 2015 – Sunday)

2:00	HALF DAY TUTORIAL Post Silicon Debug and Patchable Design Masahiro Fujita, V. Singh, and Rajesh Mittal
5:00	

Day 1 (Nov 23, 2015 – Monday)

9:00	Symposium Inauguration PLENARY SESSION Keynote 1: Subhasish Mitra (Stanford University) Keynote 2: Friedrich Hapke (Mentor Graphics)
9:30	
10:00	
10:30	
11:00	

11:30	Session 1A	Session 1B
12:00	1.5 hours -3 papers	1.5 hours -3 papers
12:30		
1:00		

1:30 LUNCH

2:00	Session 2A	Session 2B
2:30	2 hours – 4 papers	2 hours – 4 papers
3:00		
3:30		
4:00		

4:30	Session 3A	Session 3B
5:00	1.5 hours 3 papers	1.5 hours 3 papers
5:30		
6:00		

7:00	Ph.D Thesis contest presentation
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Day 2 (Nov 24, 2015 – Tuesday)

9:00
9:30
10:00
10:30
11:00

Keynote 3: Harry Chen (Mediatek, Taiwan)

Panel Discussion

2 - hours

Coffee break & Ph.D contest Poster session

11:30
12:00
12:30
1:00
1:30

<p>Session 4A</p> <p>2 hours -4 papers</p>	<p>Session 4B</p> <p>2 hours -4 papers</p>
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2:00 LUNCH

2:30
3:00
3:30
4:00
4:30
5:00
5:30
6:00
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11:00

SOCIAL PROGRAM

Day 3 (Nov 25, 2015 – Wednesday)

9:00	Keynote 4: Masahiro Fujita (Tokyo University)	
9:30	1-hour session	
10:00		
10:30	Session 5A	Session 5B
11:00	2 hours - 4 papers	2 hours - 4 papers
11:30		
12:00		
12:30		
1:00	LUNCH	
1:30	Session 6A	Session 6B
2:00	2 hours - 4 papers	2 hours - 4 papers
2:30		
3:00		
3:30	CLOSING SESSION	
4:00		
4:30	Inauguration of WRTL	
5:00	PLENARY Session	
5:30		
6:00		

Total: 4 Keynotes

5 Industry talks

38 papers + 1 Panel Session