

Harsh Shukla

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Research Interest	Optimization, Optimal control, Numerical methods for PDE, Computational control.	
Current Position	I am working on a research project with Prof. Dr. Debraj Chakraborty, Control and Computing group, IIT Bombay (Mumbai). Designation: Project Engineer Topic: Control and Optimization of Zinc Oxide (ZnO) Plasma for PLD (pulsed laser deposition process)	
Educational Qualification	8.01 CGPA, B. Tech, Electrical Engineering [2006-'10] From S. V. National Institute of Technology, Surat, Gujarat, India. 70.80% Aggregate in High School Certificate [in March 2006] From Gujarat Board, India. 89.43% Aggregate in Secondary School Certificate [in March 2004] From Gujarat Board, India.	
Technical Skills	Software	Matlab, d-SPACE, Keil μ vision, Etap
	Programming	C, C++, Assembly, VEE Pro (Graphical Programming)
		8085 microprocessor, 8051 micro controller and its family
	Operating System	Linux and Windows
Internship and Training	<ol style="list-style-type: none"> 1. Internship (With stipend) at Heinz Nixdorf Institute, University of Paderborn, Germany for 3 months (June-August 2009) 2. Industrial Training in Reliance Industries Limited (Previously I.P.C.L.) for 28 Days during (June-July 2008) 	
Academic Achievements	<ol style="list-style-type: none"> 1. Third Prize winner in National level Paper Presentation in Mind bend 2009 Technical Symposium of SVNIT, Surat.(April 2009) 2. Selected from India in DAAD-IAESTE Internship Program (2009) to go to Heinz Nixdorf Research Institute, Germany. 3. Science Project was selected for state level project presentation in 2004 school level science fair organized by Government of Gujarat. 4. "Ganit Darshan" (Mathematical magazine) award for 100/100 in Mathematics in S.S.C. 5. Merit Scholarship by N.C.C. cadet welfare society. 6. State Education Minister scholarship on the basis of performance in 10th Board. 7. 800/800 in GRE Quantitative section. 	
Projects-Internship	<ol style="list-style-type: none"> 1. Admittance Measurement of Piezoelectric transducer. 2. Build a System that is capable of finding object in its surrounding. 3. To upload calculated arbitrary waveform into wave form generator using Vee Pro Graphical Programming. <p>Place : Mechatronic and Dynamic, Heinz Nixdorf Institute, University of Paderborn, Germany.</p>	

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Curriculum-Project	<ol style="list-style-type: none">1. Automation in Railway crossing and train tracking system: (8th Semester).2. Boosting and maintaining constant dc voltage output in RLE load: (7th Semester)3. Railway signal control at mega station using micro-controller: (6th Semester)4. Railway line signal control using flip flop: (5th Semester)	
Organizational Experience and Co-curricular achievements	<ol style="list-style-type: none">1. Working Committee Head of CHRD (Centre for Human Resource and Development) in college2. Captain of Champion team at School level cricket Championship.3. NCC (National Cadet Corps) "A" certificate holder with rank Cadet Sergeant. Also got "Best Cadet award".4. Playing "Tabla" (Indian classical percussion instrument) from last 7 YEARS. Degree holder of "Praveshika Purna"5. Event coordinator in Sparsh '08 Cultural Festival of NIT Surat.	
Personal Profile	Name	Harsh
	Family Name	Shukla
	Date of Birth	07-September-1988
	Languages	English, Hindi, Gujarati
	Nationality	Indian
	Hobbies	Travelling to New Places, Playing Sports (Squash, Table tennis and Cricket), Listening Music, Playing Tabla.

Declaration:

I hereby declare that the above-mentioned information is correct to the best of my knowledge and I bear full responsibility for the correctness of the above-mentioned particulars.

Place: Mumbai.

(Harsh Shukla)