

## Selection process for M.Tech. RA May 2026

### Wadhvani Electronics Lab (WEL), Communications, Controls and DSP Laboratories

The selection process for M.Tech. RA applications to Wadhvani Electronics Lab (WEL) and other related labs (Communication, Controls and DSP Labs) will be conducted in two stages:

- 1. Shortlisting based on performance in written test:** Written test duration will be 1.5 to 2 hours and syllabus will be based on design and debugging of analog and digital circuits and embedded systems, analytical and logical reasoning.
- 2. Skill test of shortlisted candidates:** Based on written test performance, approximately top 100 candidates will be shortlisted for skill test of 2 to 2.5 hours duration. The skill test will be conducted in WEL lab to evaluate engineering design and relevant laboratory skills (breadboarding circuits; using oscilloscope, function generator, multi-meter to debug circuits; reporting of experimental data and drawing inferences etc.)

Final selection will be based on performance in skill test. The schedule and further details will be announced on the online notice board.