



## thyristor\_g.ece

### Attributes

```

mainnodes: anode cathode
outvar: i1=brc_of_th0 v1=brv_of_th0
main_var: gate
rparms: ron=0.1 roff=10M g_high=1.0

```

### Description

thyristor\_g.ece is an idealized thyristor which behaves like a resistance  $R_{on}$  when it is on and  $R_{off}$  when it is off. The value of the general variable **gate** is used to determine if the thyristor is on or off.

If  $gate > g\_high/2$ , the thyristor is triggered.

The branch voltage **v1** and the branch current **i1** are made available as output variables.

AC behaviour is not implemented.