



thyristor_bridge_1.ece

Attributes

mainnodes: plus minus a b c

outvar:

+ i1=brc_of_th1 v1=brv_of_th1

+ i2=brc_of_th2 v2=brv_of_th2

+ i3=brc_of_th3 v3=brv_of_th3

+ i4=brc_of_th4 v4=brv_of_th4

+ i5=brc_of_th5 v5=brv_of_th5

+ i6=brc_of_th6 v6=brv_of_th6

main_var: g1 g2 g3 g4 g5 g6

rparms: ron=0.1 roff=10M g_high=1

Description

`thyristor_bridge_1.ece` is a thyristor bridge as shown in the figure. The thyristors T1 to T6 are of type `thyristor_g` and present a resistance R_{on} when on and R_{off} when off. They are triggered by general variables `g1` to `g6`, respectively. As an example, if `g1 > g_high/2`, then thyristor T1 is triggered.

The branch voltages (`v1` to `v6`) and the branch currents (`i1` to `i6`) are made available as output variables.

AC behaviour is not implemented.