

inv_clock_6_1.gce

Attributes

```
mainvars:  
+   a b c ref  
+   g1 g2 g3 g4 g5 g6  
rparms:  
+   t_delay=2.0u g_high=1 epsl=1.0e-6  
+   delt_min=0.02u delt_nrml=2.0u
```

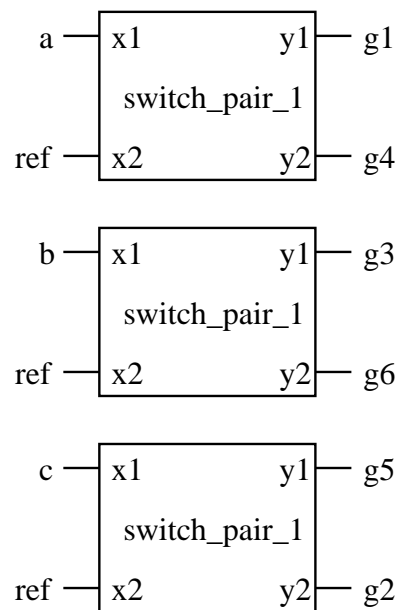


Figure 1: Implementation of inv_clock_6_1.gce

Description

inv_clock_6_1.gce is used to produce clock pulses for an inverter block as shown in the figure. The real parameters `t_delay`, `g_high`, `epsl`, `delt_min`, and `delt_nrml` are passed on to `switch_pair_1.gce`.

AC behaviour is not implemented.