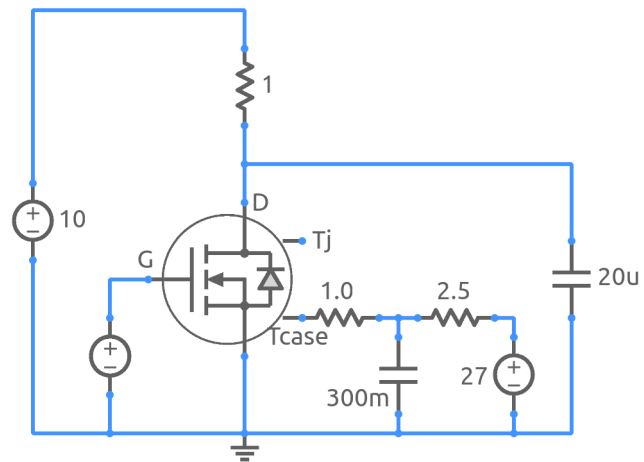


test\_coolmos\_3a.sqproj



SSW simulation of level-3 CoolMOS transistor with square wave gate input