ee101_jk_2.sqproj
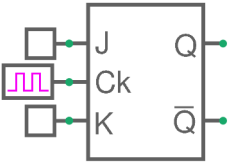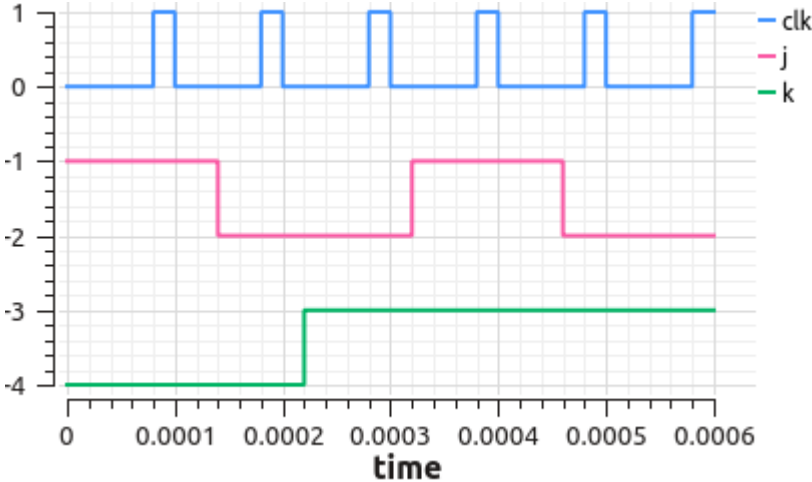

Note: positive edge-triggered flip-flop

The purpose of this simulation exercise is to understand the response of a positive edge-triggered JK flip-flop to some given input J and K waveforms as shown in the figure.

(Note that the y-axis values vary between 0 and 1 in reality; for convenience, an offset has been used for the waveforms in the above plot.)

Exercise Set

1. For the given J and K waveforms, plot Q versus time.

2. Compare your plot with simulation results.