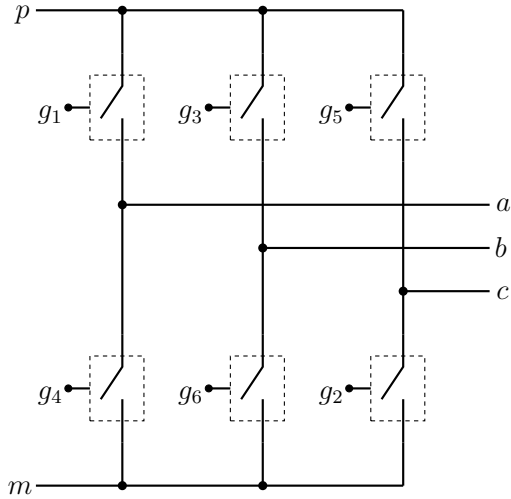


csi3_1.gme



Attributes

```
mainnodes_anlg: a b c p m
main_var: g1 g2 g3 g4 g5 g6
rparms:
+ r_on=1m
+ r_off=100k
+ g_high=1.0
+ cap=0.2n
outvar_anlg:
+ is1=i1_of_s1
+ is2=i1_of_s2
+ is3=i1_of_s3
+ is4=i1_of_s4
+ is5=i1_of_s5
+ is6=i1_of_s6
```

Description

csi3_1.gme is a current source inverter as shown in the figure. $R_{\text{on}}/R_{\text{off}}$ -type switches are used in the model. The gate signals, g1 to g6, are externally supplied. The switch resistance is r_on if the corresponding gate input is greater than g_high/2; else, it is r_off.

A capacitance (given by cap) is added between a and p, between b and p, and between c and p. It may help convergence of the Newton-Raphson process in some cases.

AC behaviour is not implemented.