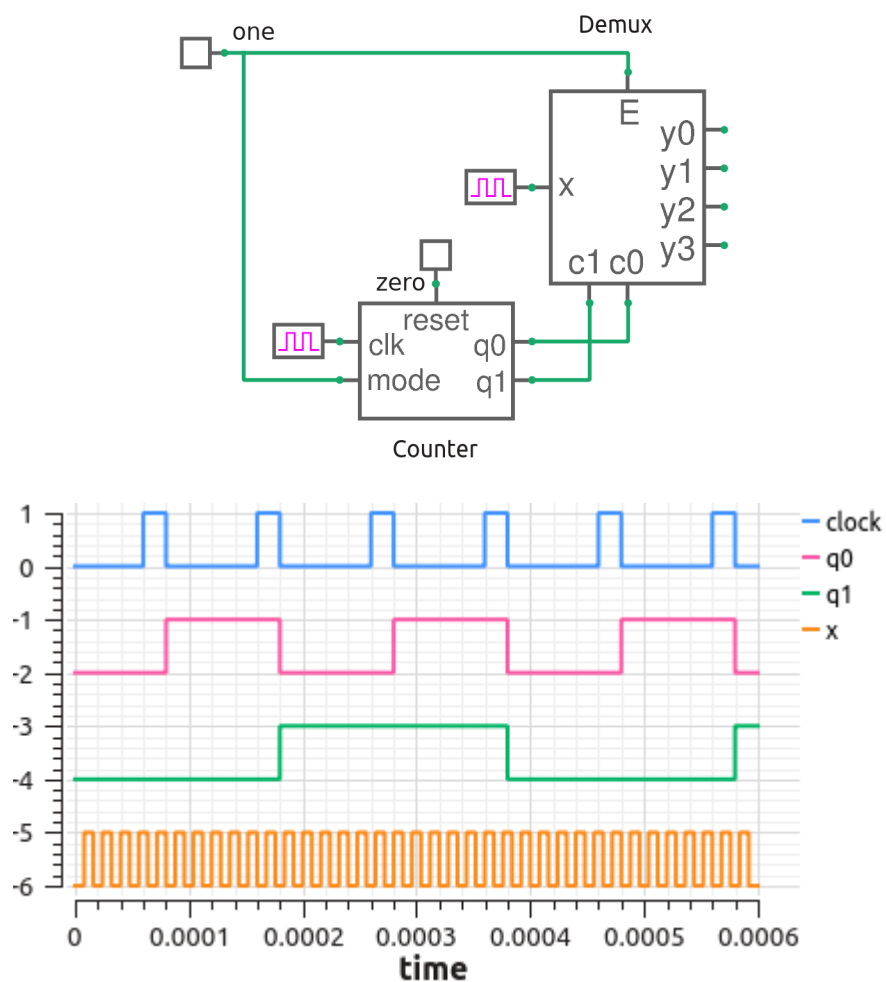


demux_test_1.sqproj



The purpose of this simulation exercise is to understand the operation of a 1-to-4 demultiplexer. A counter is used to change the Select bits in the sequence 00, 01, 10, 11, 00, ... (see figure). The input to the demultiplexer is a square wave.

(Note that the y -axis values vary between 0 and 1 in reality; for convenience, an offset has been used for the waveforms in the above plot.)

Exercise Set

1. Plot (on paper) the expected outputs y_0 , y_1 , y_2 , y_3 versus time.
2. Compare your plot with simulation results.