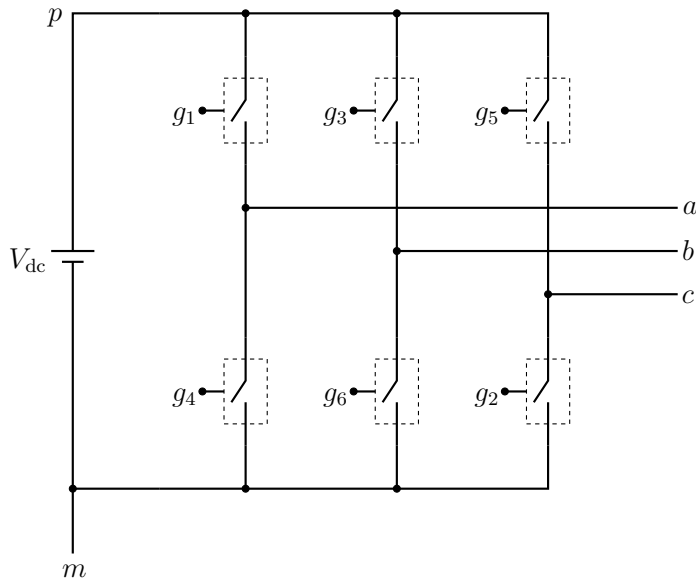


vsi3_ideal_1.gme



Attributes

```
mainnodes_anlg: a b c m
main_var: g1 g2 g3 g4 g5 g6
rparms:
+ vdc=100
+ g_high=1
```

Description

vsi3_ideal_1.gme is a voltage source inverter with ideal switches. The gate signals, g1 to g6, are externally supplied. A switch is considered to be on (zero resistance) if the corresponding gate signal is greater than g_high/2; else, it is considered to be off (infinite resistance).

Only the nodes a, b, c, and m are made available to the user.

AC behaviour is not implemented.