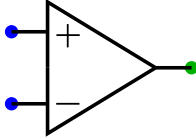


`cmptr1.dce`



Attributes

```
mainnodes: d_out
analog_nodes: vplus vminus
rparams: dlylh=10n dlyhl=10n
```

Description

`cmptr1.dce` compares voltages at analog (electrical) nodes `vplus` and `vminus`, and makes the digital output `d_out` equal to 1 if `vplus > vminus`; else 0.

The real parameters, `dlyhl` and `dlylh`, are the delays associated with high-to-low and low-to-high transitions at the output, respectively.