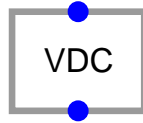


vsrcdc_box.ece



Attributes

```
mainnodes: p n
outvar:
+ i1=cur(n)_of_v0
+ v1=v1_of_v0
+ pwr_elec=pwr_elec_of_ece
rparms:
+ vdc=0.0
+ k_scale=1
outvar_ac: i1ac=cur(p)_of_v0
```

Description

`vsrcdc_box.ece` is a DC voltage source of magnitude `vdc` connected between nodes `p` and `n`. The output variables `i1` and `v1` are the branch current (from `n` to `p`) and branch voltage, respectively. The power absorbed by the element is made available as the output variable `pwr_elec`.

`vsrcdc_box.ece` is functionally the same as `vsrcdc.ece` but has a different symbol. Since it looks like a box, it could be placed in a corner (with its nodes named appropriately) without making the circuit schematic look awkward.

In AC analysis, this element is replaced by a short circuit. The current is made available as the output variable `i1ac`.

The real parameter `k_scale` is used to scale `vdc` by a constant.