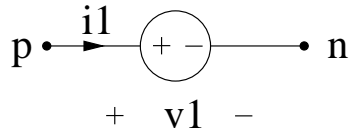


vsrce\_pwl20.ece



### Attributes

```
mainnodes: p n
outvar:
+ i1=cur(n)_of_v0
+ v1=v1_of_v0
iparms: n1=2
rparms:
+ t1=1 v1=1 t2=2 v2=2
+ t3=3 v3=3 t4=4 v4=4
+ t5=5 v5=5 t6=6 v6=6
+ t7=7 v7=7 t8=8 v8=8
+ t9=9 v9=9 t10=10 v10=10
+ t11=11 v11=11 t12=12 v12=12
+ t13=13 v13=13 t14=14 v14=14
+ t15=15 v15=15 t16=16 v16=16
+ t17=17 v17=17 t18=18 v18=18
+ t19=19 v19=19 t20=20 v20=20
```

### Description

vsrce\_pwl20.ece is a voltage source with a piecewise linear current waveform with up to 20 “break points”. The parameters have the following meaning:

**n1:** Number of break points.

**t1,t2, etc.:** Time of break point 1, 2, etc.

**is1,is2, etc.:** Voltage value of at the corresponding break point. For example, **v2=3.5** means that the current at the 2<sup>nd</sup> break point is 3.5. The source voltage is made constant (equal to **v1**) before **t1**. Also, it is made constant after the **n1<sup>th</sup>** break point.

The branch current (from **n** to **p**) and the branch voltage are made available as output variables **i1** and **v\_branch**, respectively.

AC behaviour is not implemented.