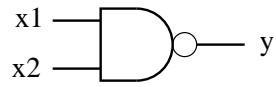


nand_2.xce



Attributes

```
main_vars: x1 x2 y  
rparms: x_high=1
```

Description

nand_2.xce is a NAND gate implemented with x-variables. The parameter **x_high** specifies the high logic level, the low logic level being zero.

AC behaviour is not implemented.