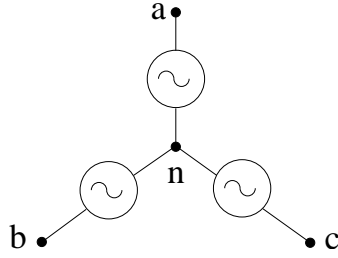


rt_vsrcac3_tbl.ece



Attributes

```
mainnodes: a b c n
iparms:
+   tbl_index_1=1
+   tbl_index_2=2
+   tbl_index_3=3
rparms:
+   v_a=1
+   v_b=1
+   v_c=1
+   phi_a=1
+   phi_b=1
+   phi_c=1
+   f_hz=1
+   rt_delt1=50u
```

Description

rt_vsrcac3_tbl.ece is a 3-phase AC voltage source connected as shown in the figure. The real parameters, v_a, v_b, v_c, phi_a, phi_b, phi_c, f_hz, and t0 represent \hat{V}_a , \hat{V}_b , \hat{V}_c , ϕ_a , ϕ_b , ϕ_c , f , and t_0 , respectively, in the following equations for the voltages:

$$V_{an}(t) = \hat{V}_a \sin(2\pi f(t - t_0) + \phi_a),$$

$$V_{bn}(t) = \hat{V}_b \sin(2\pi f(t - t_0) + \phi_b),$$

$$V_{cn}(t) = \hat{V}_c \sin(2\pi f(t - t_0) + \phi_c) .$$

(1)

The real parameter rt_delt1 should be set to the time step used in the circuit file.

The integer parameters tbl_index_1 etc. are used internally as indices for storing the tables of values for this source for one period.