

`vsrc_table_1.gce`

Attributes

`mainnodes_anlg`: p n
`sparms`: `file_name=none`

Description

`vsrc_table_1.gme` is a piecewise linear voltage source connected between nodes **p** and **n**. The voltage values $v_{pn}(t)$ are taken from a file. `file_name` is the name of the file (assumed to be present in the **Output** directory) with data in the following format:

```
t1  x1
t2  x2
t3  x3
t4  x4
...
```

The t values are expected to be in ascending order. If the time lies between two \mathbf{t} values in the file (say, t_k and t_{k+1}), the voltage value is obtained by interpolation.

AC behaviour is not implemented.