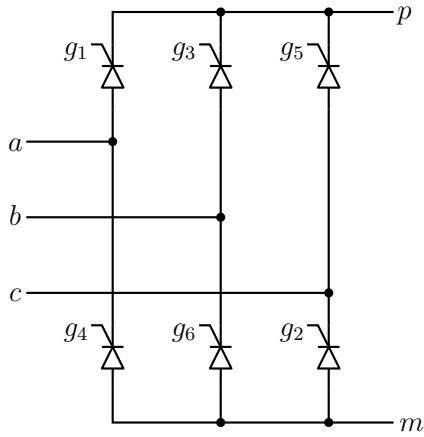


thyristor_bridge_3ph_1.gme



Attributes

```
mainnodes_anlg: a b c p m
main_var: g1 g2 g3 g4 g5 g6
rparms:
+ r_on=1m
+ r_off=100k
+ g_high=1.0
+ cap=0.2n
outvar_anlg:
+ i_T1=i1_of_t1
+ i_T2=i1_of_t2
+ i_T3=i1_of_t3
+ i_T4=i1_of_t4
+ i_T5=i1_of_t5
+ i_T6=i1_of_t6
```

Description

thyristor_bridge_3ph_1.gme is a three-phase thyristor bridge as shown in the figure.

R_{on}/R_{off} -type thyristors are used in the model. The gate signals, $g1$ to $g6$, are externally supplied. If a gate input is greater than $g_high/2$, it is considered to be high.

A capacitance (given by cap) is added between a and m , between b and m , and between c and m . It may help convergence of the Newton-Raphson process in some cases.

AC behaviour is not implemented.