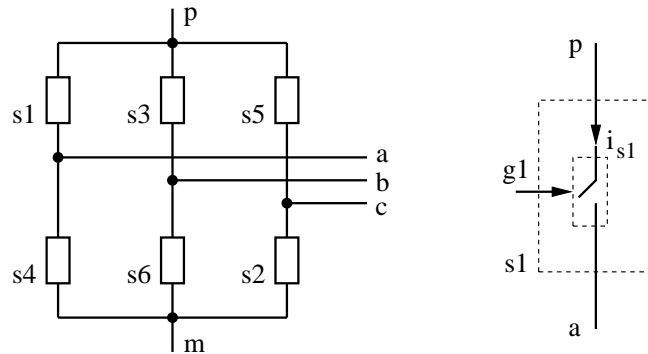


## rt\_invrtr\_1.ece



### Attributes

```
mainnodes: a b c p m
main_var: g1 g2 g3 g4 g5 g6
rparams: ron=1m roff=1M g_high=1.0
```

### Description

rt\_invrtr\_1.ece is an inverter circuit (see figure) in which the switches are  $R_{on}/R_{off}$ -type, **ron** and **roff** being the on- and off-state resistances, respectively. Switch **s1** is controlled by gate signal **g1**, and so on. If **g1** is greater than **g\_high/2**, **s1** is closed; otherwise, it is open.