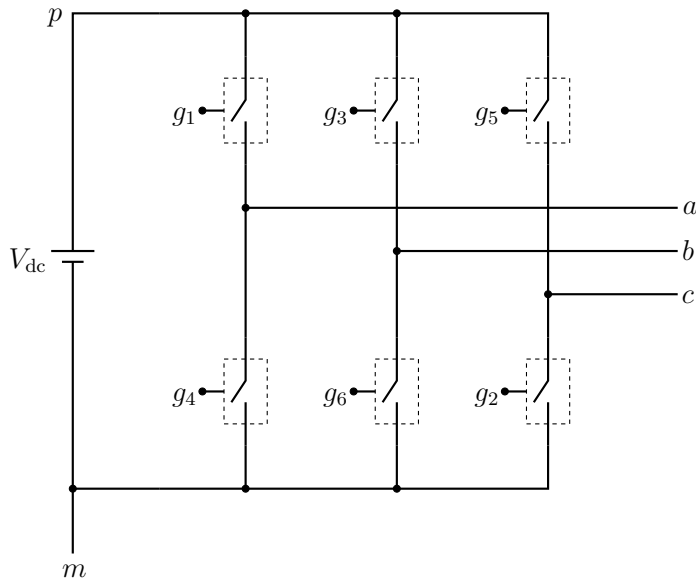


## vsi3\_ideal\_3.gme



### Attributes

```
mainnodes_anlg: a b c m
main_var: g1
aux_var: g2 g3 g4 g5 g6
iparms:
+ flag_frequency=1
+ flag_period=0
rparms:
+ g_high=1.0
+ t_period=20m
+ frequency=50
+ vdc=100
outvar_anlg:
+ g1=var_of_g1
+ g2=var_of_g2
+ g3=var_of_g3
+ g4=var_of_g4
+ g5=var_of_g5
+ g6=var_of_g6
```

### Description

vsi3\_ideal\_3.gme is a voltage source inverter with ideal switches. The gate signal **g1** is externally supplied, and the other gate signals (**g2** to **g6**) are internally generated. A switch is

considered to be on (zero resistance) if the corresponding gate signal is greater than `g_high/2`; else, it is considered to be off (infinite resistance).

The other parameters have the following meaning:

**flag\_frequency:** If this parameter is set to 1, the period of the gate signals is computed using the real parameter **frequency**.

**flag\_period:** If this parameter is set to 1, the period of the gate signals is given by the real parameter **t\_period**.

Only the nodes **a**, **b**, **c**, **m**, and **g1** are made available to the user. AC behaviour is not implemented.