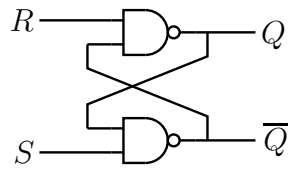


`rsff_nand.dce`



### Attributes

```
mainnodes: r s q qbar  
rparms: dly=5n
```

### Description

`rsff_nand.dce` is an RS flip-flop made with NAND gates, as shown in the figure, without direct set and reset inputs.

The real parameter `dly` is the gate delay for each of the NAND gates.