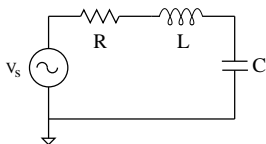


SEQUEL: Exercises

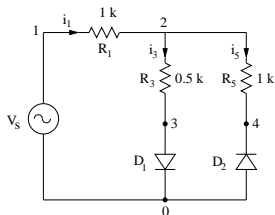
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Frequency Response of RLC circuit



Plot $|I|$ versus f (log-log) for $R = 1\text{ k}\Omega$, $L = 1\text{ mH}$, $C = 1\text{ }\mu\text{F}$.
Change R and see its effect on the frequency response.
Compare with theory.

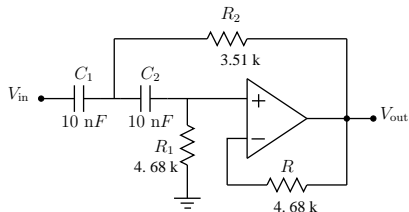
A diode circuit



Plot V_2 versus V_1 for $-5\text{ V} < V_s < 5\text{ V}$.

(copy/paste/edit diode_circuit_1.sqproj).

A second-order Op-Amp filter



Plot the magnitude of V_{out} as a function of frequency for a fixed input.
(Copy/paste/edit `low_pass_filter_1.sqproj`).

A modulo-6 counter

Design a modulo-6 binary counter with the following states:

0	0	0
0	0	1
0	1	0
0	1	1
1	0	0
1	0	1
0	0	0
-	-	-

(Copy/paste/edit `jkffcntr.sqproj`).