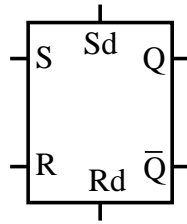


rsff1.dce



Attributes

```
mainnodes: r s sd rd q qbar
rparams: dlyhl=5n dlylh=5n
stparams: state_sv=0 q_sv=0
```

Description

rsff1.dce is an RS flip-flop. *r*, *s*, *q*, *qbar* are the *R*, *S*, *Q*, and \overline{Q} nodes of the flip-flop, respectively. *sd* and *rd* are the direct set and reset inputs, respectively.

The real parameters, *dlyhl* and *dlylh*, are the delays associated with high-to-low and low-to-high transitions at the output, respectively.

The start-up parameter *q_sv* decides the state of the flip-flop in start-up simulation.