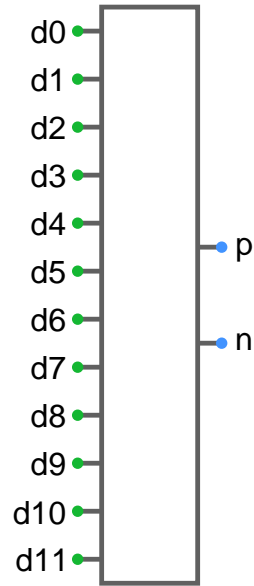


dac12.ece



Attributes

mainnodes: p n
digital_nodes: d0 d1 d2 d3 d4 d5 d6 d7 d8 d9 d10 d11
rparms: vref=5.0

Description

dac12.ece is an ideal 12-bit DAC. The output voltage – which appears between nodes **p** and **n** – is given by

$$V_{\text{out}} = \frac{V_{\text{ref}}}{2^{12}} \sum_{i=0}^{11} 2^i d_i. \quad (1)$$