

`not.dce`



Attributes

```
mainnodes: a abar
rparms: dlyhl=5n dlylh=5n
stparms: state_sv=0
```

Description

`not.dce` is an inverter, with input `a` and output `abar`. The real parameters, `dlyhl` and `dlylh`, are the delays associated with high-to-low and low-to-high transitions at the output, respectively.

The start-up parameter `state_sv` decides the output value in start-up simulation.