

## `jfet_spice_p.ece`

### Description

`jfet_spice_p.ece` is a *p*-type JFET model. The real parameters have the same meaning as the JFET model implemented in SPICE 3.

The gate and drain currents are made available as output variables `ig` and `id`, respectively.

The power absorbed by the element is made available as the output variable `pwr_elec`.

AC (small-signal) implementation of the model is also incorporated, following SPICE 3.