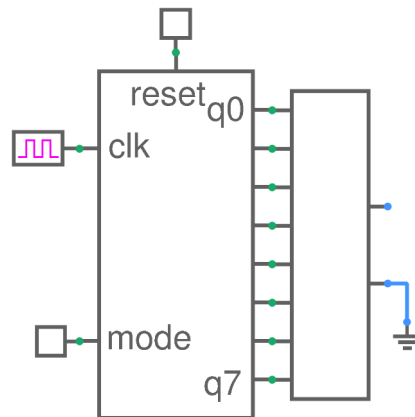


test_dac8.sqproj



Shown in the figure is an 8-bit DAC driven by a binary counter. As the counter output changes from 0000 0000 to 1111 1111, we expect the DAC output to change in discrete steps.

Exercise Set

1. With the given clock parameters and **vref** (the reference voltage for the DAC), work out the waveform for v_{out} . In particular, what is the resolution of the DAC?
2. Compare your waveform with simulation results.