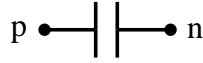


`c_pu_sync.ece`



Attributes

```
mainnodes: p n
outvar: i1=cur(p)_of_c1 v1=v1_of_c1
stparms: v0sv=0
iparms:
+   i_compute_strt=0
+   indx=1
rparms: x_pu=1 f_b=50
```

Description

`c_pu_sync.ece` is a capacitor connected between nodes `p` and `n`. The capacitance is specified as a per unit value (`x_pu`), with `f_b` as the base frequency. `v0sv` is used to specify the “start-up” value of the capacitor voltage in startup simulation. The output variables `i1` and `v1` are the branch current and branch voltage, respectively.

When the integer parameter `i_compute_strt` is specified as 1, the initial value of the capacitor voltage is internally computed in the synchronous machine template and is passed to `c_pu_sync.ece` through a Fortran common block. The integer parameter `indx` is used to specify whether the capacitor is in phase a, b, or c line.

AC behaviour is not implemented.