

switch_pair_0.gce

Attributes

```
mainvars: x1 x2 y1 y2
rparms:
+   g_high=1
+   epsl=1.0e-6
+   delt_min=1.0e-6
+   delt_nrml=1.0e-3
```

Description

`switch_pair_0.gce` is designed to generate signals required to drive a pair of switches. It compares general variables `x1` and `x2` and produces outputs `y1` and `y2`. The value of `y1` is `g_high` if `x1 > x2`; else, it is zero. For `y2`, the opposite is true; viz., `y2` is equal to `g_high` if `x1 < x2`; else, it is zero.

It should be noted that `switch_pair_0.gce` only produces the signals to drive a pair of switches. The switches themselves are *external* to `switch_pair_0.gce`.

The parameters `delt_min`, `delt_nrml`, and `epsl` are used for controlling the simulator time steps. Additional time points are forced, depending on the values of `delt_min` and `delt_nrml`, when `x1` and `x2` are within `epsl` of each other. This feature allows accurate simulation without having to make the average time step very small. Generally, `delt_nrml` should be made equal to the typical simulator time step (`delt_const`) while `delt_min` should be made much smaller (say, by a factor of 100).

AC behaviour is not implemented.