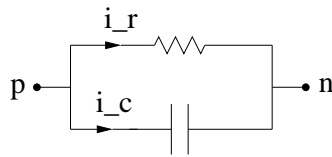


rc_parallel.ece



Attributes

```
mainnodes: p n
outvar:
+   i_r=cur(p)_of_r1
+   i_c=cur(p)_of_c1
  stparms: v0sv=0
  rparms: r=0.1 c=0.1u
outvar_ac:
+   i_r_ac=cur(p)_of_r1
+   i_c_ac=cur(p)_of_c1
```

Description

`rc_parallel.ece` is a parallel combination of a resistance `r` and a capacitance `c`. `v0sv` is used to specify the “start-up” value of the capacitor voltage in start-up simulation. The resistor and capacitor currents are made available as output variables `i_r` and `i_c` (`i_r_ac` and `i_c_ac` in AC).