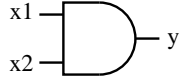


and\_2.gce



#### Attributes

```
mainvars: x1 x2 y  
rparms: g_high=1
```

#### Description

and\_2.gce is an AND gate implemented with general variables. The parameter `g_high` specifies the high logic level, the low logic level being zero.

AC behaviour is not implemented.