

BJT Amplifiers: Part 1



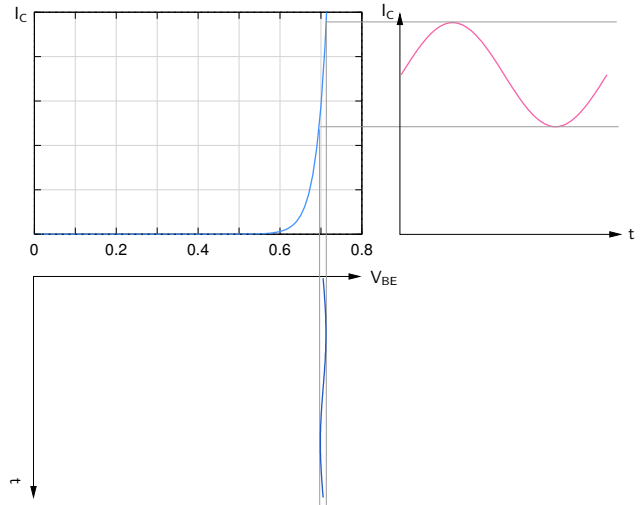
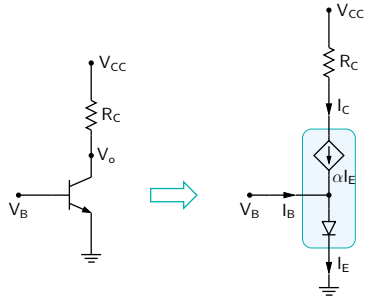
M. B. Patil

mbpatil@ee.iitb.ac.in

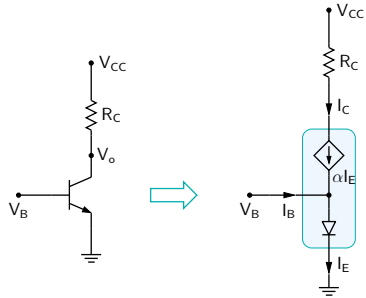
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Department of Electrical Engineering
Indian Institute of Technology Bombay

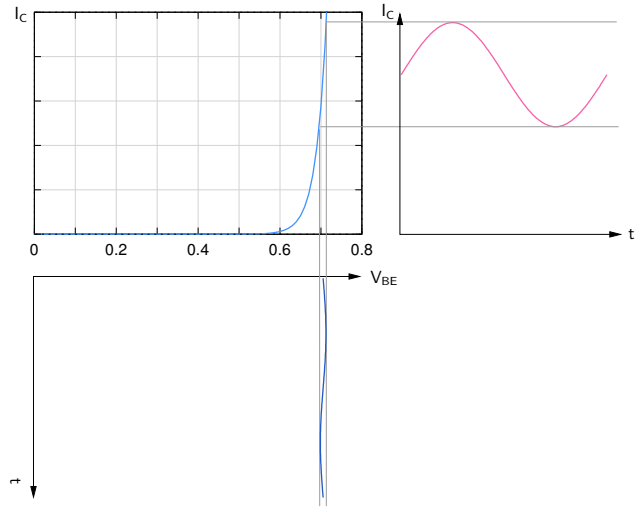
BJT amplifier: basic operation



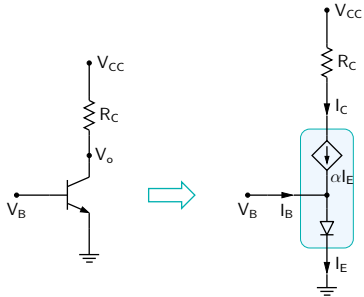
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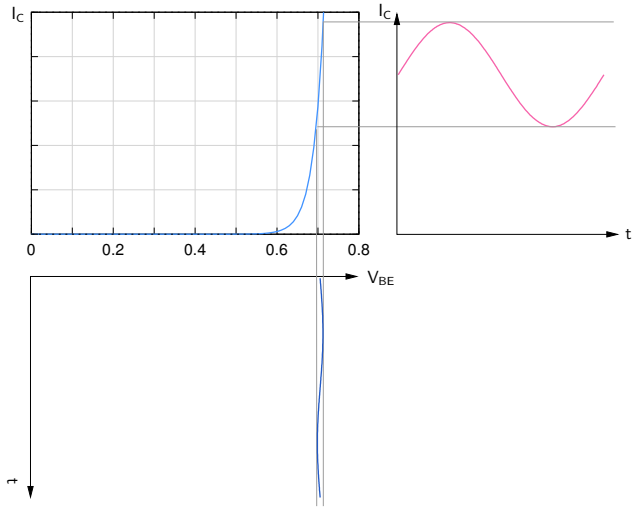
* In the active mode, I_C changes exponentially with V_{BE} : $I_C = \alpha_F I_{ES} [\exp(V_{BE}/V_T) - 1]$



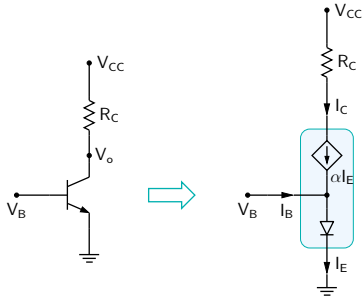
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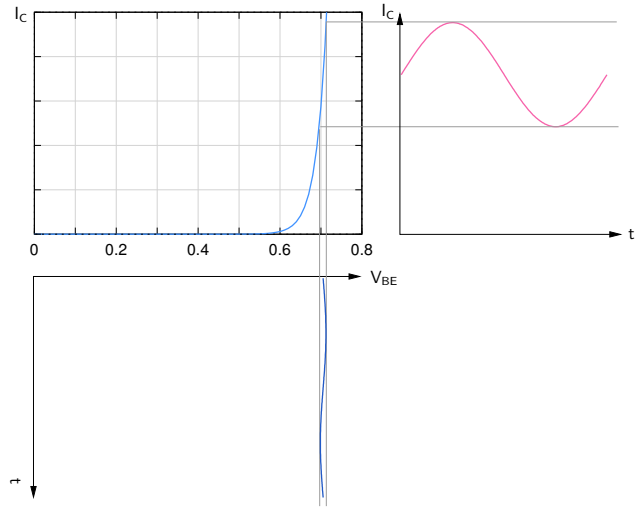
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- * $V_o(t) = V_{CC} - I_C(t) R_C$
 \Rightarrow the amplitude of V_o , i.e., $\widehat{I_C} R_C$, can be made much larger than $\widehat{V_B}$.



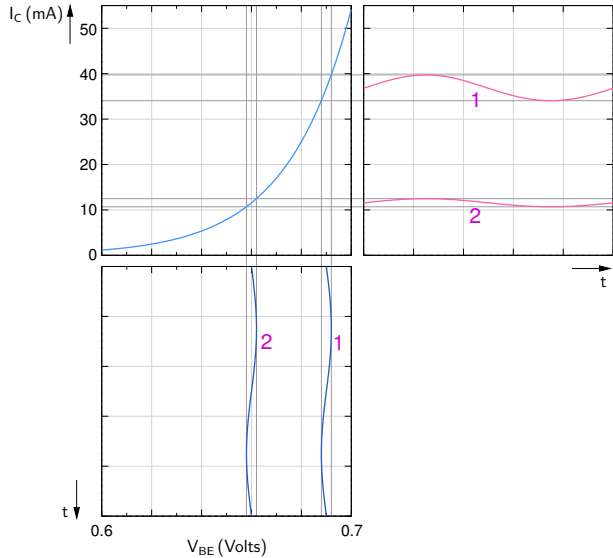
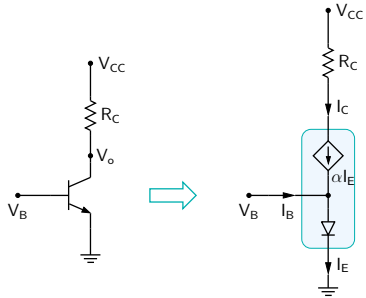
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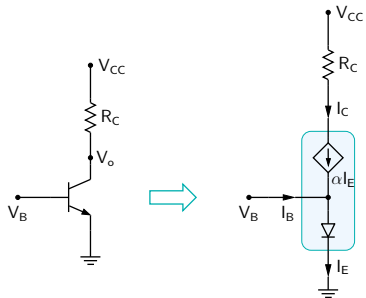
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 \Rightarrow the amplitude of V_o , i.e., $\hat{I}_C R_C$, can be made much larger than \hat{V}_B .
- * Note that both the input (V_{BE}) and output (V_o) voltages have DC ("bias") components.



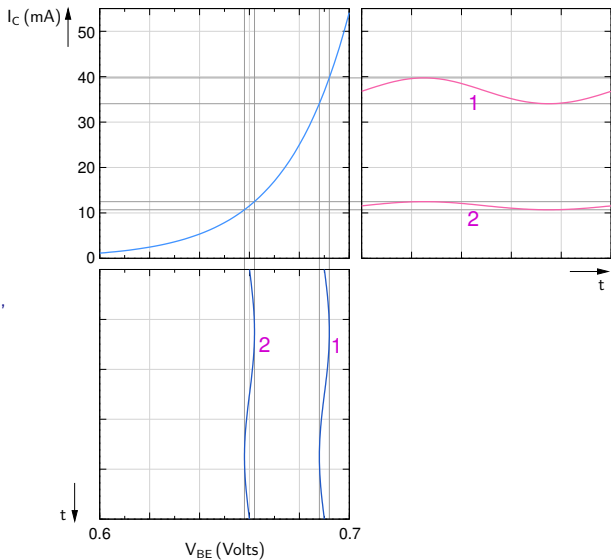
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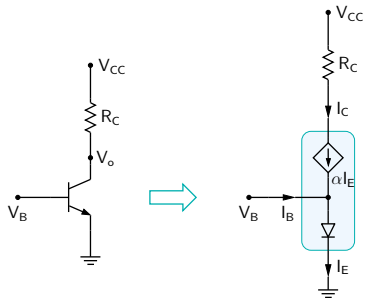
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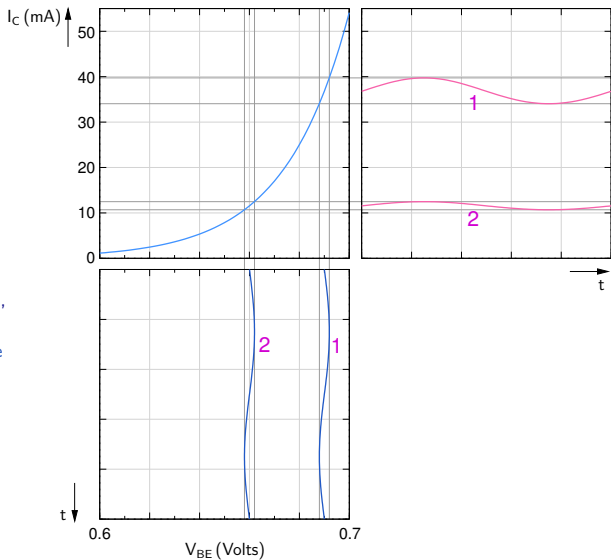
* The gain depends on the DC (bias) value of V_{BE} , the input voltage in this circuit.



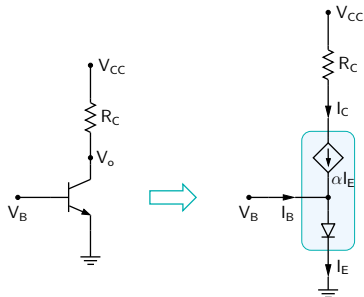
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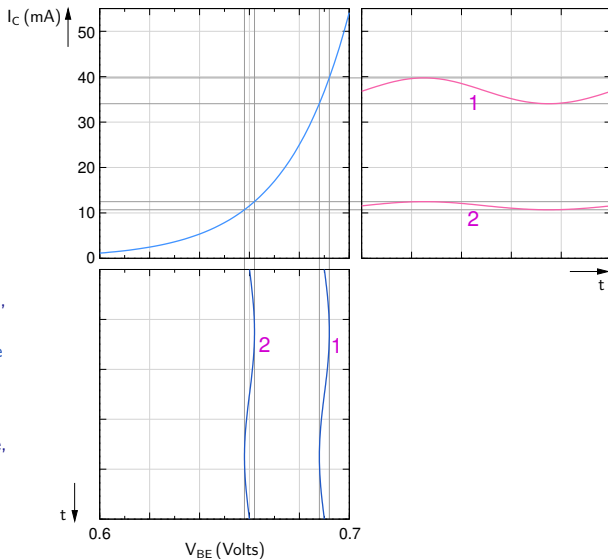
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- * In practice, it is not possible to set the bias value of the input voltage to the desired value (e.g., 0.673 V).



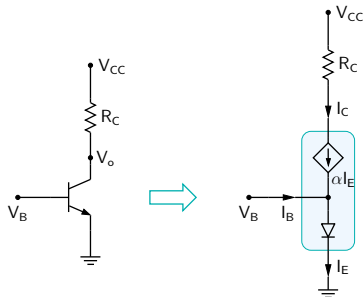
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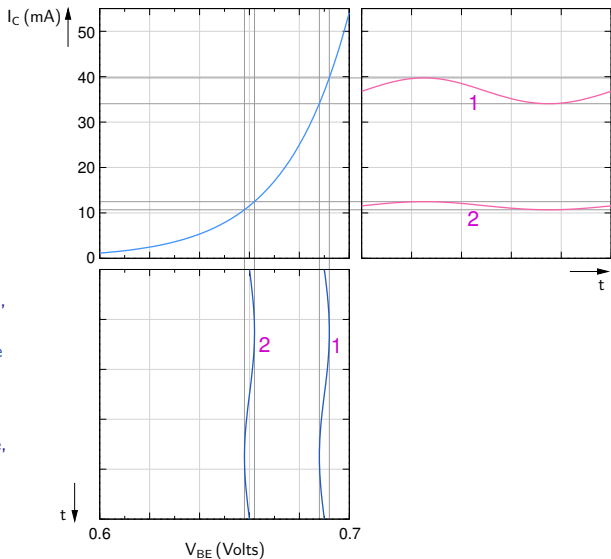
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- * Even if we could set the input bias as desired, device-to-device variation, change in temperature, etc. would cause the gain to change.
→ need a better biasing method.



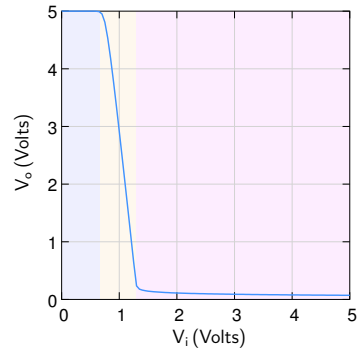
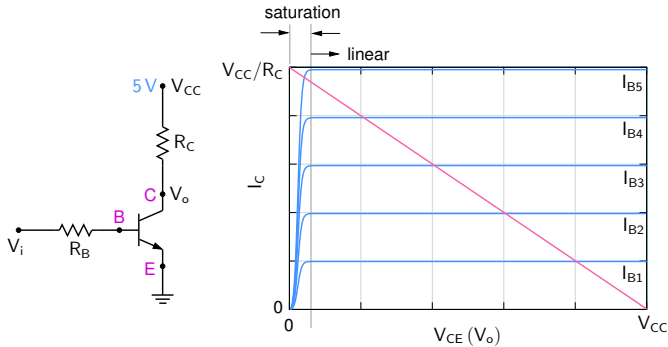
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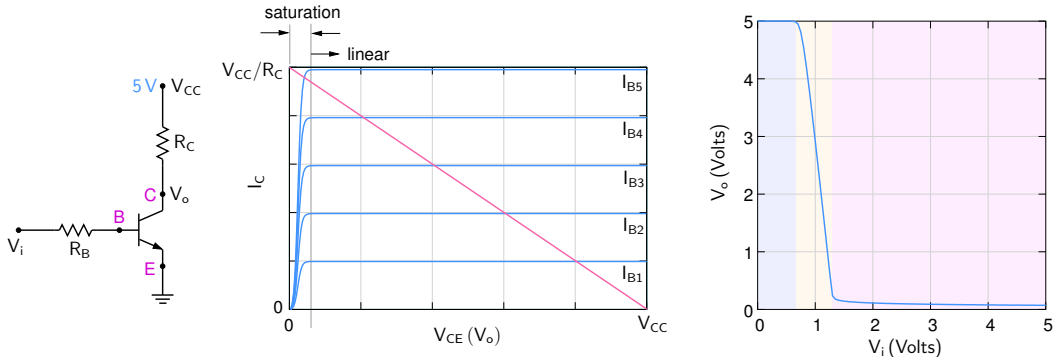
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- * Even if we could set the input bias as desired, device-to-device variation, change in temperature, etc. would cause the gain to change. → need a better biasing method.
- * Biasing the transistor at a specific V_{BE} is equivalent to biasing it at a specific I_C .



BJT amplifier biasing

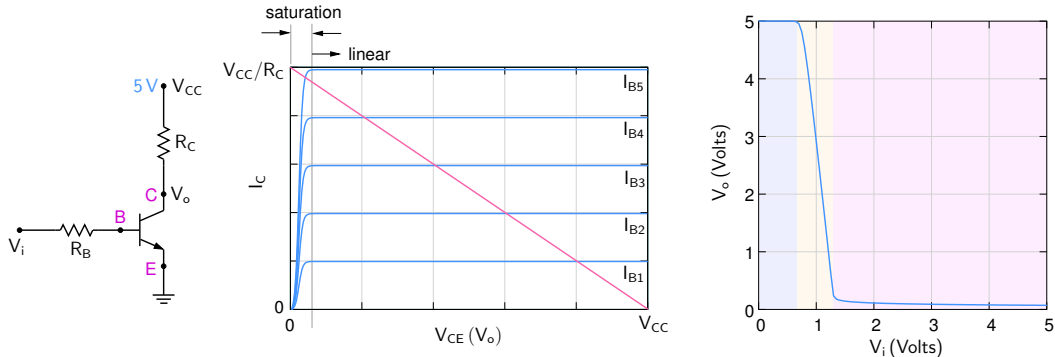


Consider a more realistic BJT amplifier circuit, with R_B added to limit the base current (and thus protect the transistor).



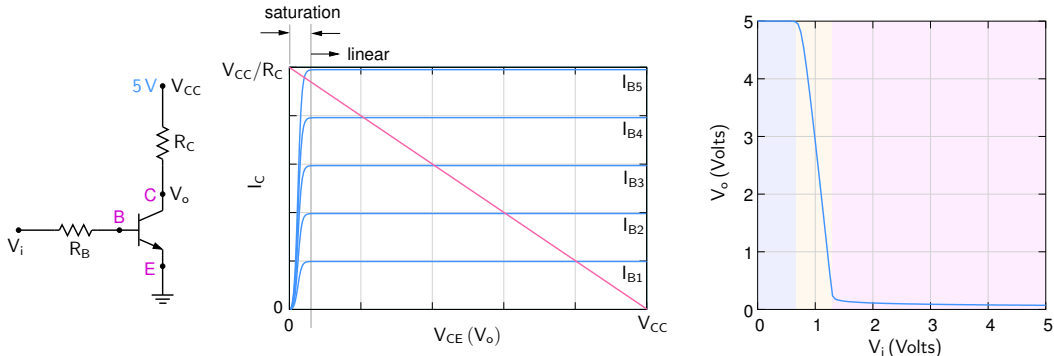
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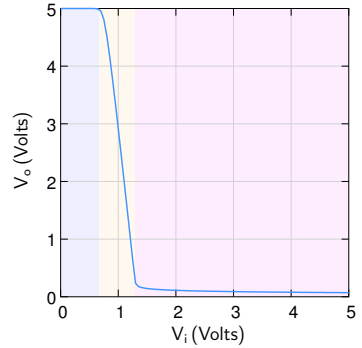
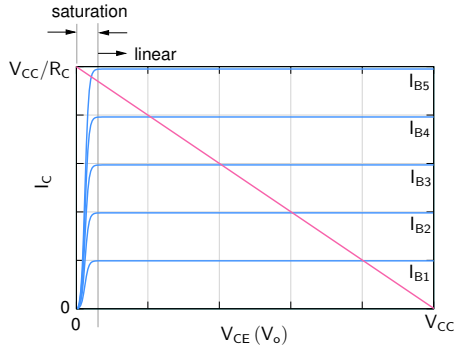
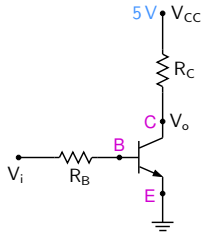
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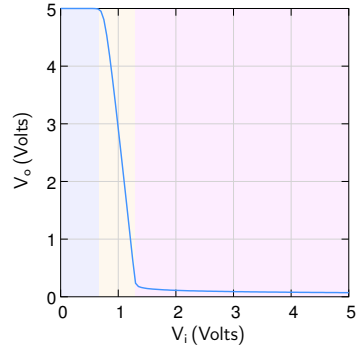
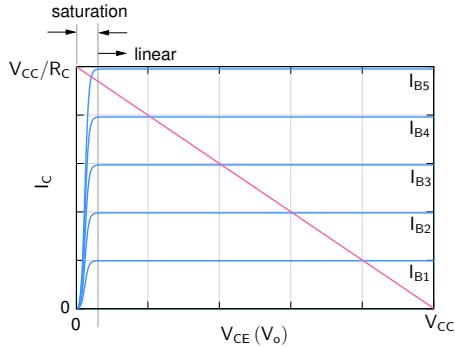
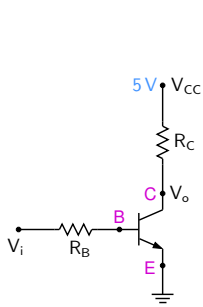
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- * As V_i is increased further, V_o reaches V_{CE}^{sat} (about 0.2V), and the BJT enters the saturation region (both B-E and B-C junctions are forward biased).

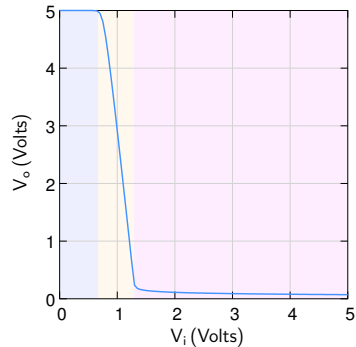
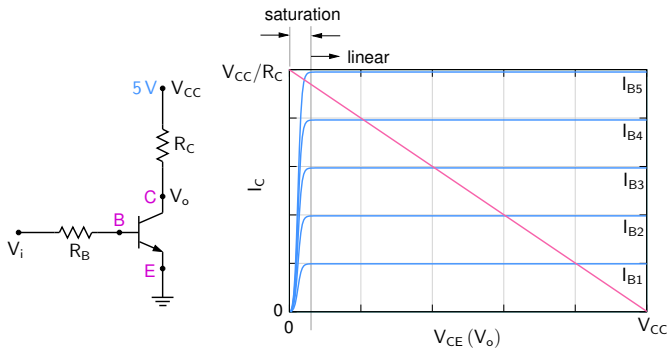
BJT amplifier biasing



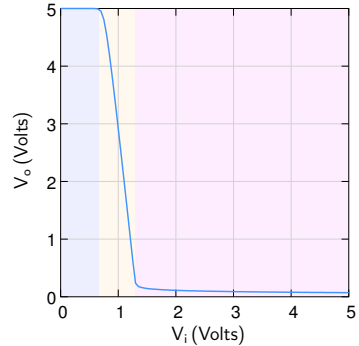
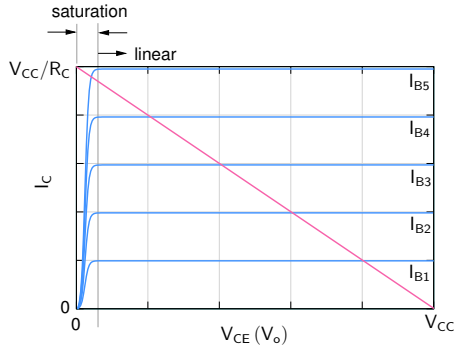
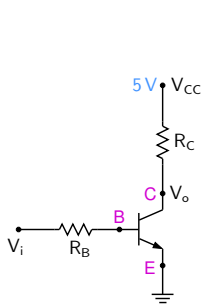
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* The gain of the amplifier is given by $\frac{dV_o}{dV_i}$.

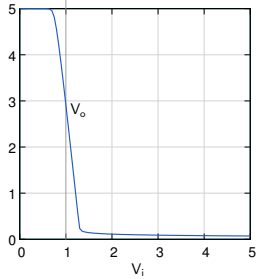
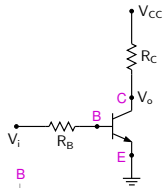


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- * Since V_o is nearly constant for $V_i < 0.7V$ (due to cut-off) and $V_i > 1.3V$ (due to saturation), the circuit will not work as an amplifier in this range.

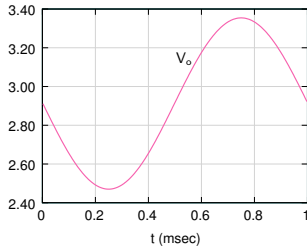
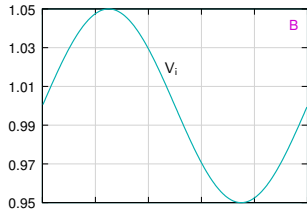
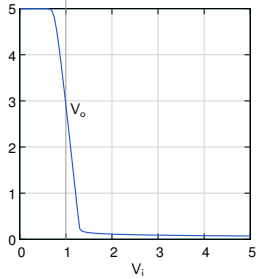
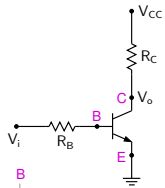


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- * Further, to get a large swing in V_o without distortion, the DC bias of V_i should be at the centre of the amplifying region, i.e., $V_i \approx 1 \text{ V}$.

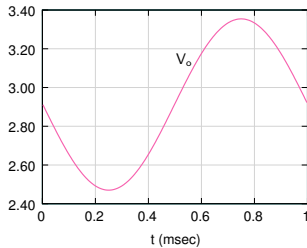
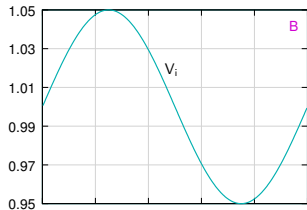
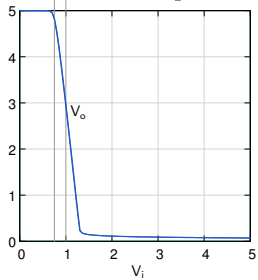
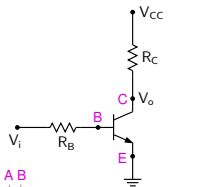
BJT amplifier biasing



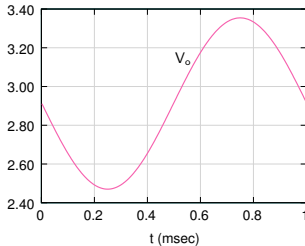
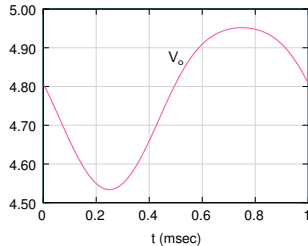
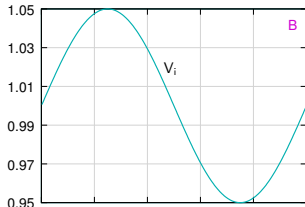
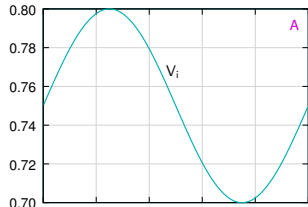
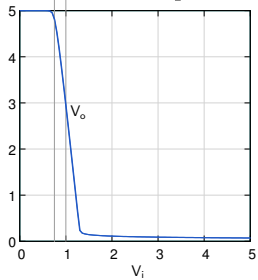
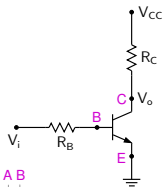
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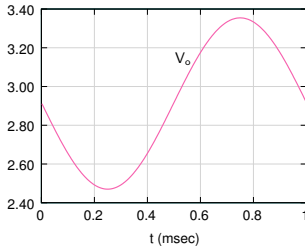
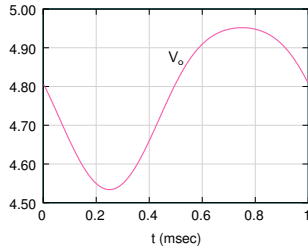
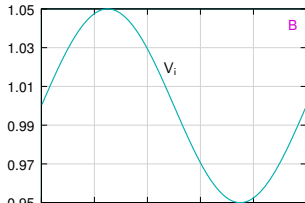
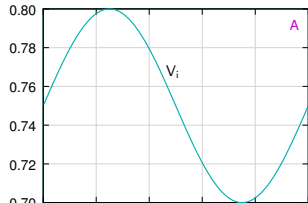
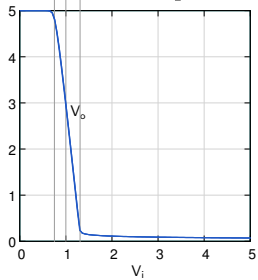
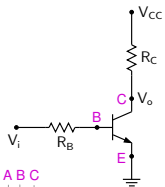
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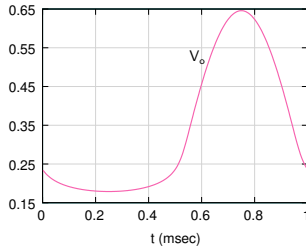
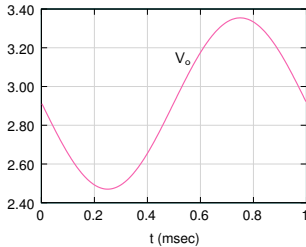
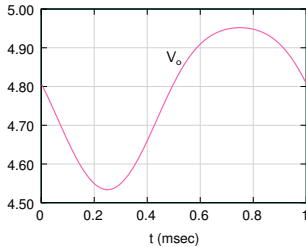
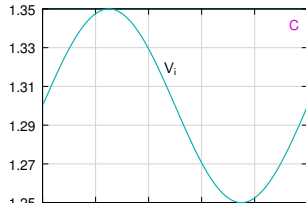
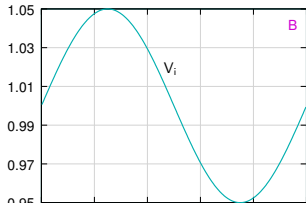
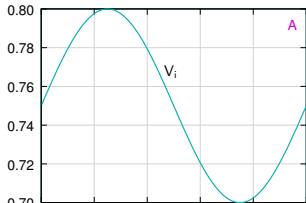
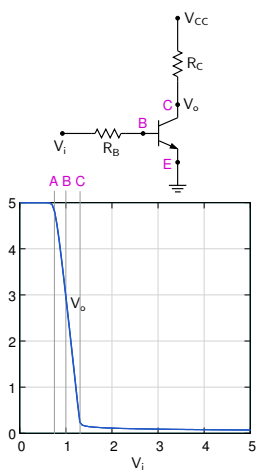
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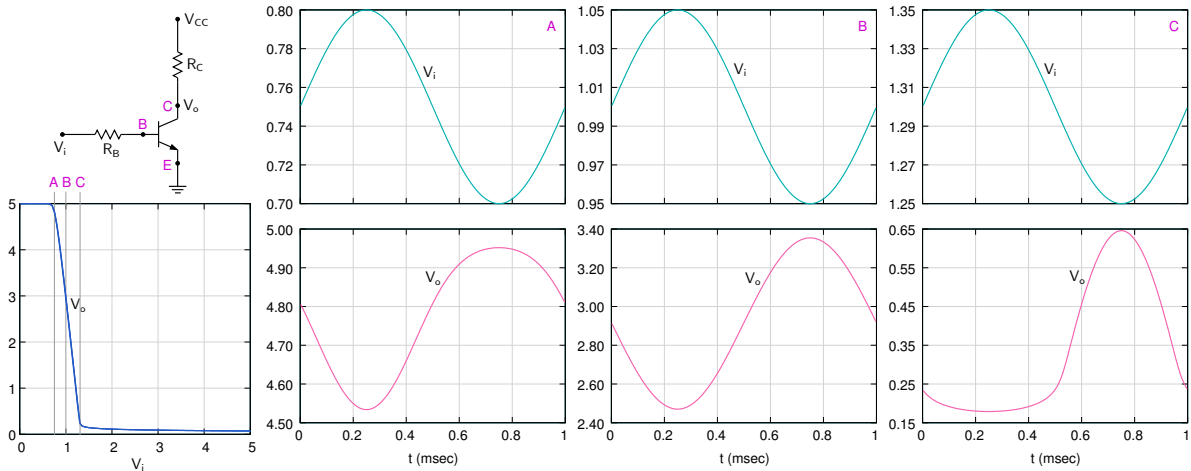
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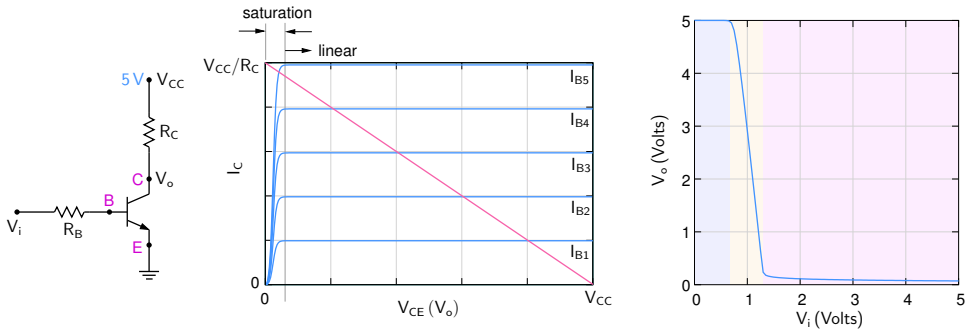


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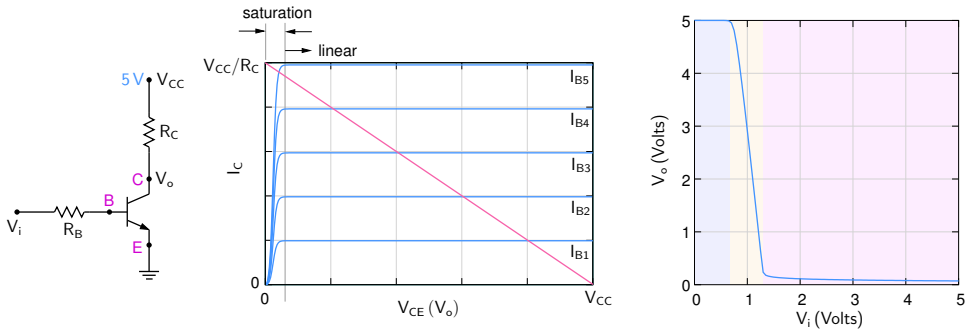
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BJT amplifier

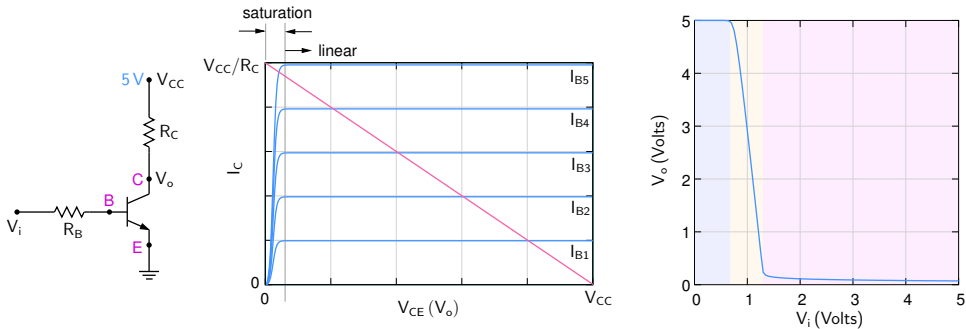


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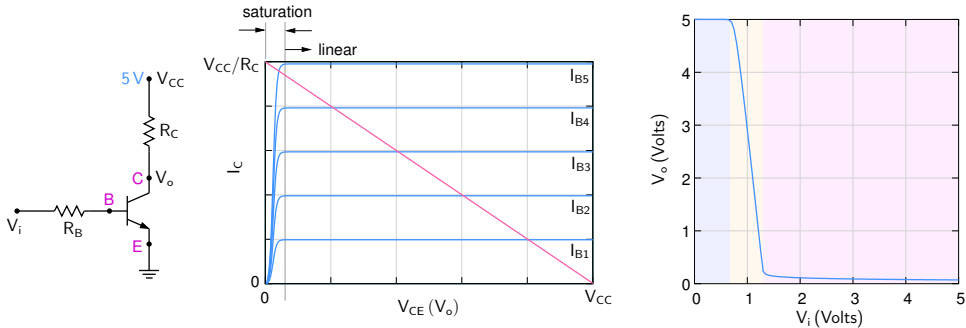
BJT amplifier



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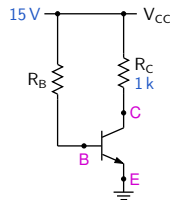


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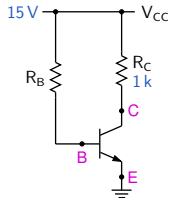
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 - adjusting the input DC bias to ensure that the BJT remains in the linear (active) region with a certain bias value of V_{BE} (or I_C).
 - mixing the input DC bias with the signal voltage.
- * The first issue is addressed by using a suitable biasing scheme, and the second by using "coupling" capacitors.

BJT amplifier: a simple biasing scheme



“Biasing” an amplifier \Rightarrow selection of component values for a certain DC value of I_C (or V_{BE}) (i.e., when no signal is applied).

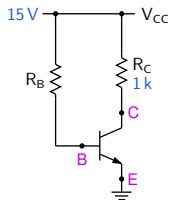
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Equivalently, we may bias an amplifier for a certain DC value of V_{CE} , since I_C and V_{CE} are related:
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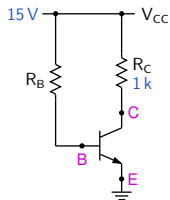


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As an example, for $R_C = 1\text{ k}$, $\beta = 100$, let us calculate R_B for $I_C = 3.3\text{ mA}$, assuming the BJT to be operating in the active mode.

BJT amplifier: a simple biasing scheme



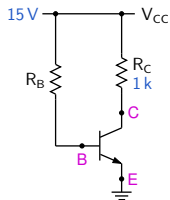
“Biasing” an amplifier \Rightarrow selection of component values for a certain DC value of I_C (or V_{BE}) (i.e., when no signal is applied).

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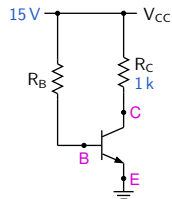
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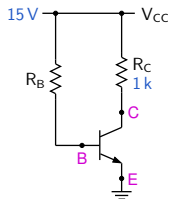
$$\rightarrow R_B = \frac{14.3\text{ V}}{33\text{ }\mu\text{A}} = 430\text{ k}\Omega.$$

BJT amplifier: a simple biasing scheme (continued)



With $R_B = 430\text{ k}$, we expect $I_C = 3.3\text{ mA}$, assuming $\beta = 100$.

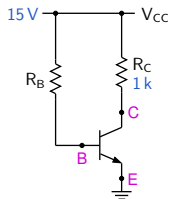
BJT amplifier: a simple biasing scheme (continued)



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However, in practice, there is a substantial variation in the β value (even for the same transistor type). The manufacturer may specify the nominal value of β as 100, but the actual value may be 150, for example.

BJT amplifier: a simple biasing scheme (continued)



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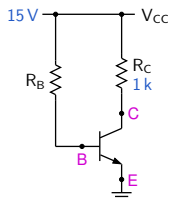
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With $\beta = 150$, the actual I_C is,

$$I_C = \beta \times \frac{V_{CC} - V_{BE}}{R_B} = 150 \times \frac{(15 - 0.7)\text{ V}}{430\text{ k}} = 5\text{ mA},$$

which is significantly different than the intended value, viz., 3.3 mA.

BJT amplifier: a simple biasing scheme (continued)



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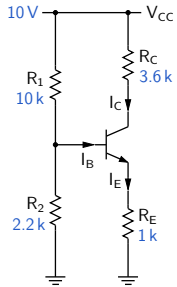
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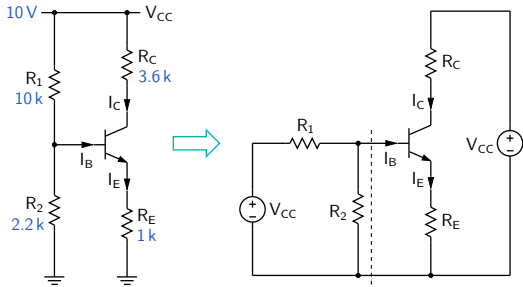
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→ need a biasing scheme which is not so sensitive to β .

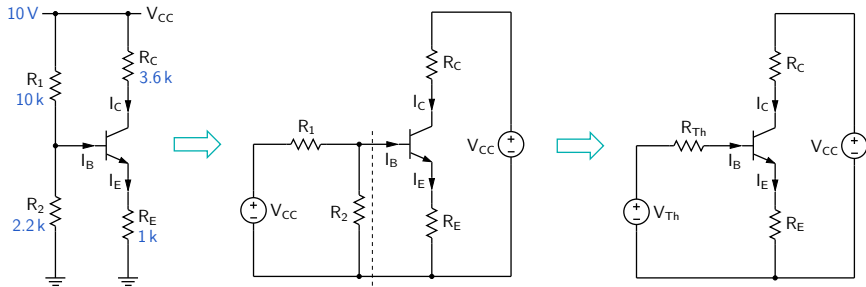
BJT amplifier: improved biasing scheme



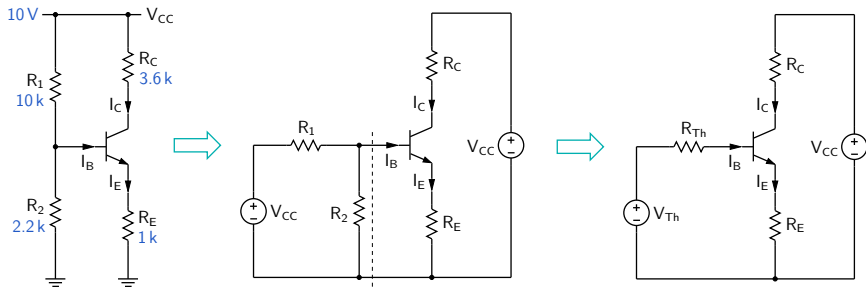
BJT amplifier: improved biasing scheme



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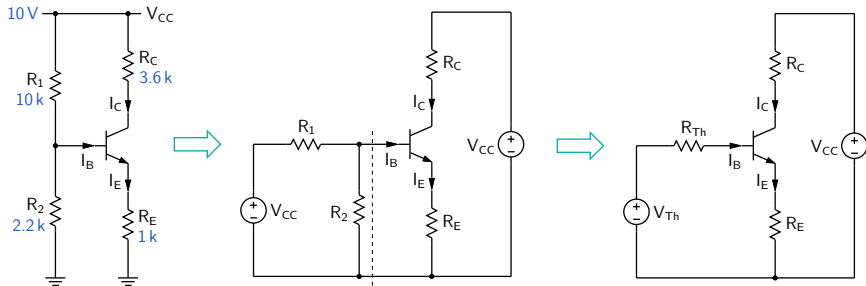


BJT amplifier: improved biasing scheme



$$V_{Th} = \frac{R_2}{R_1 + R_2} V_{CC} = \frac{2.2\text{k}}{10\text{k} + 2.2\text{k}} \times 10\text{V} = 1.8\text{V}, \quad R_{Th} = R_1 \parallel R_2 = 1.8\text{k}$$

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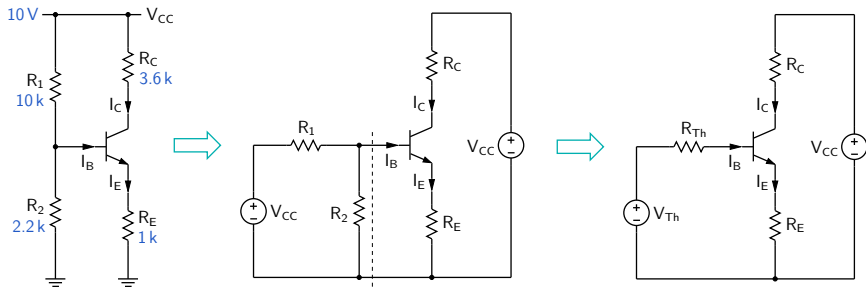


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Assuming the BJT to be in the active mode,

$$\text{KVL: } V_{Th} = R_{Th} I_B + V_{BE} + R_E I_E = R_{Th} I_B + V_{BE} + (\beta + 1) I_B R_E$$

BJT amplifier: improved biasing scheme



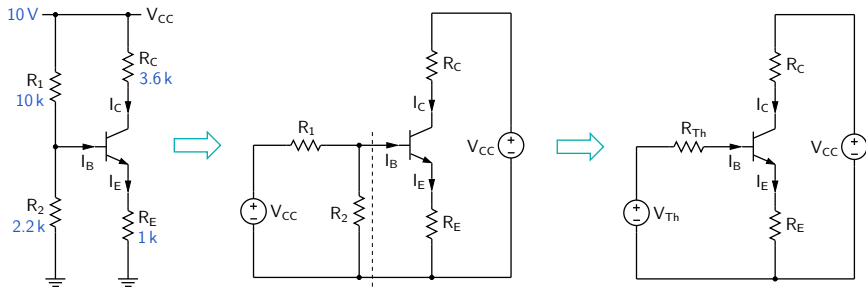
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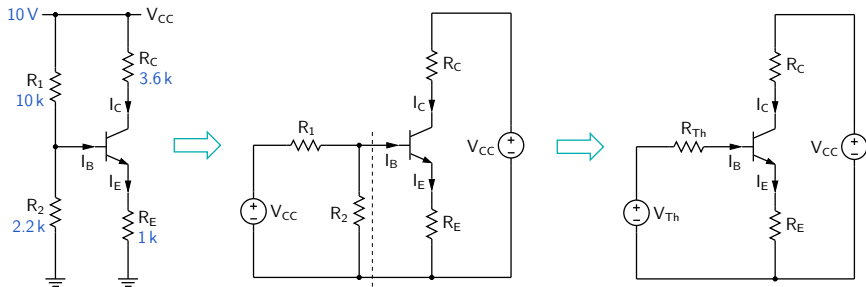
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For $\beta = 100$, $I_C = 1.07\text{mA}$.

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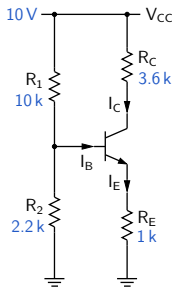
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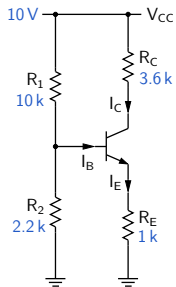
For $\beta = 200$, $I_C = 1.085\text{mA}$.

BJT amplifier: improved biasing scheme (continued)



With $I_C = 1.1\text{ mA}$, the various DC ("bias") voltages are

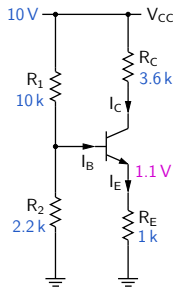
BJT amplifier: improved biasing scheme (continued)



With $I_C = 1.1 \text{ mA}$, the various DC ("bias") voltages are

$$V_E = I_E R_E \approx 1.1 \text{ mA} \times 1 \text{ k} = 1.1 \text{ V},$$

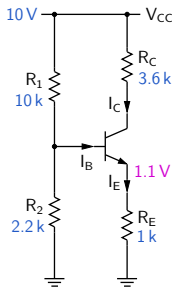
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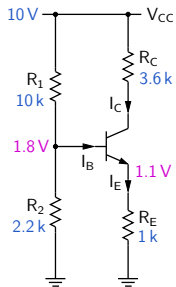


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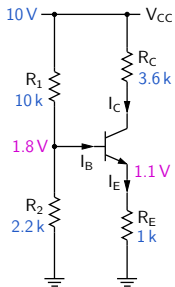


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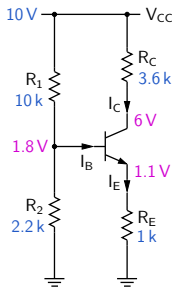
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BJT amplifier: improved biasing scheme (continued)



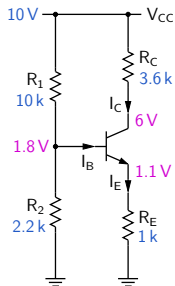
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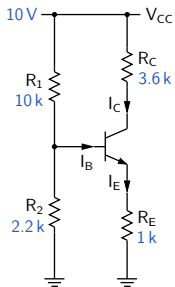
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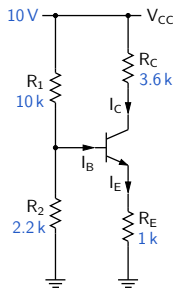
$$V_{CE} = V_C - V_E = 6 - 1.1 = 4.9\text{V}.$$

BJT amplifier: improved biasing scheme (continued)



A quick estimate of the bias values can be obtained by ignoring I_B (which is fair if β is large). In that case,

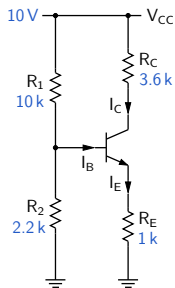
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BJT amplifier: improved biasing scheme (continued)

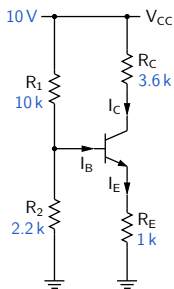


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BJT amplifier: improved biasing scheme (continued)



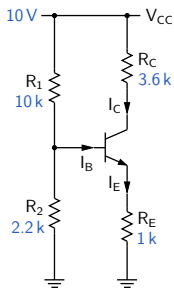
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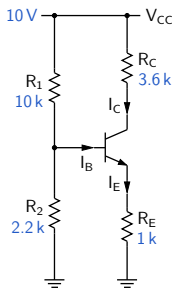
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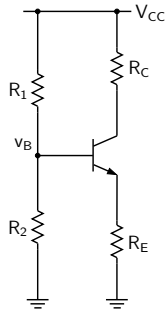
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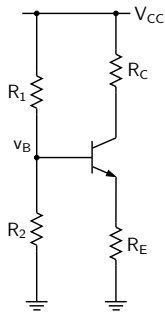
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Adding signal to bias

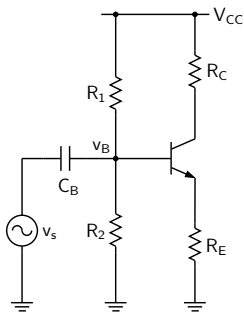


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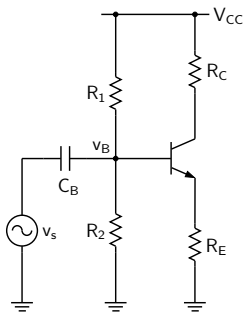


- * As we have seen earlier, the input signal $v_s(t) = \hat{V} \sin \omega t$ (for example) needs to be mixed with the desired bias value V_B so that the net voltage at the base is $v_B(t) = V_B + \hat{V} \sin \omega t$.

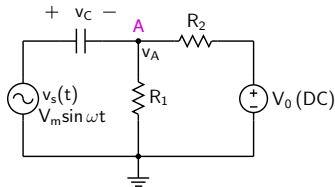
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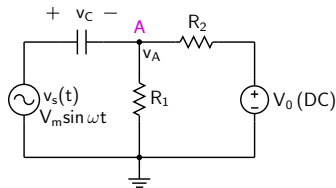
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- * This can be achieved by using a coupling capacitor C_B .
- * Let us consider a simple circuit to illustrate how a coupling capacitor works.

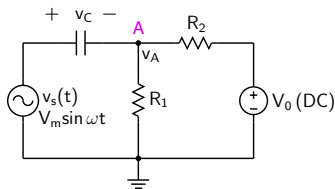


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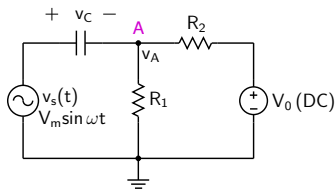


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There are two ways to obtain the solution:

(1) Solve the circuit equations directly:

$$\frac{v_A(t)}{R_1} + \frac{v_A(t) - V_0}{R_2} = C \frac{d}{dt} (v_s(t) - v_A(t)).$$



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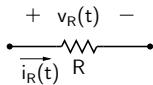
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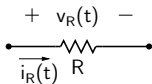
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- (2) Use the DC circuit + AC circuit approach.

Resistor in sinusoidal steady state

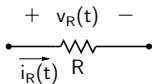


Resistor in sinusoidal steady state



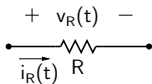
Let $v_R(t) = V_R + v_r(t)$ where $V_R = \text{constant}$, $v_r(t) = \widehat{V}_R \sin(\omega t + \alpha)$,
 $i_R(t) = I_R + i_r(t)$ where $I_R = \text{constant}$, $i_r(t) = \widehat{I}_R \sin(\omega t + \alpha)$.

Resistor in sinusoidal steady state



Let $v_R(t) = V_R + v_r(t)$ where $V_R = \text{constant}$, $v_r(t) = \widehat{V}_R \sin(\omega t + \alpha)$,
 $i_R(t) = I_R + i_r(t)$ where $I_R = \text{constant}$, $i_r(t) = \widehat{I}_R \sin(\omega t + \alpha)$.

Since $v_R(t) = R \times i_R(t)$, we get $[V_R + v_r(t)] = R \times [I_R + i_r(t)]$.



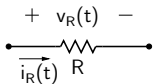
Let $v_R(t) = V_R + v_r(t)$ where $V_R = \text{constant}$, $v_r(t) = \widehat{V}_R \sin(\omega t + \alpha)$,
 $i_R(t) = I_R + i_r(t)$ where $I_R = \text{constant}$, $i_r(t) = \widehat{I}_R \sin(\omega t + \alpha)$.

Since $v_R(t) = R \times i_R(t)$, we get $[V_R + v_r(t)] = R \times [I_R + i_r(t)]$.

This relationship can be split into two:

$V_R = R \times I_R$, and $v_r(t) = R \times i_r(t)$.

Resistor in sinusoidal steady state



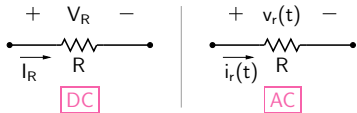
Let $v_R(t) = V_R + v_r(t)$ where $V_R = \text{constant}$, $v_r(t) = \widehat{V}_R \sin(\omega t + \alpha)$,
 $i_R(t) = I_R + i_r(t)$ where $I_R = \text{constant}$, $i_r(t) = \widehat{I}_R \sin(\omega t + \alpha)$.

Since $v_R(t) = R \times i_R(t)$, we get $[V_R + v_r(t)] = R \times [I_R + i_r(t)]$.

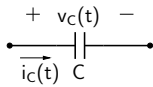
This relationship can be split into two:

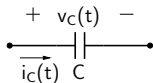
$V_R = R \times I_R$, and $v_r(t) = R \times i_r(t)$.

In other words, a resistor can be described by

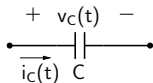


Capacitor in sinusoidal steady state



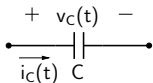


Let $v_C(t) = V_C + v_c(t)$ where $V_C = \text{constant}$, $v_c(t) = \widehat{V}_C \sin(\omega t + \alpha)$,
 $i_C(t) = I_C + i_c(t)$ where $I_C = \text{constant}$, $i_c(t) = \widehat{I}_C \sin(\omega t + \beta)$.



Let $v_C(t) = V_C + v_c(t)$ where $V_C = \text{constant}$, $v_c(t) = \widehat{V}_C \sin(\omega t + \alpha)$,
 $i_C(t) = I_C + i_c(t)$ where $I_C = \text{constant}$, $i_c(t) = \widehat{I}_C \sin(\omega t + \beta)$.

Since $i_C(t) = C \frac{dv_C}{dt}$, we get $[I_C + i_c(t)] = C \frac{d}{dt} (V_C + v_c(t))$.



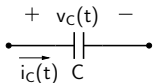
Let $v_C(t) = V_C + v_c(t)$ where $V_C = \text{constant}$, $v_c(t) = \widehat{V}_C \sin(\omega t + \alpha)$,
 $i_C(t) = I_C + i_c(t)$ where $I_C = \text{constant}$, $i_c(t) = \widehat{I}_C \sin(\omega t + \beta)$.

Since $i_C(t) = C \frac{dv_C}{dt}$, we get $[I_C + i_c(t)] = C \frac{d}{dt} (V_C + v_c(t))$.

This relationship can be split into two:

$$I_C = C \frac{dV_C}{dt} = 0, \text{ and } i_c(t) = C \frac{dv_c}{dt}.$$

Capacitor in sinusoidal steady state



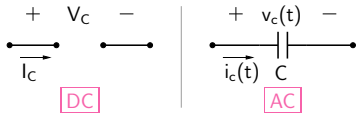
Let $v_C(t) = V_C + v_c(t)$ where $V_C = \text{constant}$, $v_c(t) = \widehat{V}_C \sin(\omega t + \alpha)$,
 $i_C(t) = I_C + i_c(t)$ where $I_C = \text{constant}$, $i_c(t) = \widehat{I}_C \sin(\omega t + \beta)$.

Since $i_C(t) = C \frac{dv_C}{dt}$, we get $[I_C + i_c(t)] = C \frac{d}{dt} (V_C + v_c(t))$.

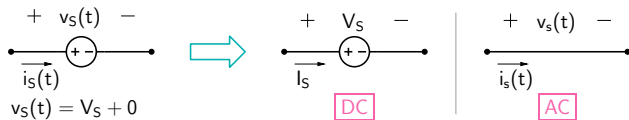
This relationship can be split into two:

$$I_C = C \frac{dV_C}{dt} = 0, \text{ and } i_c(t) = C \frac{dv_c}{dt}.$$

In other words, a capacitor can be described by

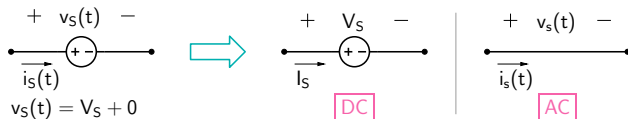


DC voltage source:

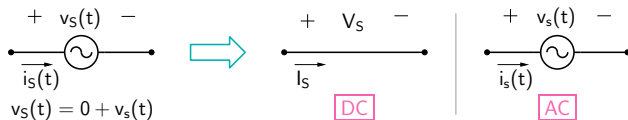


Voltage sources in sinusoidal steady state

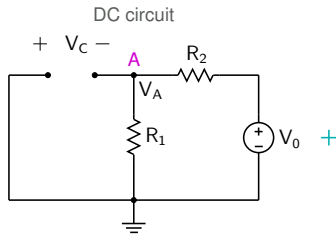
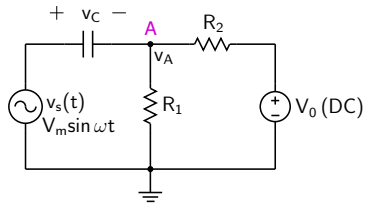
DC voltage source:



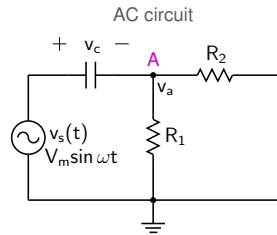
AC voltage source:



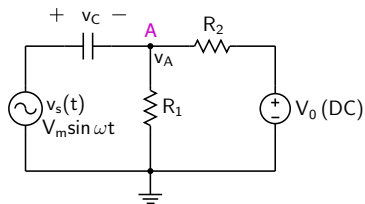
RC circuit with DC + AC sources



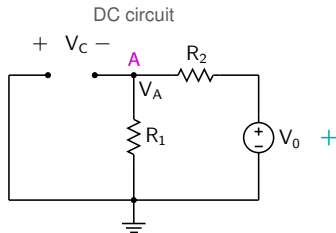
+



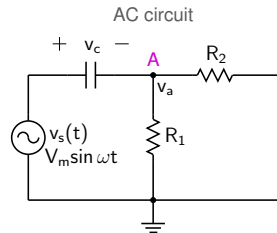
RC circuit with DC + AC sources



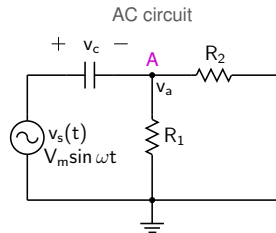
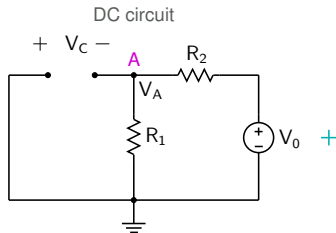
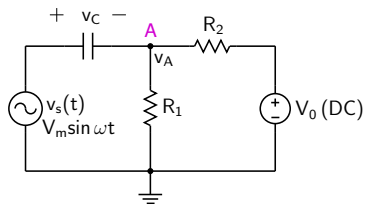
DC circuit:
$$\frac{V_A}{R_1} + \frac{V_A - V_0}{R_2} = 0.$$



(1)



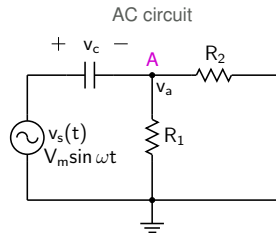
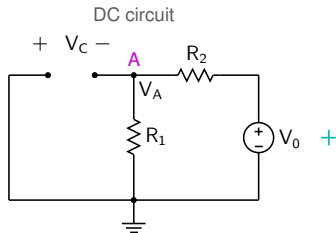
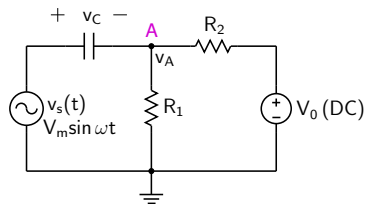
RC circuit with DC + AC sources



DC circuit:
$$\frac{V_A}{R_1} + \frac{V_A - V_0}{R_2} = 0. \quad (1)$$

AC circuit:
$$\frac{v_a}{R_1} + \frac{v_a}{R_2} = C \frac{d}{dt} (v_s - v_a). \quad (2)$$

RC circuit with DC + AC sources

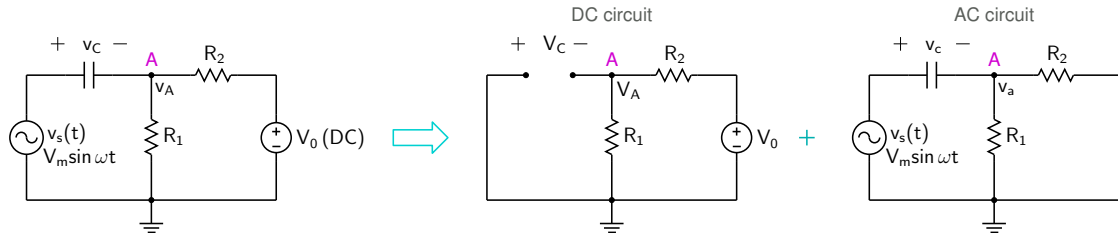


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$$\frac{v_a}{R_1} + \frac{v_a}{R_2} = C \frac{d}{dt} (v_s - v_a). \quad (2)$$

Adding (1) and (2), we get
$$\frac{V_A + v_a}{R_1} + \frac{V_A + v_a - V_0}{R_2} = C \frac{d}{dt} (v_s - v_a). \quad (3)$$

RC circuit with DC + AC sources



DC circuit:
$$\frac{V_A}{R_1} + \frac{V_A - V_0}{R_2} = 0. \quad (1)$$

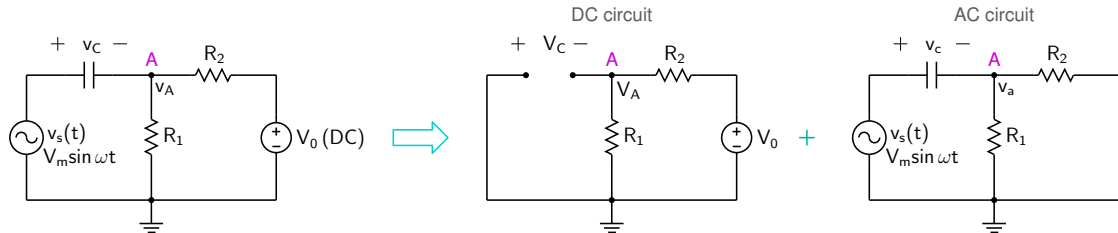
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$$\frac{v_a}{R_1} + \frac{v_a}{R_2} = C \frac{d}{dt} (v_s - v_a). \quad (2)$$

Adding (1) and (2), we get
$$\frac{V_A + v_a}{R_1} + \frac{V_A + v_a - V_0}{R_2} = C \frac{d}{dt} (v_s - v_a). \quad (3)$$

Compare with the equation obtained directly from the original circuit:

$$\frac{v_A}{R_1} + \frac{v_A - V_0}{R_2} = C \frac{d}{dt} (v_s - v_A). \quad (4)$$

RC circuit with DC + AC sources



DC circuit:
$$\frac{V_A}{R_1} + \frac{V_A - V_0}{R_2} = 0. \quad (1)$$

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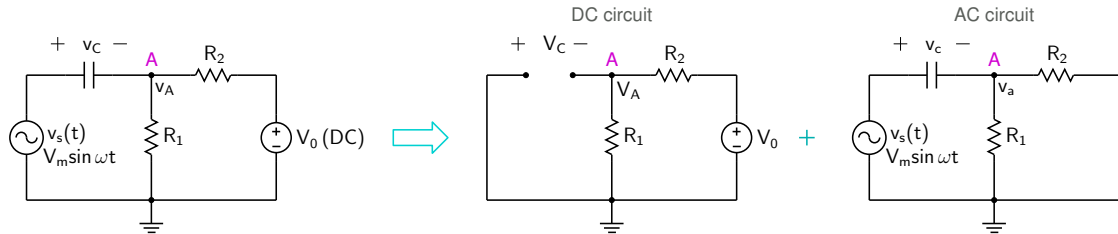
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Compare with the equation obtained directly from the original circuit:

$$\frac{v_A}{R_1} + \frac{v_A - V_0}{R_2} = C \frac{d}{dt} (v_s - v_A). \quad (4)$$

Eqs. (3) and (4) are identical since $v_A = V_A + v_a$.

RC circuit with DC + AC sources



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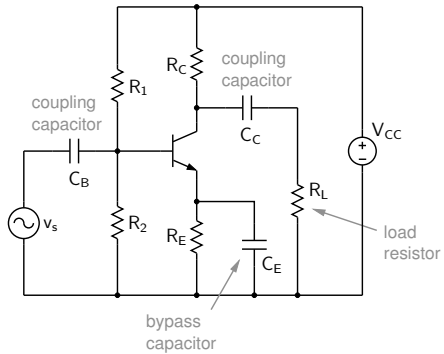
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Eqs. (3) and (4) are identical since $v_A = V_A + v_a$.

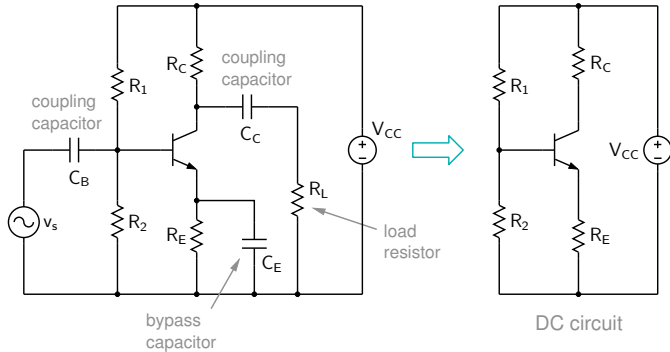
→ Instead of computing $v_A(t)$ directly, we can compute V_A and $v_a(t)$ separately, and then use

$$v_A(t) = V_A + v_a(t).$$

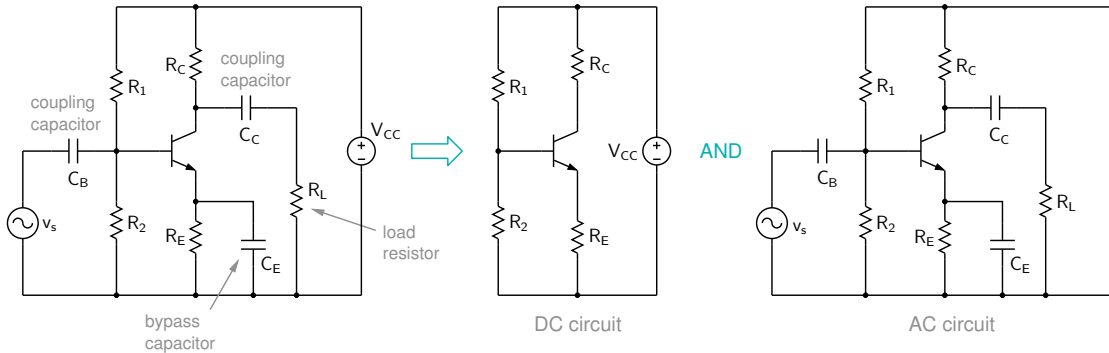
Common-emitter amplifier



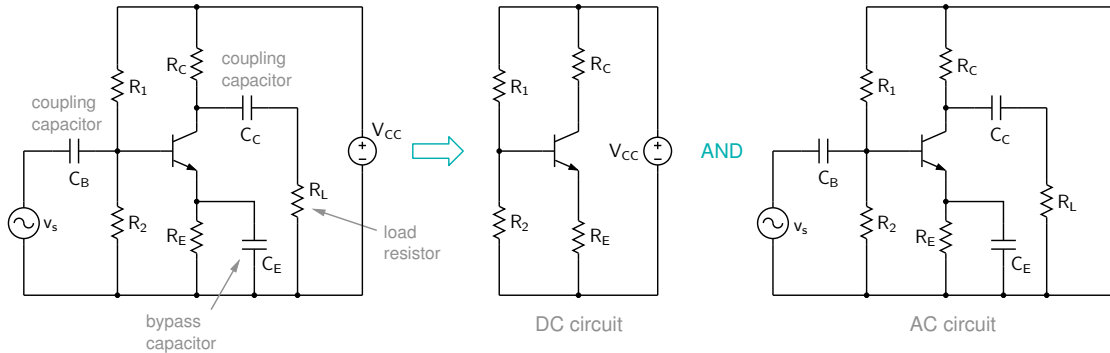
Common-emitter amplifier



Common-emitter amplifier

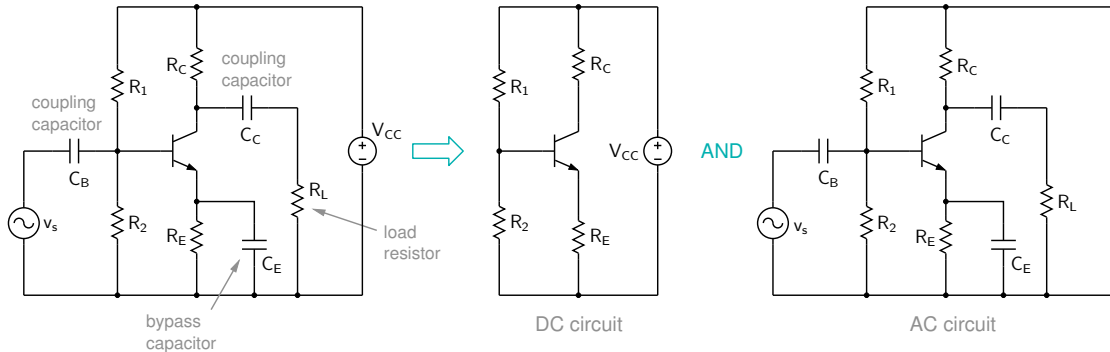


Common-emitter amplifier



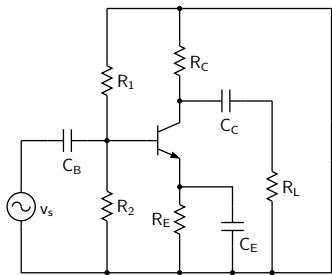
* The coupling capacitors ensure that the signal source and the load resistor do not affect the DC bias of the amplifier. (We will see the purpose of C_E a little later.)

Common-emitter amplifier

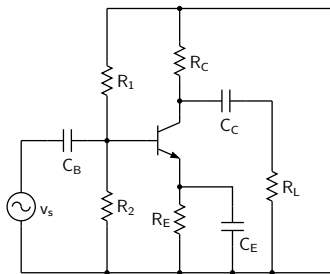


- * The coupling capacitors ensure that the signal source and the load resistor do not affect the DC bias of the amplifier. (We will see the purpose of C_E a little later.)
- * This enables us to bias the amplifier without worrying about what load it is going to drive.

Common-emitter amplifier: AC circuit



Common-emitter amplifier: AC circuit



- * The coupling and bypass capacitors are “large” (typically, a few μF), and at frequencies of interest, their impedance is small.

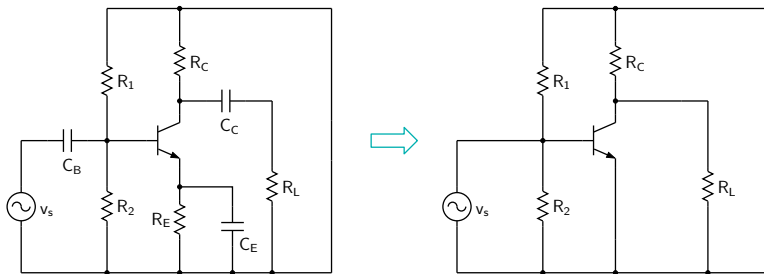
For example, for $C = 10 \mu F$, $f = 1 \text{ kHz}$,

$$Z_C = \frac{1}{2\pi \times 10^3 \times 10 \times 10^{-6}} = 16 \Omega,$$

which is much smaller than typical values of R_1 , R_2 , R_C , R_E (a few $\text{k}\Omega$).

$\Rightarrow C_B$, C_C , C_E can be replaced by short circuits at the frequencies of interest.

Common-emitter amplifier: AC circuit



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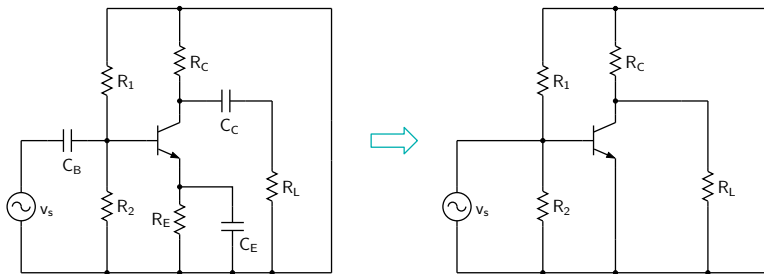
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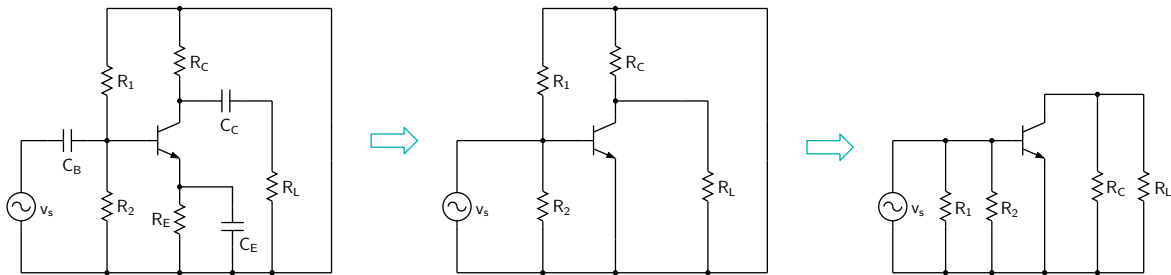
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- * The circuit can be re-drawn in a more friendly format.

Common-emitter amplifier: AC circuit



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For example, for $C = 10 \mu F$, $f = 1 \text{ kHz}$,

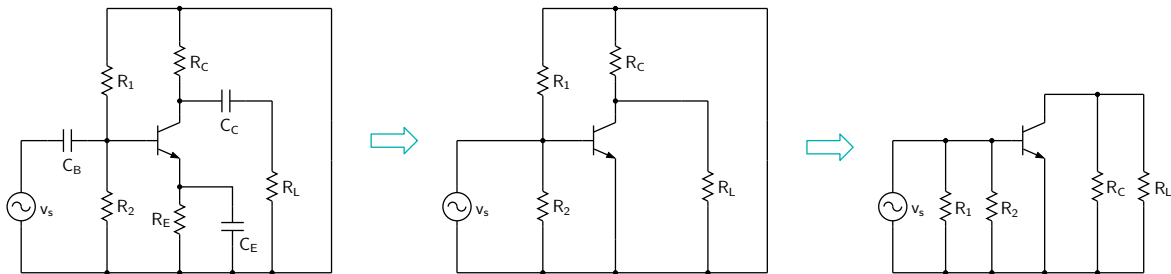
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Common-emitter amplifier: AC circuit



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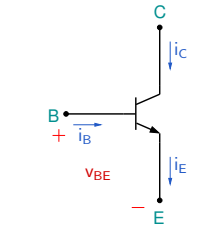
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$$Z_C = \frac{1}{2\pi \times 10^3 \times 10 \times 10^{-6}} = 16 \Omega,$$

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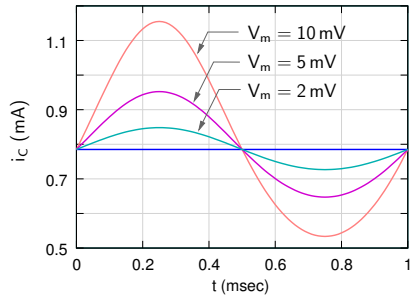
$\Rightarrow C_B$, C_C , C_E can be replaced by short circuits at the frequencies of interest.

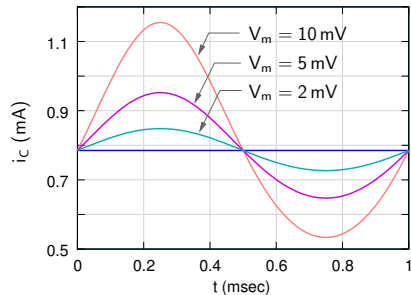
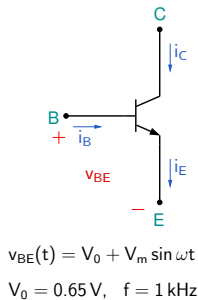
- * The circuit can be re-drawn in a more friendly format.
- * We now need to figure out the AC description of a BJT.



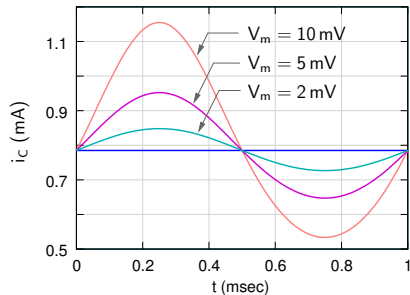
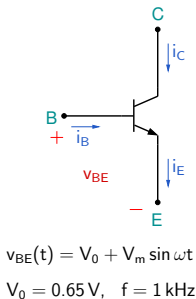
$$v_{BE}(t) = V_0 + V_m \sin \omega t$$

$$V_0 = 0.65 \text{ V}, \quad f = 1 \text{ kHz}$$



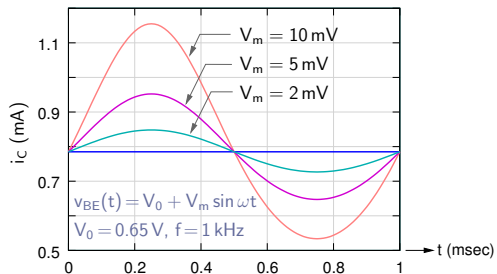
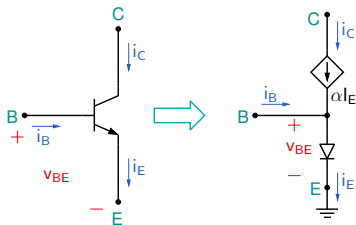


- * As the v_{BE} amplitude increases, the shape of $i_C(t)$ deviates from a sinusoid → distortion.



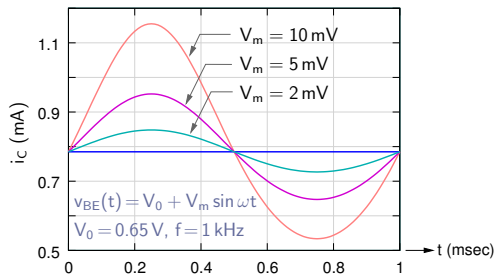
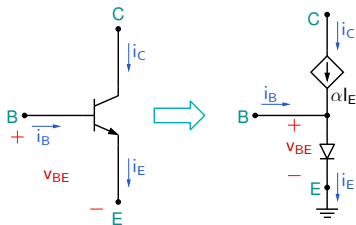
- * As the v_{BE} amplitude increases, the shape of $i_C(t)$ deviates from a sinusoid → distortion.
- * If $v_{be}(t)$, i.e., the time-varying part of v_{BE} , is kept small, i_C varies linearly with v_{BE} . How small? Let us look at this in more detail.

BJT: small-signal model



Let $v_{BE}(t) = V_{BE} + v_{be}(t)$ (bias+signal), and $i_C(t) = I_C + i_c(t)$.

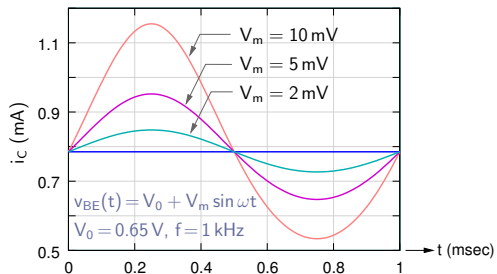
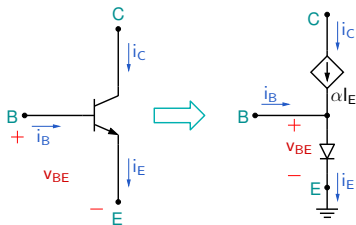
BJT: small-signal model



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Assuming active mode, $i_C(t) = \alpha i_E(t) = \alpha I_{ES} \left[\exp\left(\frac{v_{BE}(t)}{V_T}\right) - 1 \right]$.

BJT: small-signal model



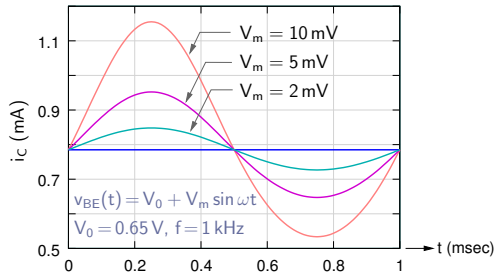
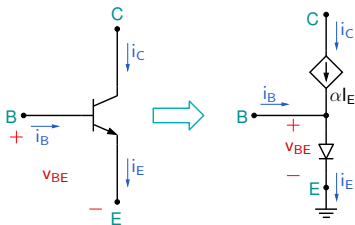
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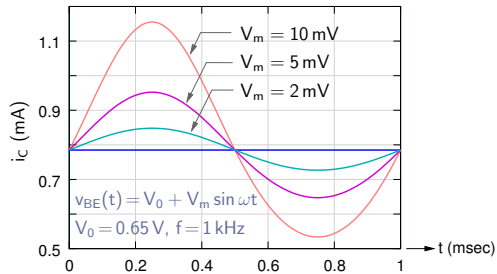
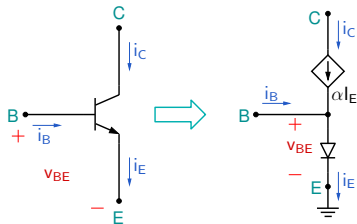
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If $v_{be}(t) = 0$, $i_C(t) = I_C$ (the bias value of i_C), i.e., $I_C = \alpha I_{ES} \exp\left(\frac{V_{BE}}{V_T}\right)$

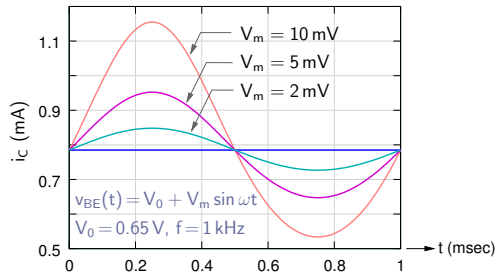
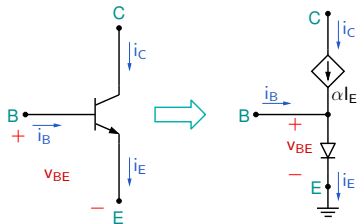
$$\Rightarrow i_C(t) = I_C \exp\left(\frac{v_{be}(t)}{V_T}\right).$$

BJT: small-signal model



$$i_C(t) = I_C \exp\left(\frac{v_{be}(t)}{V_T}\right) = I_C \left[1 + x + \frac{x^2}{2} + \dots\right], \quad x = v_{be}(t)/V_T.$$

BJT: small-signal model

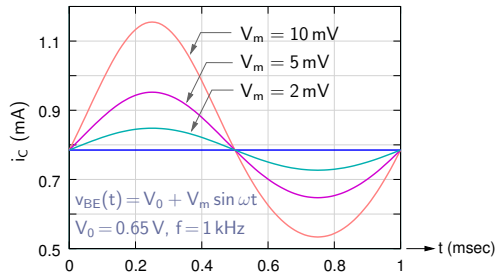
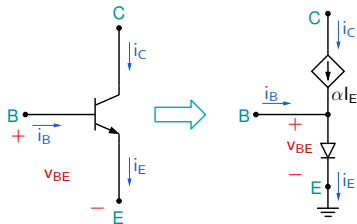


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If x is small, i.e., if the amplitude of $v_{be}(t)$ is small compared to the thermal voltage V_T , we get

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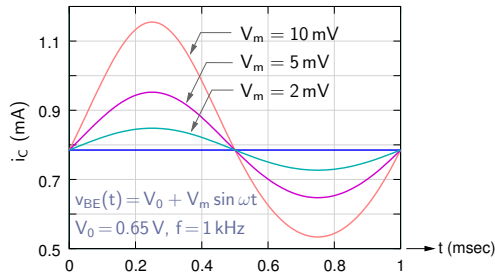
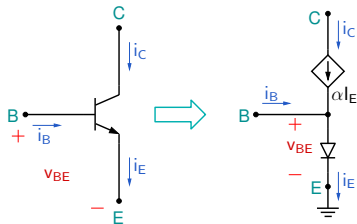
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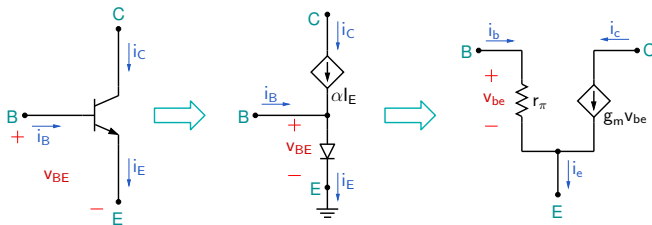
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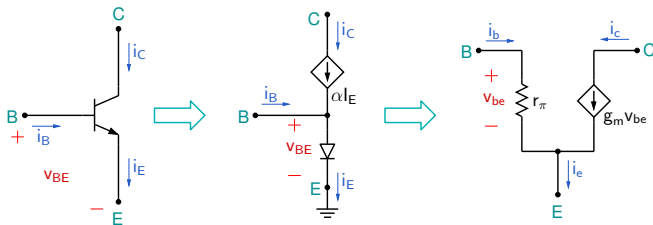
$$i_C(t) = I_C + i_c(t) = I_C \left[1 + \frac{v_{be}(t)}{V_T}\right] \Rightarrow \boxed{i_c(t) = \frac{I_C}{V_T} v_{be}(t)}$$

BJT: small-signal model



The relationship, $i_c(t) = \frac{I_C}{V_T} v_{be}(t)$ can be represented by a VCCS, $i_c(t) = g_m v_{be}(t)$, where $g_m = I_C/V_T$ is the “transconductance.”

BJT: small-signal model

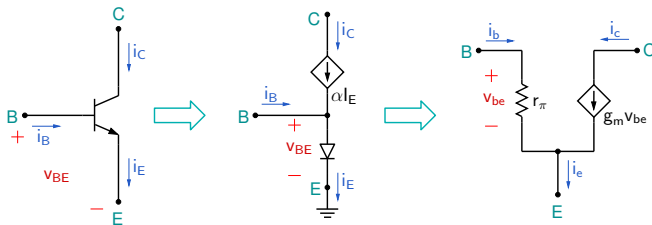


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For the base current, we have,

$$i_B(t) = I_B + i_b(t) = \frac{1}{\beta} [I_C + i_c(t)]$$

$$\rightarrow i_b(t) = \frac{1}{\beta} i_c(t) = \frac{1}{\beta} g_m v_{be}(t) \rightarrow v_{be}(t) = (\beta/g_m) i_b(t).$$



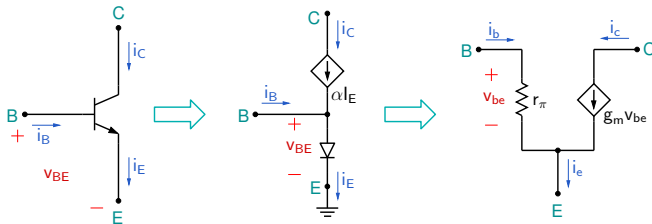
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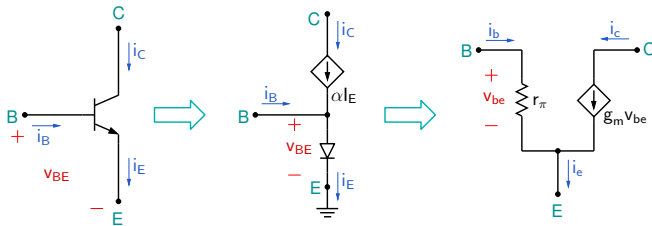
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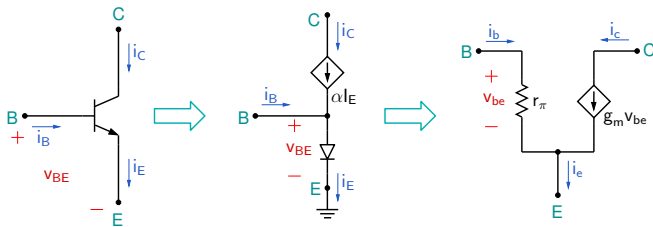
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The resulting model is called the π -model for small-signal description of a BJT.

BJT: small-signal model

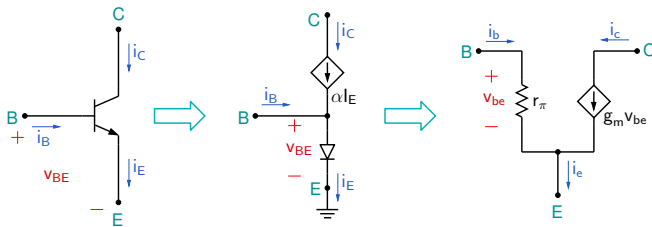


- * The transconductance g_m depends on the biasing of the BJT, since $g_m = I_C/V_T$. For $I_C = 1 \text{ mA}$, $V_T \approx 25 \text{ mV}$ (room temperature), $g_m = 1 \text{ mA}/25 \text{ mV} = 40 \text{ m}\Omega$ (milli-mho or milli-siemens).



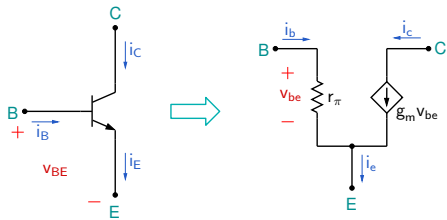
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BJT: small-signal model



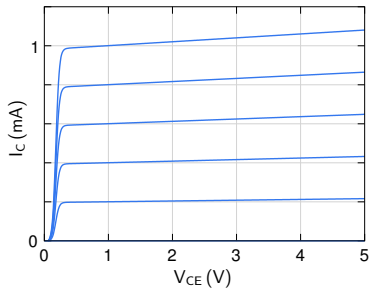
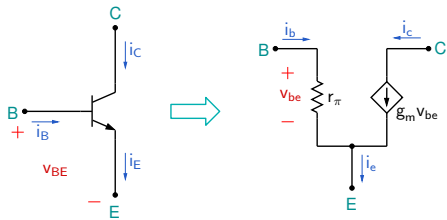
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- * Note that the small-signal model is valid only for small v_{be} (small compared to V_T).

BJT: small-signal model



* In the above model, note that i_c is independent of v_{ce} .

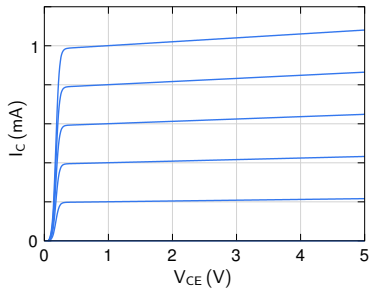
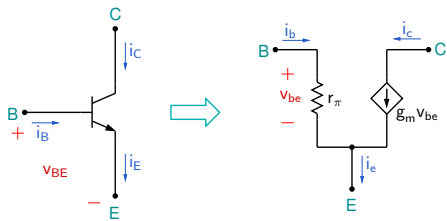
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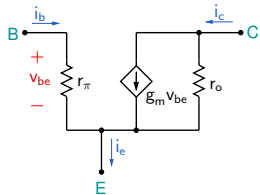
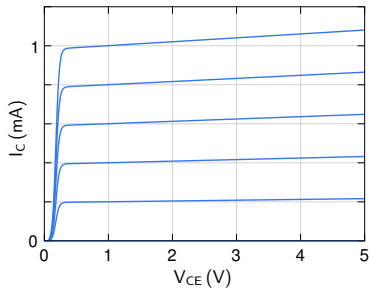
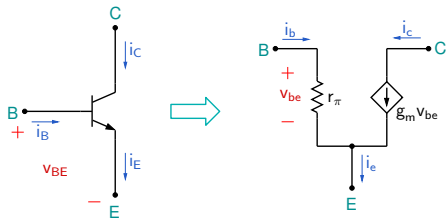
* In practice, i_c does depend on v_{ce} because of the Early effect, and $\frac{dI_C}{dV_{CE}} \approx \text{constant} = 1/r_o$, where r_o is called the output resistance.

BJT: small-signal model



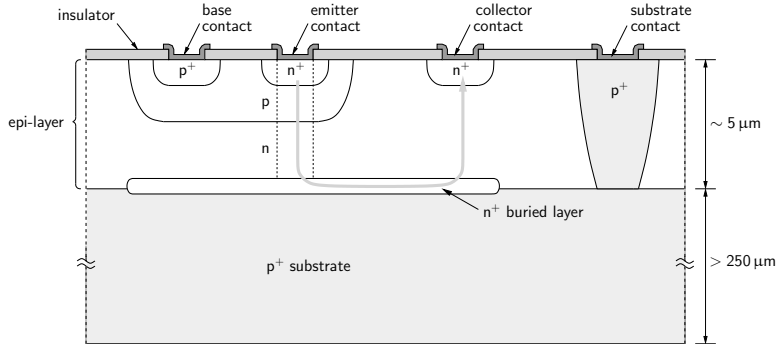
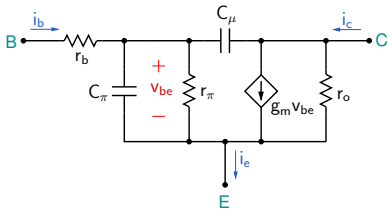
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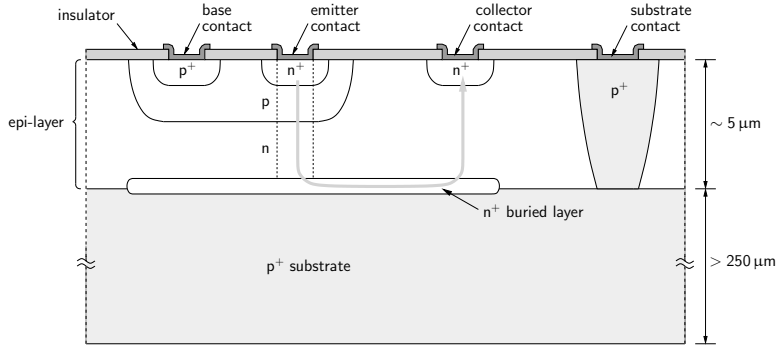
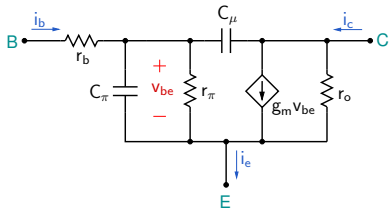
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r_b : base spreading resistance

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BJT: small-signal model



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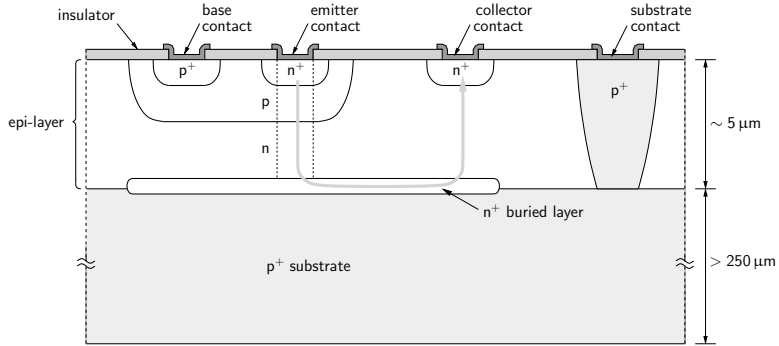
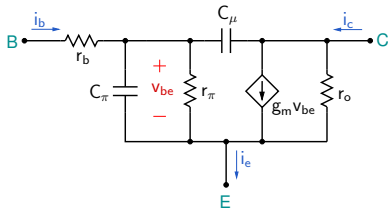
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BJT: small-signal model



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- * The capacitances are typically in the pF range. At low frequencies, $1/\omega C$ is large, and the capacitances can be replaced by open circuits.
- * Note that the small-signal models we have described are valid in the active region only.