

BJT Amplifiers: Part 2



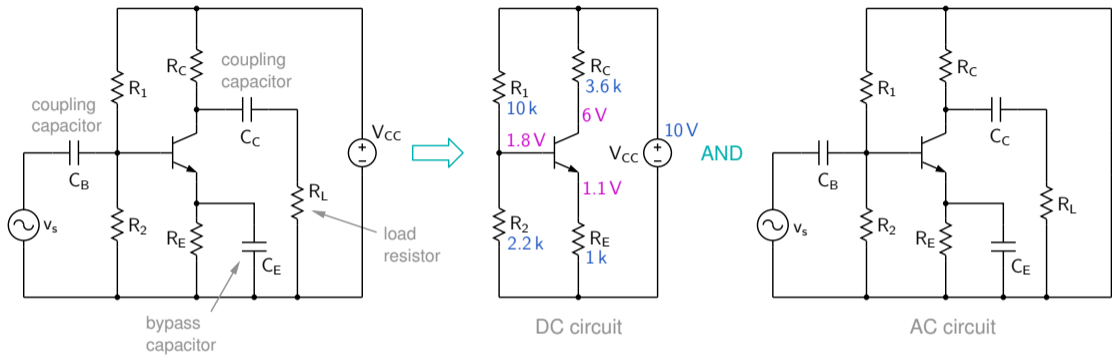
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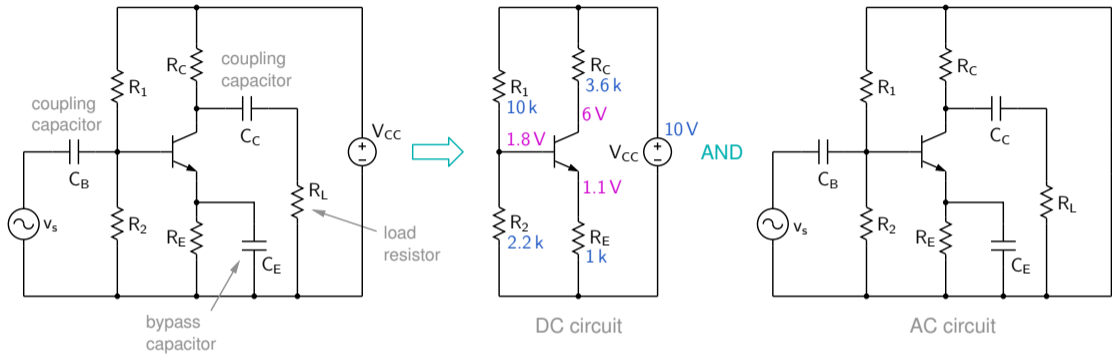
Department of Electrical Engineering
Indian Institute of Technology Bombay

Common-emitter amplifier



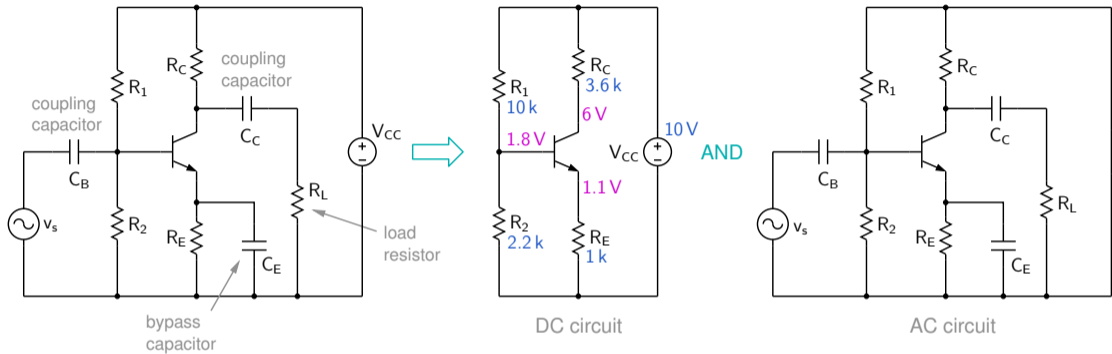
* We have already analysed the DC (bias) circuit of this amplifier and found that $V_B = 1.8\text{ V}$, $V_E = 1.1\text{ V}$, $V_C = 6\text{ V}$, and $I_C = 1.1\text{ mA}$.

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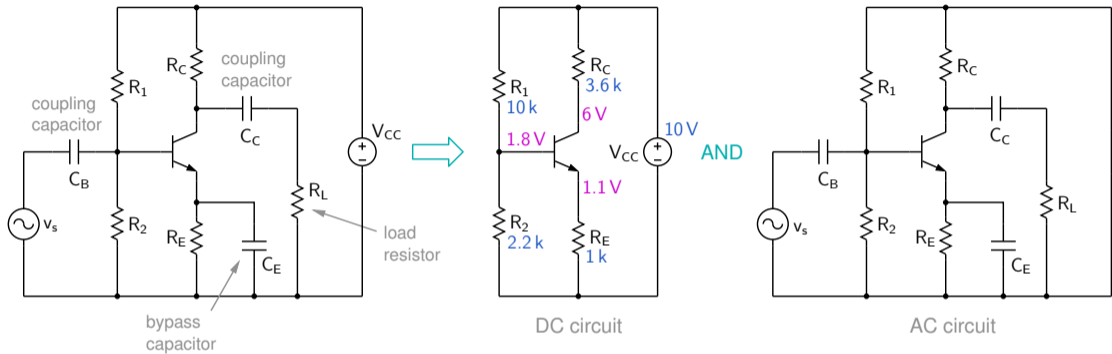
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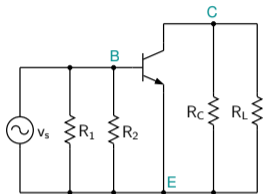
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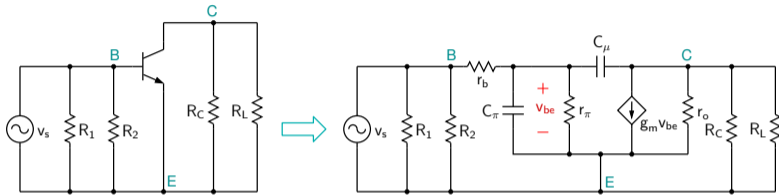


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- * We now analyse the AC (small-signal) circuit to obtain v_b , v_e , v_c , i_c .
- * We will then get the complete solution by simply adding the DC and AC results, e.g., $i_C(t) = I_C + i_c(t)$.
- * We will assume that C_B , C_C , C_E are large enough so that, at the signal frequency (say, 1 kHz), they can be replaced by short circuits.

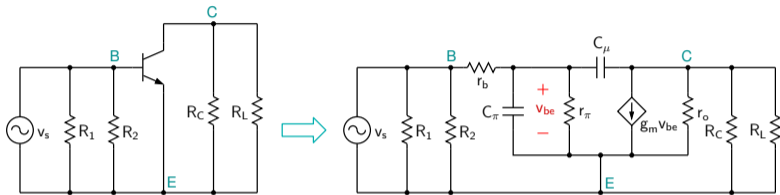
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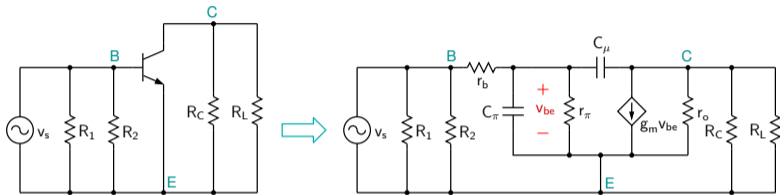


- * The parasitic capacitances C_{π} and C_{μ} are in the pF range. At a signal frequency of 1 kHz, the impedance corresponding to these capacitances is

$$\mathbf{Z} \sim \frac{-j}{\omega C} = \frac{-j}{2\pi \times 10^3 \times 10^{-12}} \sim -j 100 \text{ M}\Omega$$

→ C_{π} and C_{μ} can be replaced by open circuits.

Common-emitter amplifier



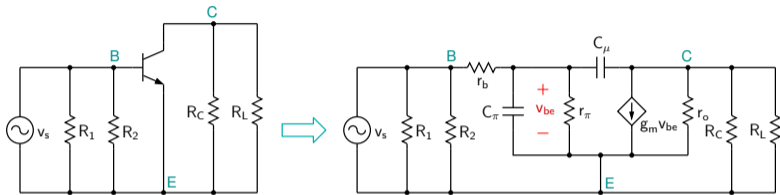
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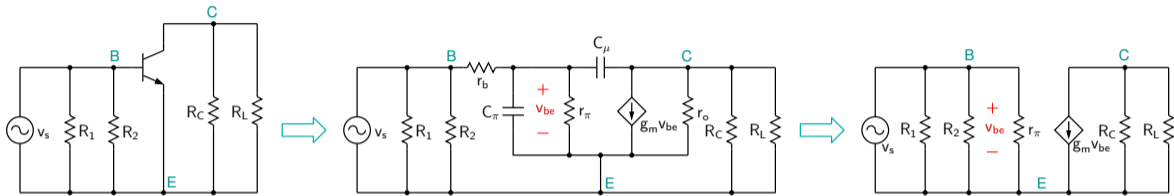
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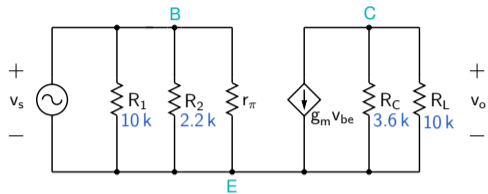
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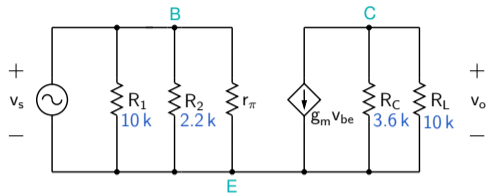
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Common-emitter amplifier



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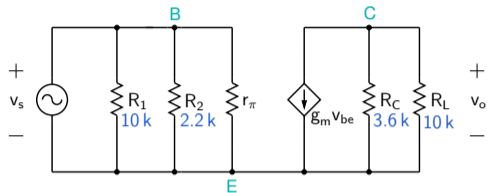


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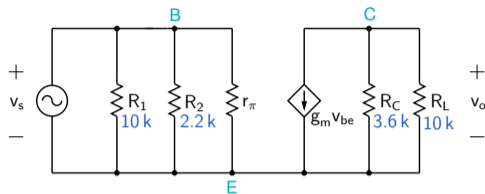
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Common-emitter amplifier



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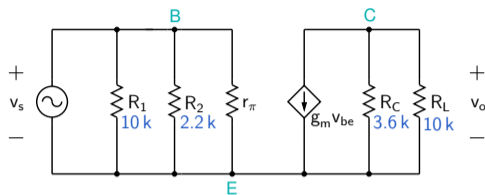
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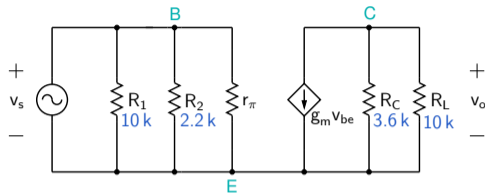
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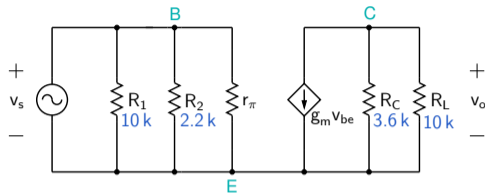
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For $v_s(t) = (2 \text{ mV}) \sin \omega t$, the AC output voltage is,

$$v_o = A_V^L v_s = -(112.5) (2 \text{ mV}) \sin \omega t = -(225 \text{ mV}) \sin \omega t$$

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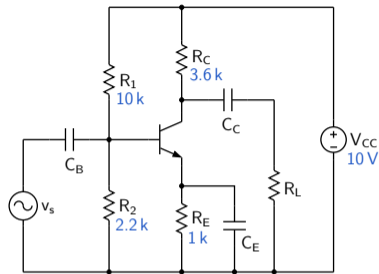
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The AC collector current is,

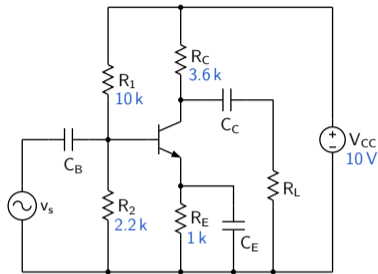
$$i_c = g_m v_{be} = g_m v_s = 42.5 \text{ m}\Omega \times (2 \text{ mV}) \sin \omega t = 85 \sin \omega t \mu\text{A}.$$



For $v_s(t) = (2 \text{ mV}) \sin \omega t$, we can now obtain expressions for the instantaneous currents and voltages:

$$v_C(t) = V_C + v_c(t) = V_C + v_o(t) = 6 \text{ V} - (225 \text{ mV}) \sin \omega t .$$

$$i_C(t) = I_C + i_c(t) = 1.1 \text{ mA} + 0.085 \sin \omega t \text{ mA} .$$

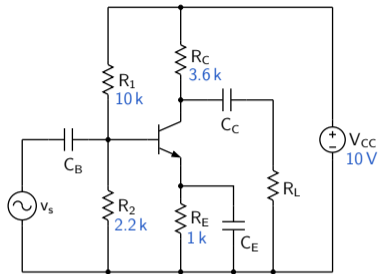


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Note that the above procedure (DC + AC analysis) can be used *only if* the small-signal approximation (i.e., $|v_{be}| \ll V_T$) is valid. In the above example, the amplitude of v_{be} is 2 mV, which is much smaller than V_T (about 25 mV).



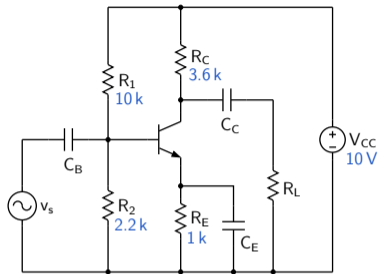
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For $v_s(t) = (20 \text{ mV}) \sin \omega t$, for example, the small-signal approximation will not hold, and a numerical simulation will be required to obtain the currents and voltages of interest.



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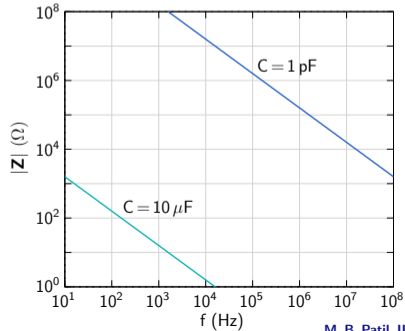
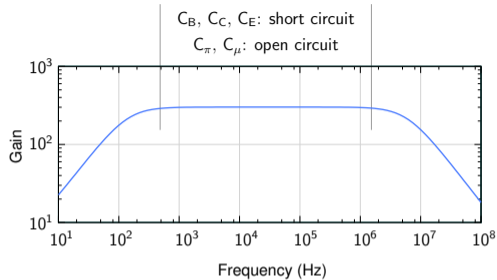
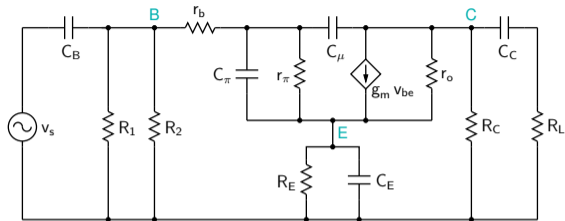
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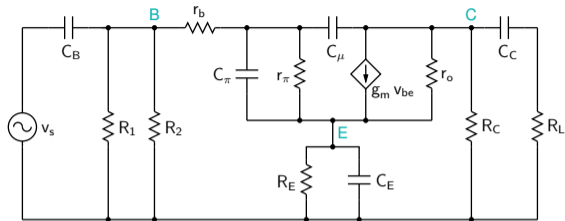
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In practice, such a situation is anyway not prevalent (because it gives rise to distortion in the output voltage) except in special types of amplifiers.

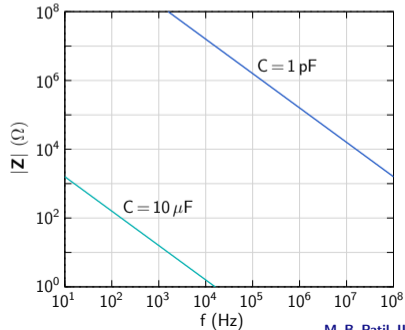
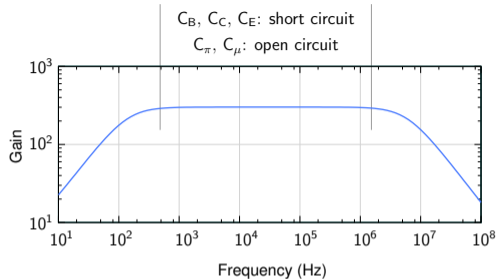
Frequency response of common-emitter amplifier



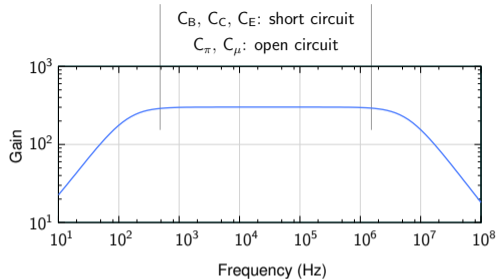
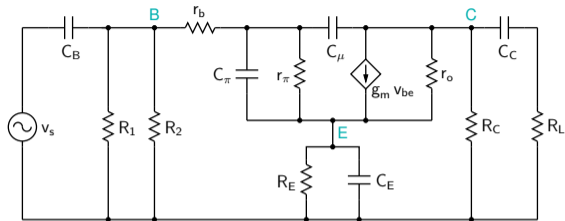
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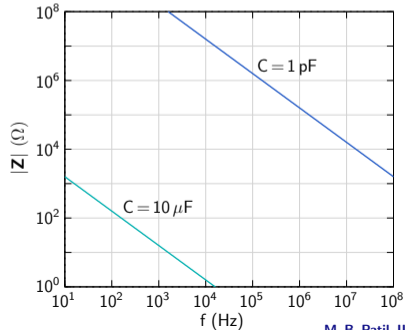
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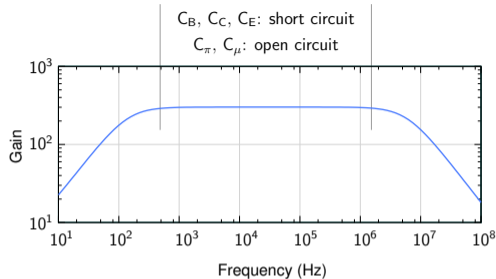
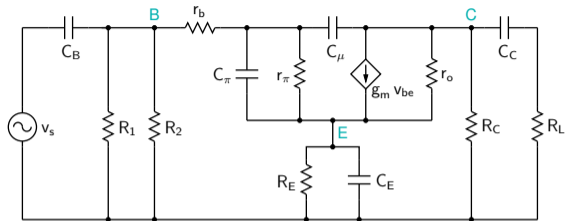
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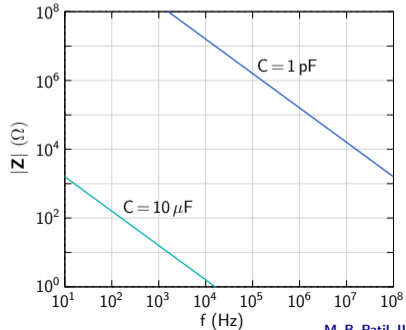
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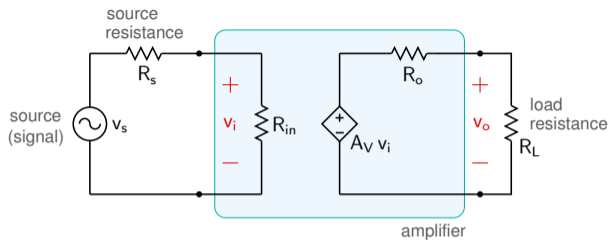
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- * In the “mid-band” range (which we have considered so far), the large capacitances behave like short circuits, and the small capacitances like open circuits. In this range, the gain is independent of frequency.

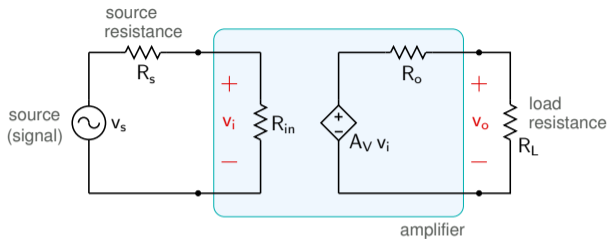


General representation of an amplifier



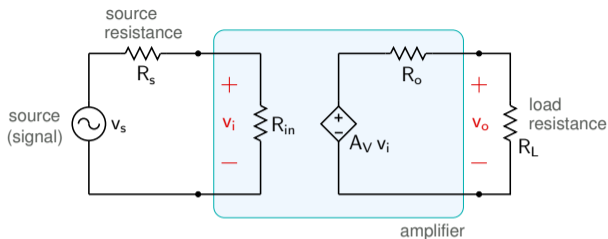
- * An amplifier is represented by a voltage gain, an input resistance R_{in} , and an output resistance R_o . For a voltage-to-voltage amplifier, a large R_{in} and a small R_o are desirable.

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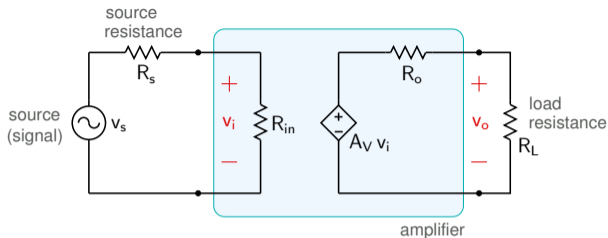
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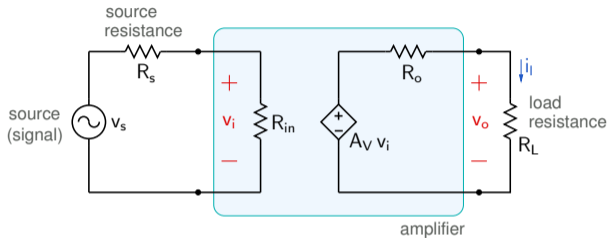
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- * The DC bias of the circuit can affect parameter values in the AC equivalent circuit (A_V , R_{in} , R_o). For example, for the common-emitter amplifier, $A_V \propto g_m = I_C/V_T$, I_C being the DC (bias) value of the collector current.

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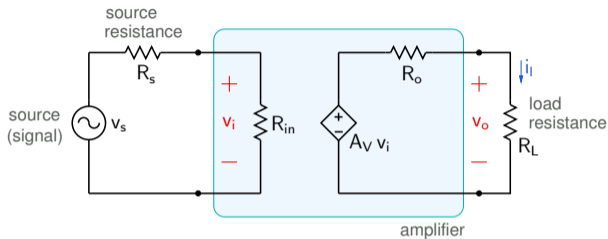
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- * Suppose we are given an amplifier as a “black box” and asked to find A_V , R_{in} , and R_o . What experiments would give us this information?

Voltage gain A_V



If $R_L \rightarrow \infty$, $i_l \rightarrow 0$, and $v_o \rightarrow A_V v_i$.

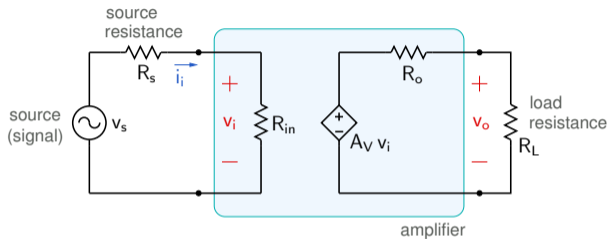
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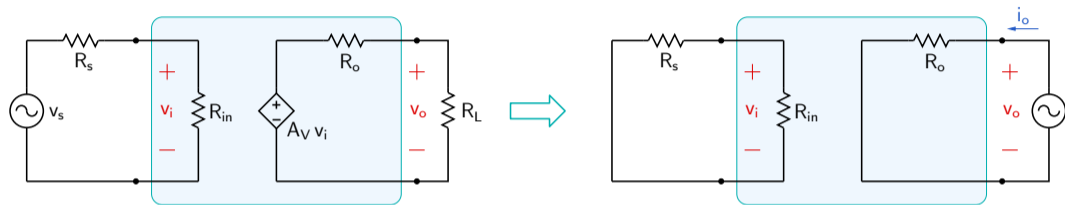
We can remove R_L (i.e., replace it with an open circuit), measure v_i and v_o , then use $A_V = v_o/v_i$.

Input resistance R_{in}



Measurement of v_i and i_i yields $R_{in} = v_i/i_i$.

Output resistance R_o



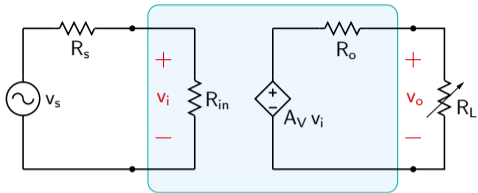
Method 1:

If $v_s \rightarrow 0$, $A_V v_i \rightarrow 0$.

Now, connect a test source v_o , and measure $i_o \rightarrow R_o = v_o/i_o$.

(This method works fine on paper, but it is difficult to use experimentally.)

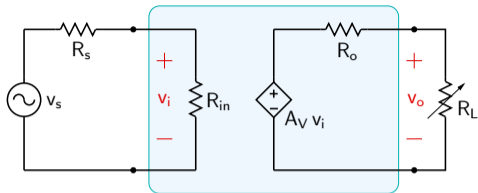
Output resistance R_o



Method 2:

$$v_o = \frac{R_L}{R_L + R_o} A_V v_i.$$

Output resistance R_o

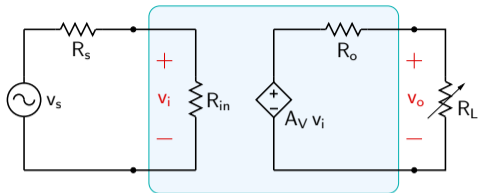


Method 2:

$$v_o = \frac{R_L}{R_L + R_o} A_V v_i.$$

If $R_L \rightarrow \infty$, $v_{o1} = A_V v_i$.

Output resistance R_o



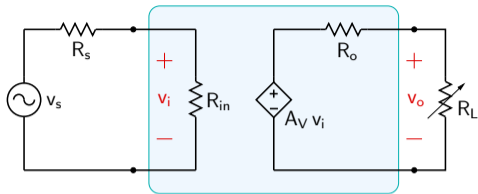
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$$\text{If } R_L \rightarrow \infty, v_{o1} = A_V v_i.$$

$$\text{If } R_L = R_o, v_{o2} = \frac{1}{2} A_V v_i = \frac{1}{2} v_{o1}.$$

Output resistance R_o



Method 2:

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Procedure:

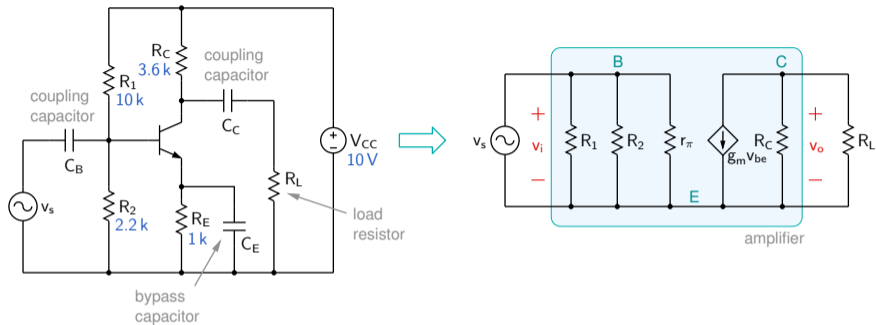
Measure v_{o1} with $R_L \rightarrow \infty$ (i.e., R_L removed).

Vary R_L and observe v_o .

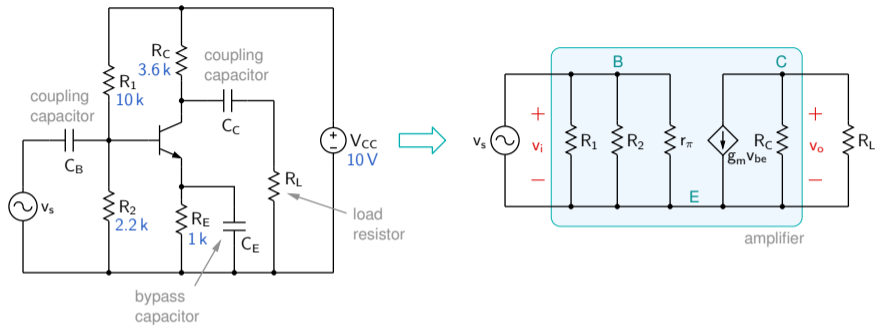
When v_o is equal to $v_{o1}/2$, measure R_L (after removing it).

R_o is the same as the measured resistance.

Common-emitter amplifier



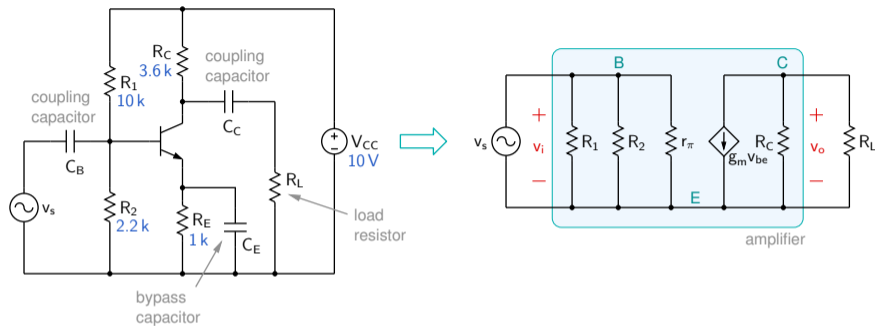
Common-emitter amplifier



$$A_V = \frac{v_o}{v_i}, \text{ with } R_L \rightarrow \infty.$$

$$A_V = \frac{-g_m v_{be} R_C}{v_i} = -g_m R_C = -42.5 \text{ mS} \times 3.6 \text{ k} = 153.$$

Common-emitter amplifier



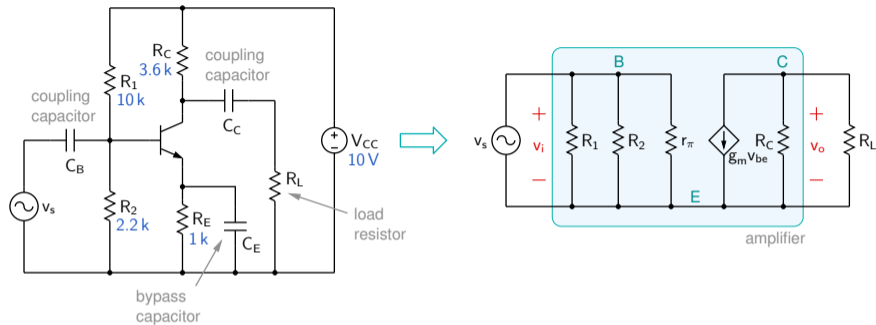
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The input resistance of the amplifier is, by inspection, $R_{in} = (R_1 \parallel R_2) \parallel r_\pi$.

$$r_\pi = \beta/g_m = 100/42.5\text{ m}\ddot{\text{U}} = 2.35\text{ k} \rightarrow R_{in} = 1\text{ k}.$$

Common-emitter amplifier



$$A_V = \frac{v_o}{v_i}, \text{ with } R_L \rightarrow \infty.$$

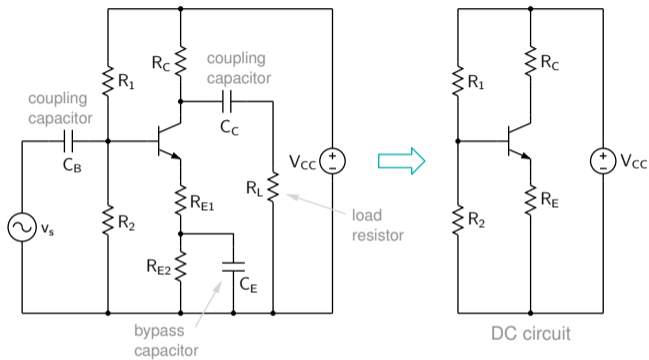
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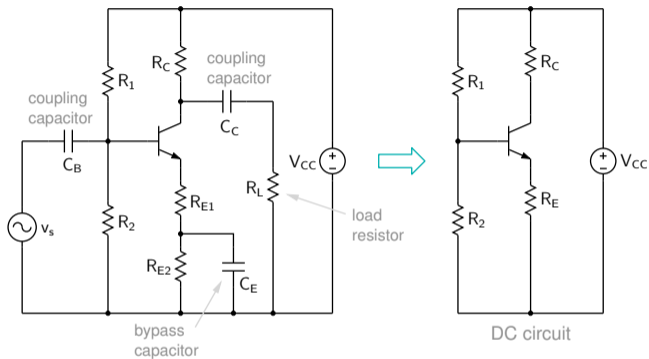
$$r_{\pi} = \beta / g_m = 100 / 42.5 \text{ m}\ddot{\text{U}} = 2.35 \text{ k} \rightarrow R_{in} = 1 \text{ k}.$$

The output resistance is R_C (by "Method 1" seen previously).

Common-emitter amplifier with partial bypass

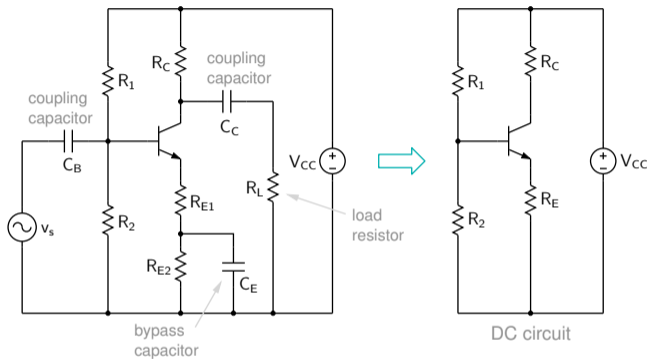


Common-emitter amplifier with partial bypass



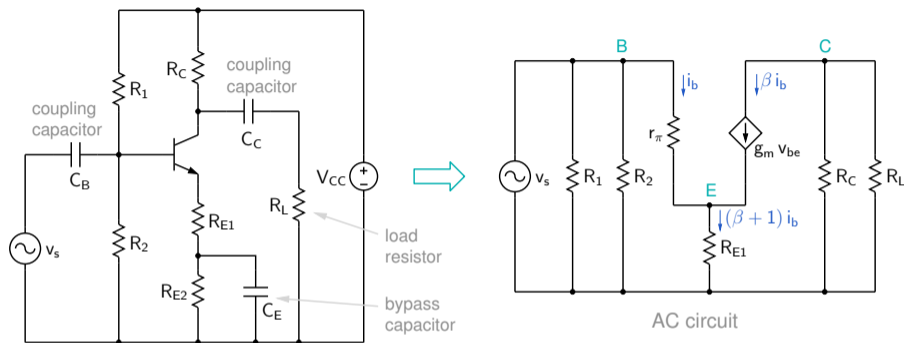
- * For DC computation, C_E is open, and the DC analysis is therefore identical to our earlier amplifier, with $R_E \leftarrow R_{E1} + R_{E2}$.

Common-emitter amplifier with partial bypass



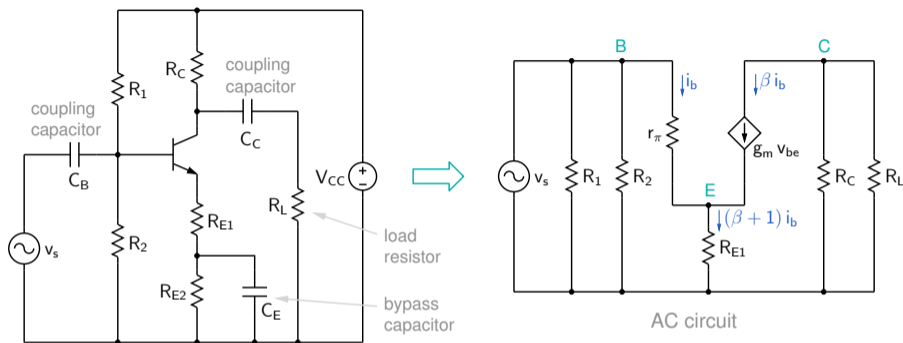
- * For DC computation, C_E is open, and the DC analysis is therefore identical to our earlier amplifier, with $R_E \leftarrow R_{E1} + R_{E2}$.
- * Bypassing a part of R_E (as opposed to all of it) does have an impact on the voltage gain (see next slide).

Common-emitter amplifier with partial bypass



Again, assume that, at the frequency of operation, C_B , C_C , C_E can be replaced by short circuits, and the BJT parasitic capacitances by open circuits.

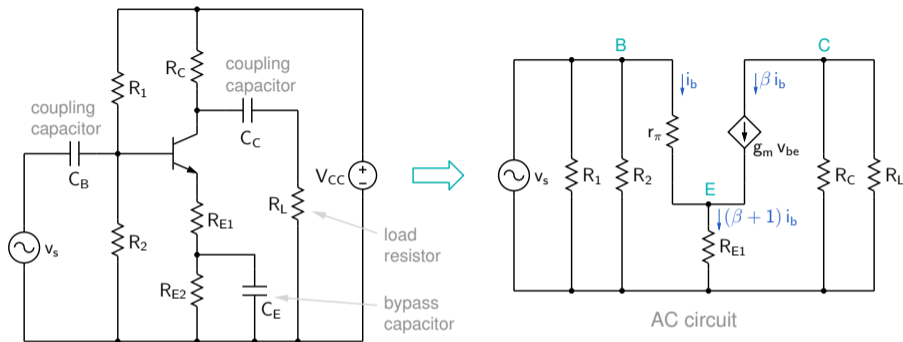
Common-emitter amplifier with partial bypass



Again, assume that, at the frequency of operation, C_B , C_C , C_E can be replaced by short circuits, and the BJT parasitic capacitances by open circuits.

$$v_s = i_b r_\pi + (\beta + 1) i_b R_{E1} \rightarrow i_b = \frac{v_s}{r_\pi + (\beta + 1) R_{E1}}.$$

Common-emitter amplifier with partial bypass

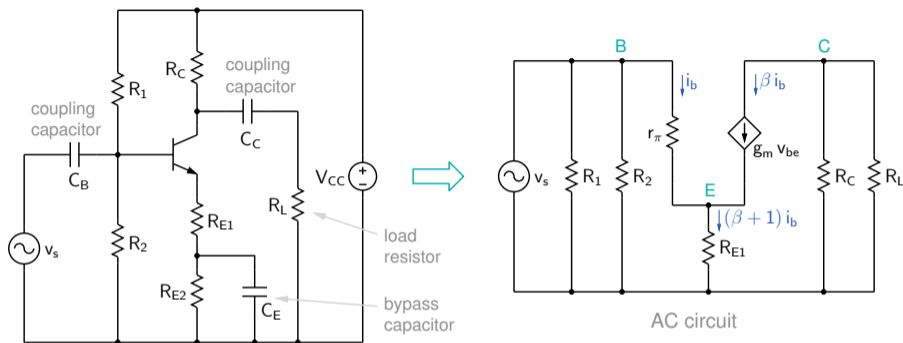


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$$v_s = i_b r_\pi + (\beta + 1) i_b R_{E1} \rightarrow i_b = \frac{v_s}{r_\pi + (\beta + 1) R_{E1}}$$

$$v_o = -\beta i_b \times (R_C \parallel R_L) \rightarrow \frac{v_o}{v_s} = -\frac{\beta (R_C \parallel R_L)}{r_\pi + (\beta + 1) R_{E1}} \approx -\frac{(R_C \parallel R_L)}{R_{E1}} \text{ if } r_\pi \ll (\beta + 1) R_{E1}.$$

Common-emitter amplifier with partial bypass



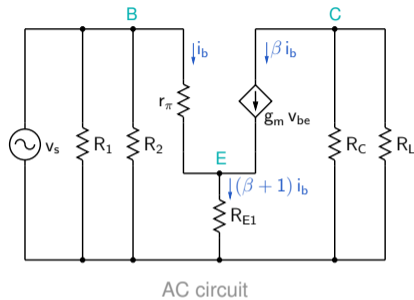
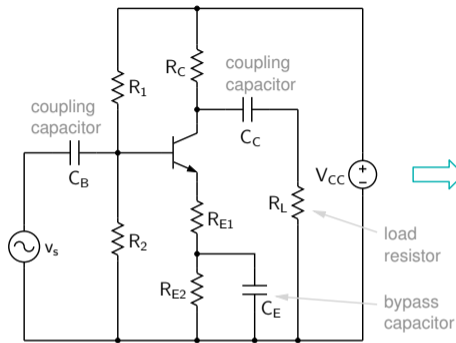
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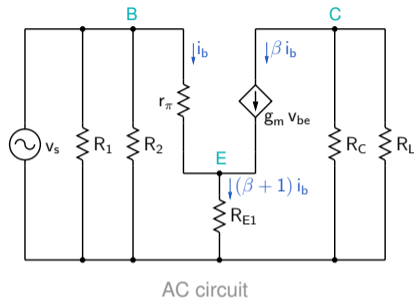
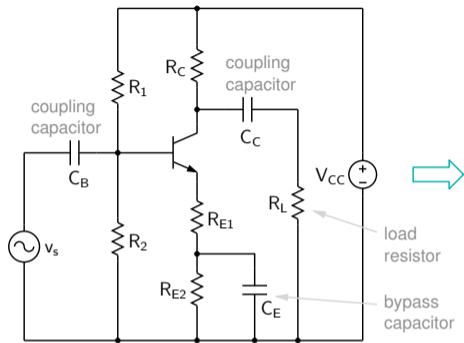
Note: R_{E1} gets multiplied by $(\beta + 1)$.

Common-emitter amplifier with partial bypass



$$\frac{v_{be}}{v_s} = \frac{r_\pi i_b}{r_\pi i_b + R_E (\beta + 1) i_b} = \frac{r_\pi}{r_\pi + R_E (\beta + 1)}$$

Common-emitter amplifier with partial bypass

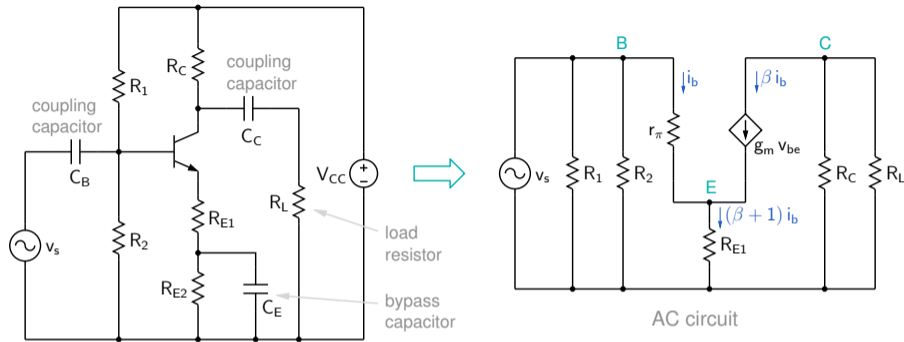


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The small-signal condition, viz., $|v_{be}(t)| \ll V_T$ now implies

$$|v_s| \frac{r_\pi}{r_\pi + R_E (\beta + 1)} \ll V_T \text{ or } |v_s| \ll V_T \times \frac{r_\pi + R_E (\beta + 1)}{r_\pi}, \text{ which is much larger than } V_T.$$

Common-emitter amplifier with partial bypass



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→ Although the gain is reduced, partial emitter bypass allows larger input voltages to be applied without causing distortion in $v_o(t)$. (For comparison, we required $|v_s| \ll V_T$ for the CE amplifier.)