

SEMICONDUCTOR DEVICES

Bipolar Junction Transistors: Part 2



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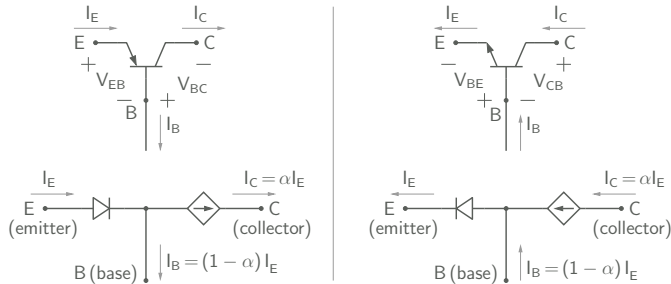
Department of Electrical Engineering
Indian Institute of Technology Bombay

- * We have considered a BJT in the active mode (B-E junction under forward bias, B-C junction under reverse bias) and obtained α .

Bipolar junction transistors: Ebers-Moll model

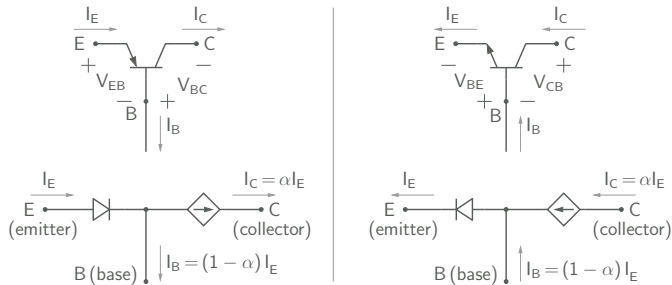
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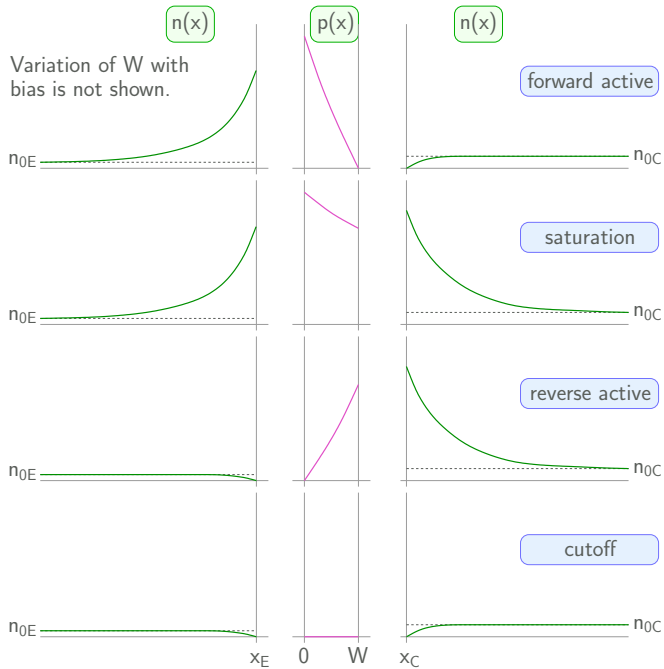
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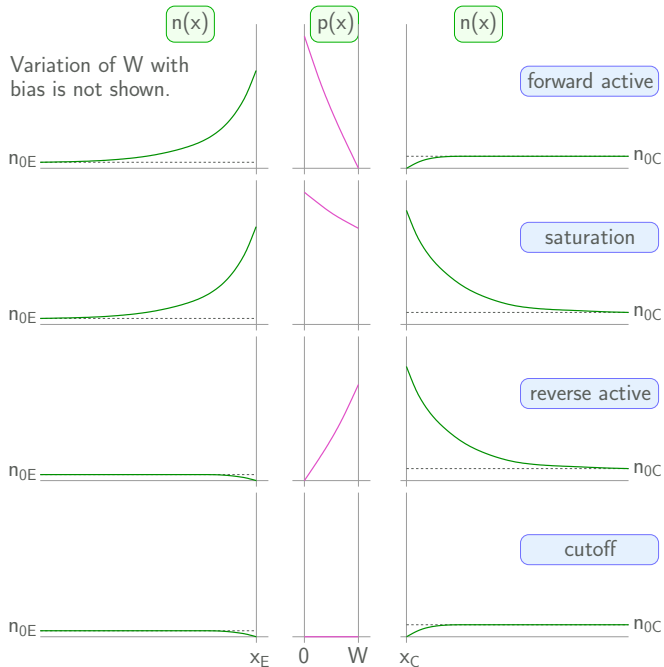
- * A generalised model valid in all modes can be obtained by removing the conditions of a forward bias across the E - B junction and a reverse bias across the C - B junction \rightarrow Ebers-Moll model.

Ebers-Moll model:
Outline of derivation for a *pnp* BJT



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Outline of derivation for a *pnp* BJT

* Boundary conditions:

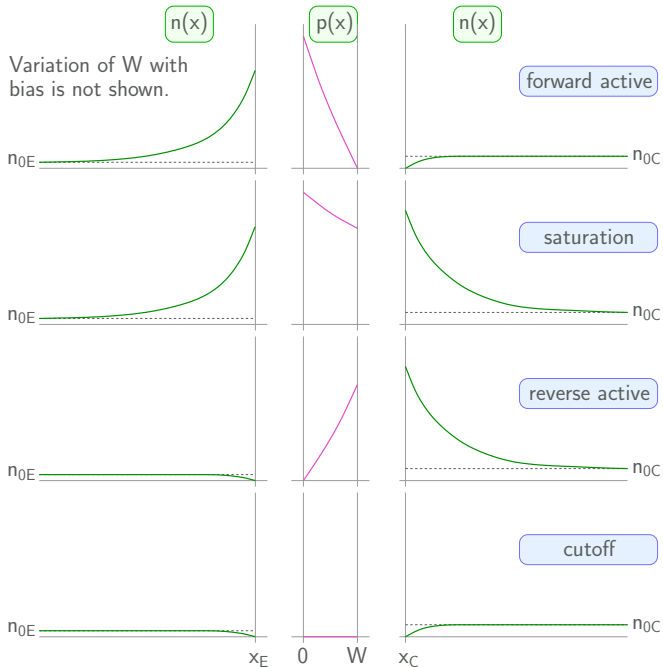


Ebers-Moll model:
Outline of derivation for a *pnp* BJT

* Boundary conditions:

$$\Delta n(x_E) = n_{0E} \left[\exp\left(\frac{V_{EB}}{V_T}\right) - 1 \right]$$

$$\Delta n(-\infty) = 0$$



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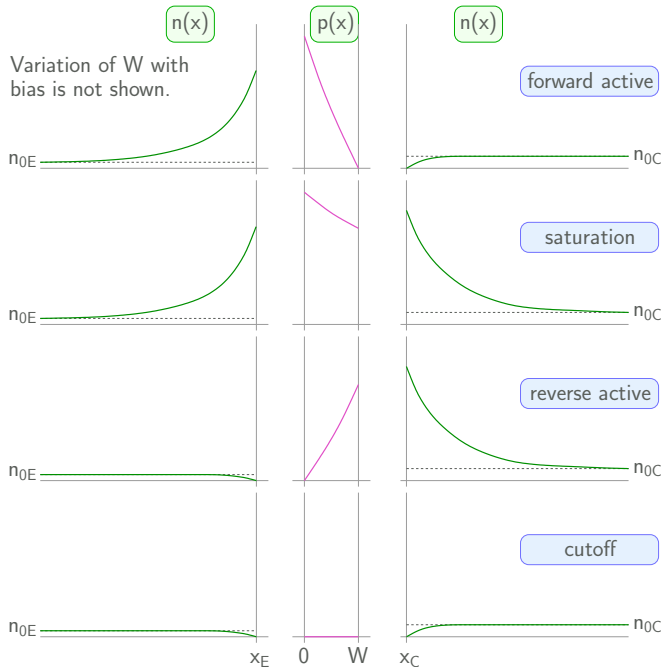
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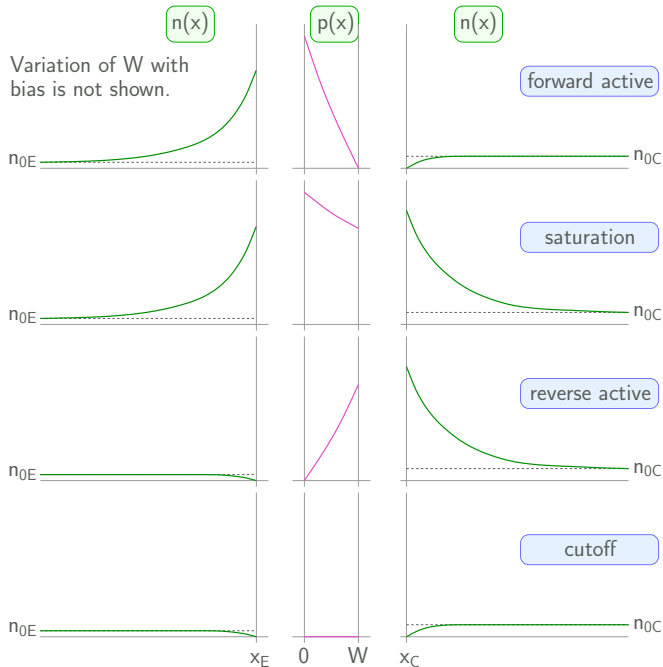
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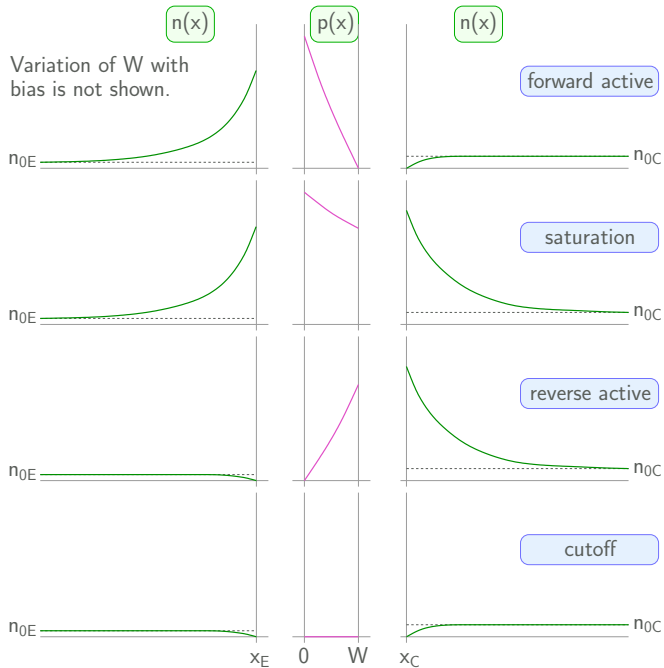
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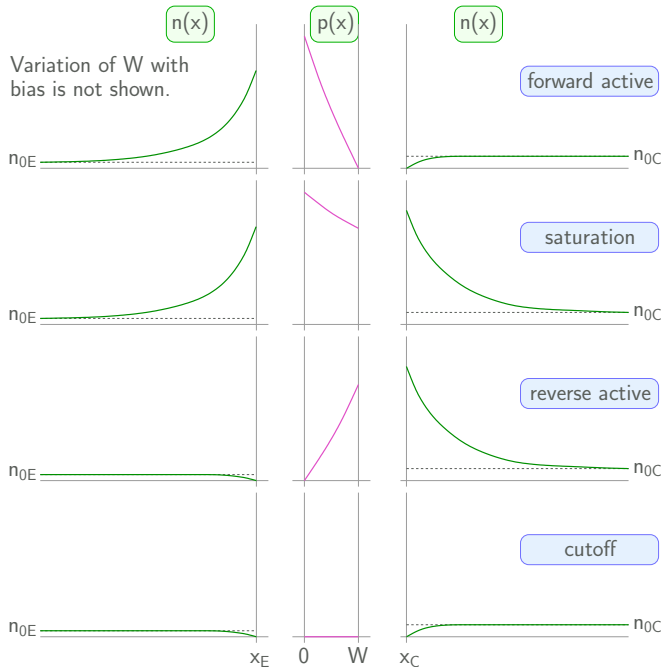


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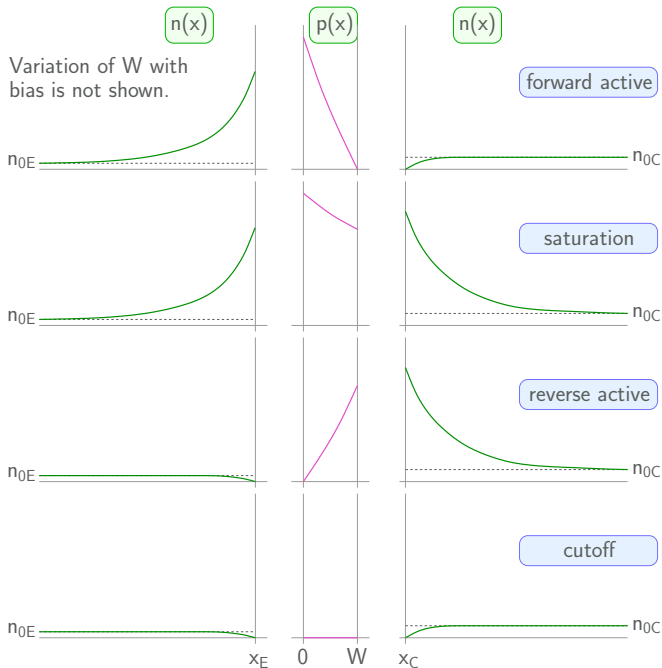
* From the solutions, obtain the following currents.

$$I_{nE}(x_E) = qAD_{nE} \frac{dn}{dx}(x_E).$$

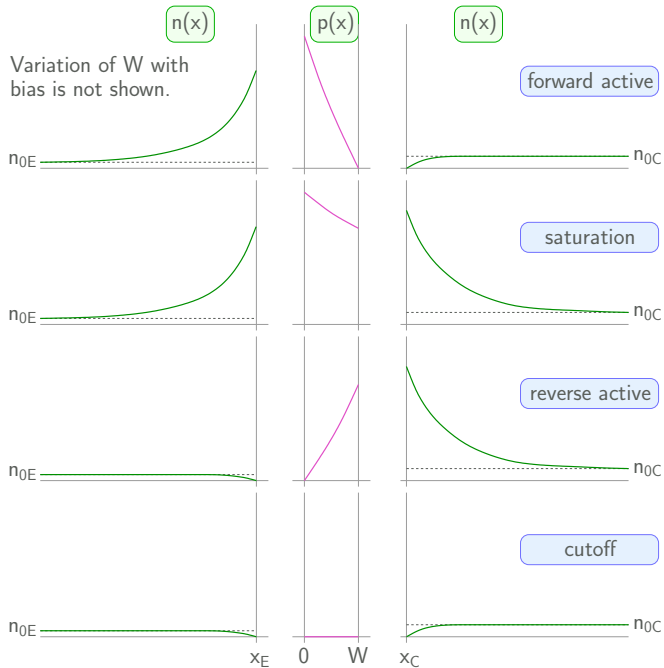
$$I_{pB}(0) = -qAD_{pB} \frac{dp}{dx}(0).$$

$$I_{pB}(W) = -qAD_{pB} \frac{dp}{dx}(W).$$

$$I_{nC}(x_C) = qAD_{nC} \frac{dn}{dx}(x_C).$$



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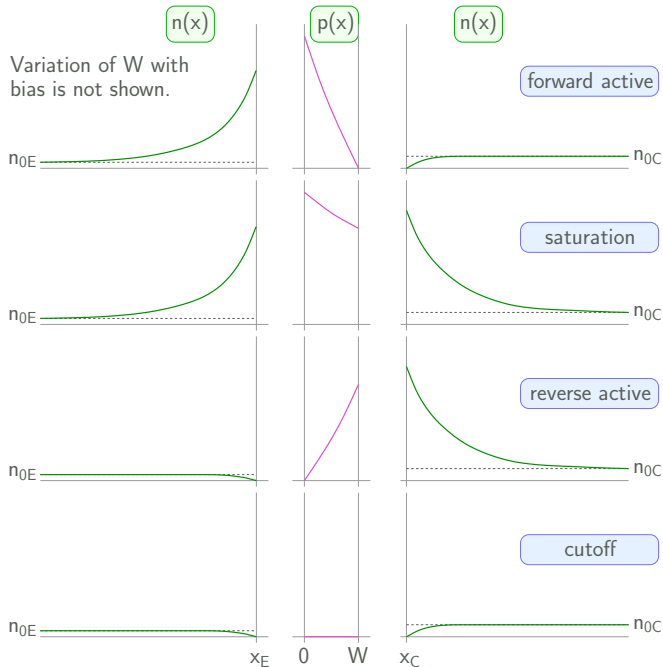
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- * Obtain the terminal currents, ignoring G-R in the depletion regions.

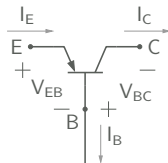
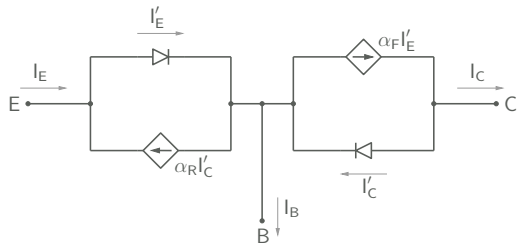
$$I_E = I_{nE}(x_E) + I_{pB}(0).$$

$$I_C = I_{nC}(x_C) + I_{pB}(W).$$

$$I_B = I_E - I_C.$$



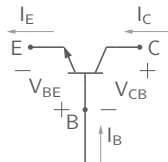
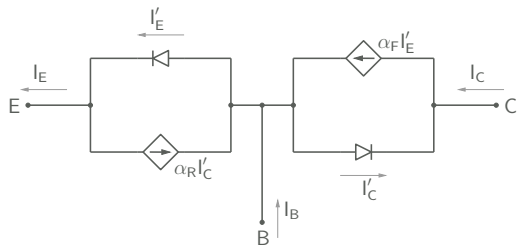
Bipolar junction transistors: Ebers-Moll model



pnp transistor

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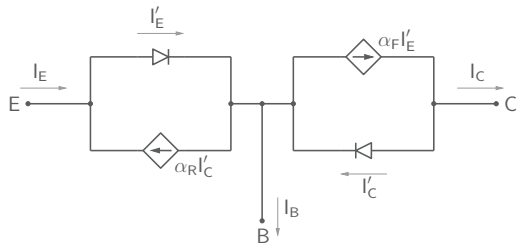


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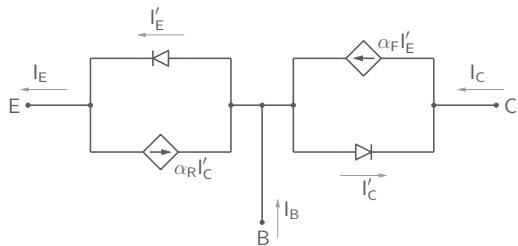
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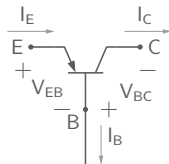
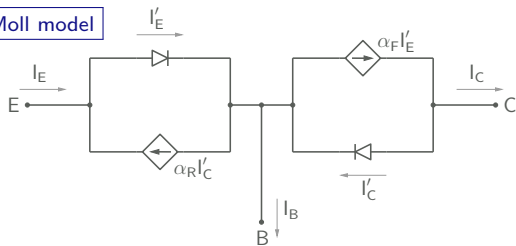
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* Current directions are assigned such that I_C , I_E , I_B are all positive if the BJT operates in the active mode.

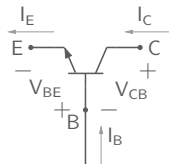
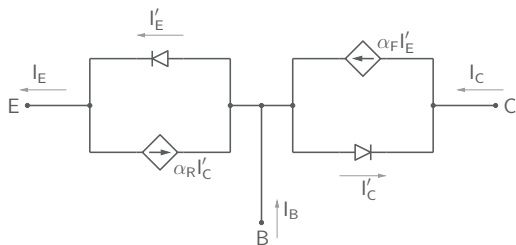
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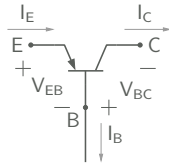
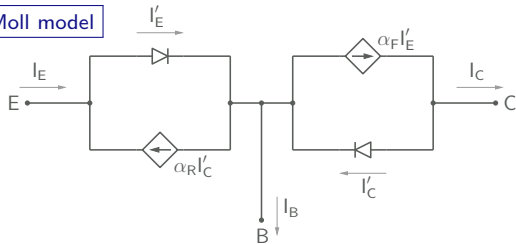


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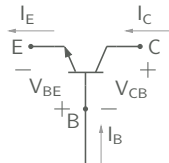
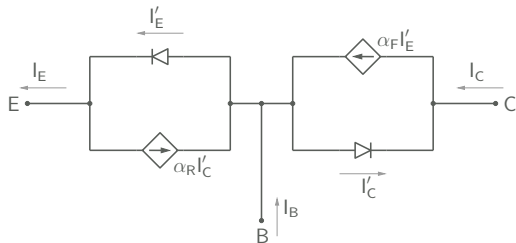
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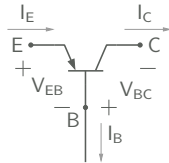
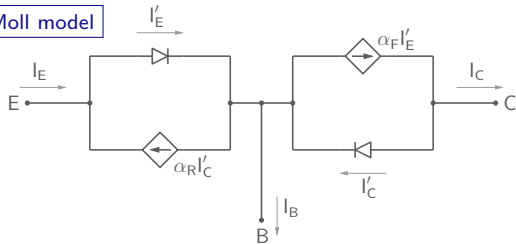
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- * To construct the Ebers-Moll model, we draw two transistor models: one in the forward active mode, the other in the reverse active mode, and connect them in parallel.

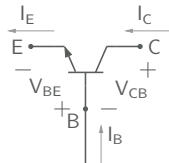
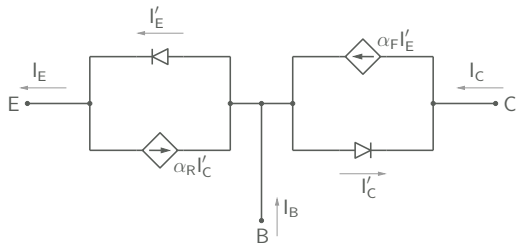
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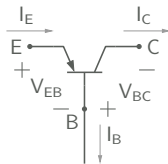
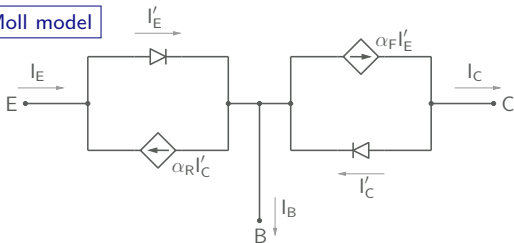
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- * To construct the Ebers-Moll model, we draw two transistor models: one in the forward active mode, the other in the reverse active mode, and connect them in parallel.
- * The forward transistor is represented by the E - B diode and the corresponding dependent source (the upper branches), and the reverse transistor by the C - B diode and the corresponding dependent source (the lower branches).

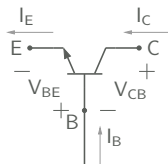
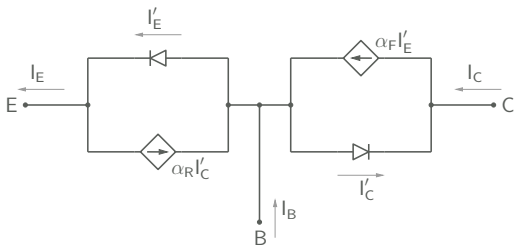
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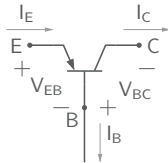
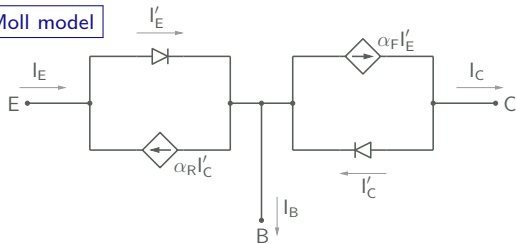


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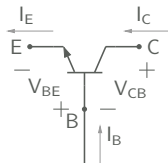
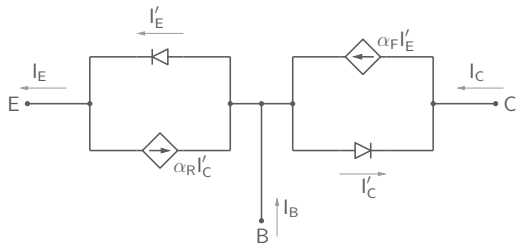
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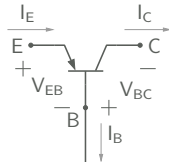
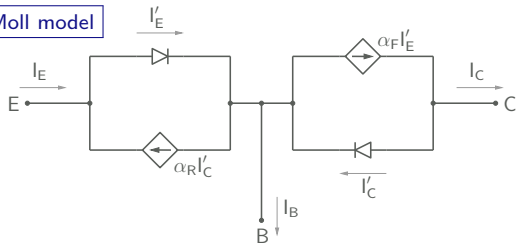
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- * The model has four parameters: I_{ES} , I_{CS} , α_F , α_R (F for forward, R for reverse) which can be related to the geometry (base width, device area) and material parameters (doping densities, diffusion coefficients, lifetimes) of the transistor.¹

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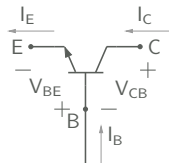
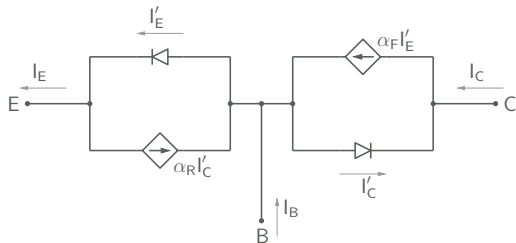
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- * With the assumptions we have made, $\alpha_F I_{ES} = \alpha_R I_{CS}$.

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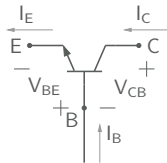
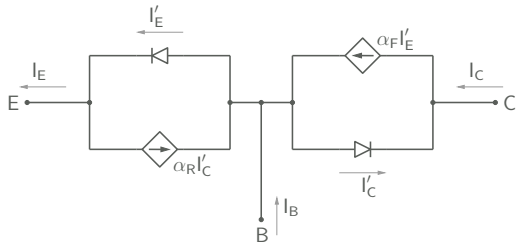
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- * The Ebers-Moll model can still be used as a “phenomenological” description of the device if model parameters are suitably extracted using measured data.
- * More advanced BJT models are available (e.g., the SPICE model²) and are used for circuit simulation.

²P. Antognetti and G. Massobrio, *Semiconductor Device Modeling with SPICE*. New York: McGraw-Hill, 1988.

Ebers-Moll model: special cases



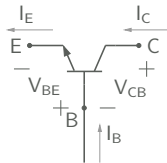
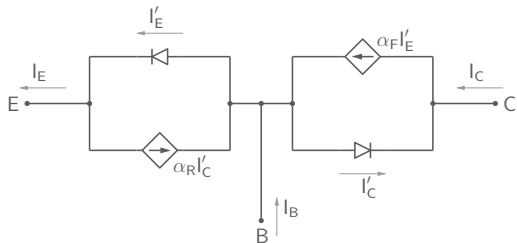
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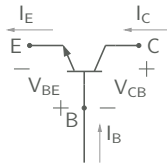
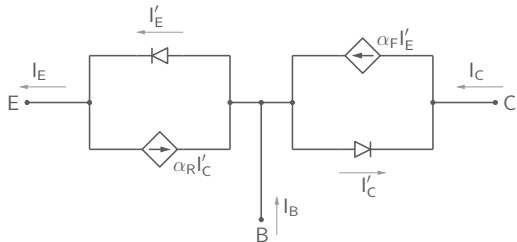
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$$\begin{aligned} I_C &= -I'_C + \alpha_F I'_E \\ &= -I'_C + \alpha_F (I_E + \alpha_R I'_C) \\ &= -I'_C (1 - \alpha_F \alpha_R) + \alpha_F I_E. \end{aligned}$$

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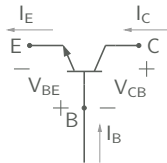
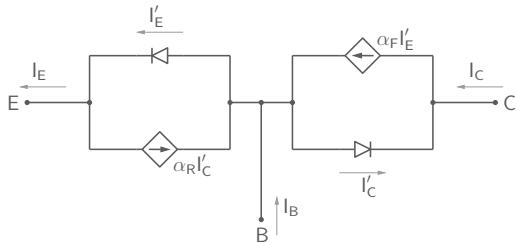
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$$I'_C = I_{CS} \left[\exp \left(\frac{V_{BC}}{V_T} \right) - 1 \right]$$

* $I_E = 0$, i.e., emitter open-circuited.

$$\begin{aligned} I_C &= -I'_C + \alpha_F I'_E \\ &= -I'_C + \alpha_F (I_E + \alpha_R I'_C) \\ &= -I'_C (1 - \alpha_F \alpha_R) + \alpha_F I_E. \end{aligned}$$

When the C - B junction is under reverse bias, $I'_C \approx -I_{CS}$, and with $I_E = 0$, we get



npn transistor

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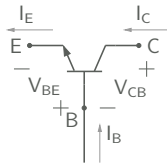
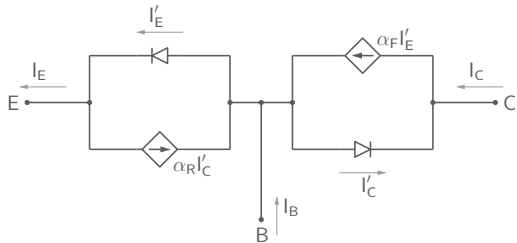
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$$I_C \equiv I_{CBO} = I_{CS} (1 - \alpha_F \alpha_R).$$

Ebers-Moll model: special cases



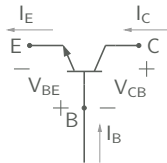
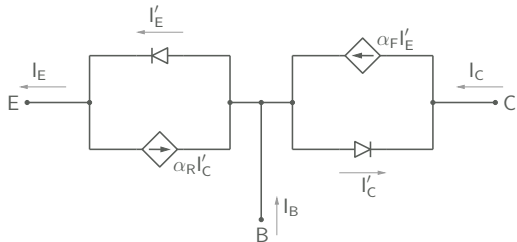
npn transistor

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Ebers-Moll model: special cases



npn transistor

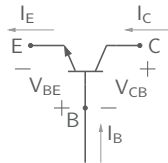
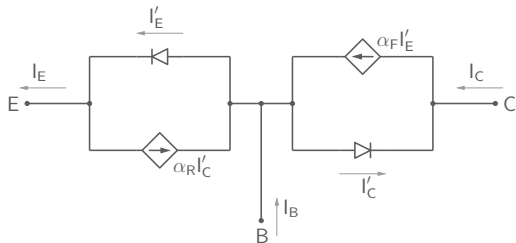
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$$I_C = -I'_C(1 - \alpha_F \alpha_R) + \alpha_F I_E = -I'_C(1 - \alpha_F \alpha_R) + \alpha_F (I_C + I_B)$$

Ebers-Moll model: special cases



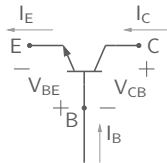
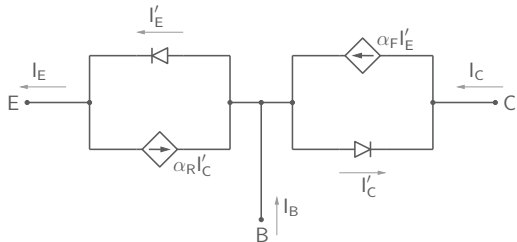
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npn transistor

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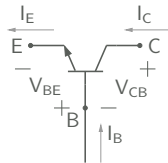
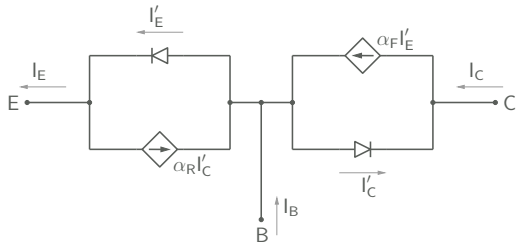
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When the C-B junction is under reverse bias, $I'_C \approx -I_{CS}$, and with $I_B = 0$, we get

$$I_C \equiv I_{CEO} = \frac{I_{CS}(1 - \alpha_F \alpha_R)}{(1 - \alpha_F)} = \frac{I_{CBO}}{(1 - \alpha_F)},$$



npn transistor

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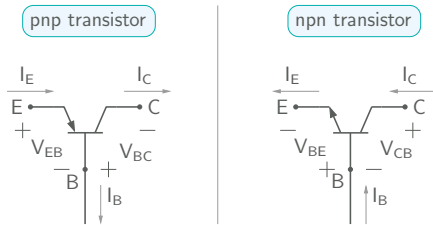
* $I_B = 0$, i.e., base open-circuited.

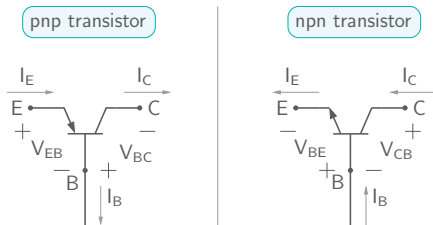
$$\begin{aligned} I_C &= -I'_C(1 - \alpha_F \alpha_R) + \alpha_F I_E = -I'_C(1 - \alpha_F \alpha_R) + \alpha_F (I_C + I_B) \\ &= \frac{-I'_C(1 - \alpha_F \alpha_R)}{1 - \alpha_F} + \frac{\alpha_F}{(1 - \alpha_F)} I_B = \frac{-I'_C(1 - \alpha_F \alpha_R)}{1 - \alpha_F} + \beta_F I_B. \end{aligned}$$

When the C - B junction is under reverse bias, $I'_C \approx -I_{CS}$, and with $I_B = 0$, we get

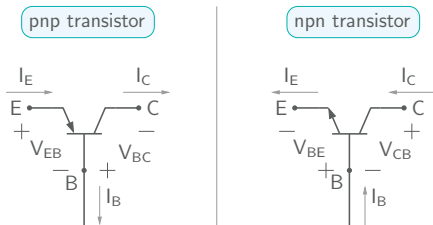
$$I_C \equiv I_{CEO} = \frac{I_{CS}(1 - \alpha_F \alpha_R)}{(1 - \alpha_F)} = \frac{I_{CBO}}{(1 - \alpha_F)},$$

which is much larger than I_{CBO} since α_F is close to 1.

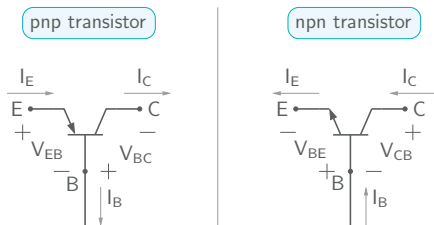




- * Unlike the diode (where there is only one current and one voltage), the BJT has three currents and three voltages.

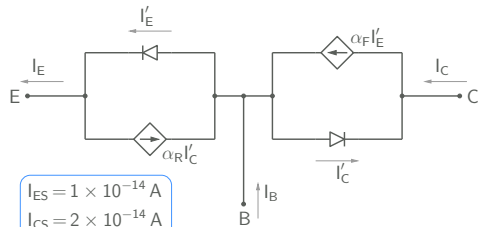


- * Unlike the diode (where there is only one current and one voltage), the BJT has three currents and three voltages.
- * The current-voltage relationship is described in the form of a “family” of curves, with a current selected as the y variable, a voltage as the x variable, and a third variable as a quantity to be held constant for each I - V curve.

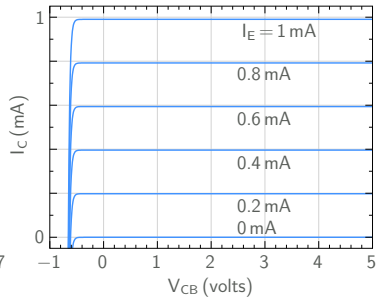
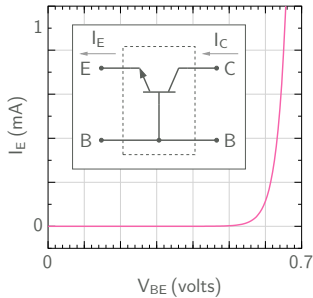


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- * The current-voltage relationship is described in the form of a “family” of curves, with a current selected as the y variable, a voltage as the x variable, and a third variable as a quantity to be held constant for each I - V curve.
- * Two descriptions, which are related to the “common-base” and “common-emitter” configurations, are commonly used.

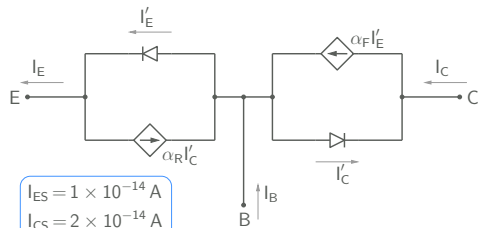
Common-base configuration



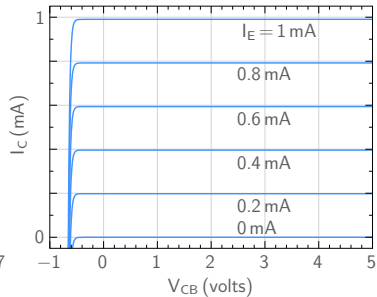
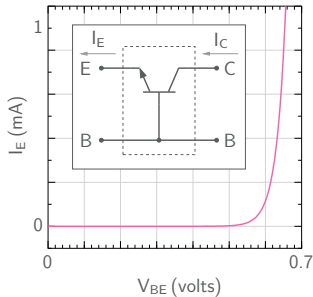
$$\begin{aligned} I_{ES} &= 1 \times 10^{-14} \text{ A} \\ I_{CS} &= 2 \times 10^{-14} \text{ A} \\ \alpha_F &= 0.99 \\ \alpha_R &= 0.5 \end{aligned}$$



Common-base configuration



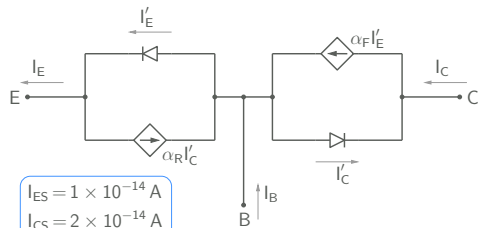
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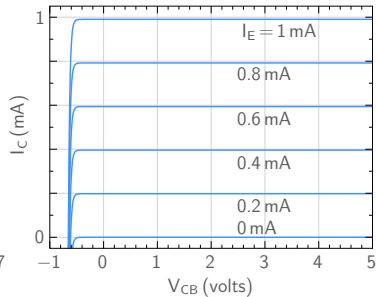
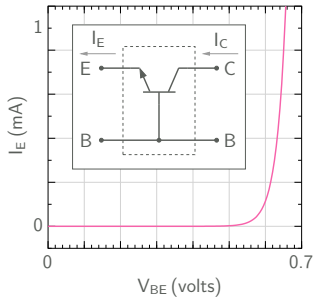
* $V_{CB} > 0V$:

C-B junction is reverse biased, $I'_C \approx -I_{CS}$, which is negligibly small.

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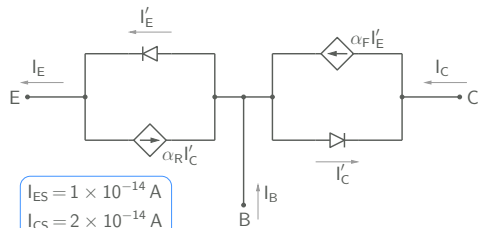


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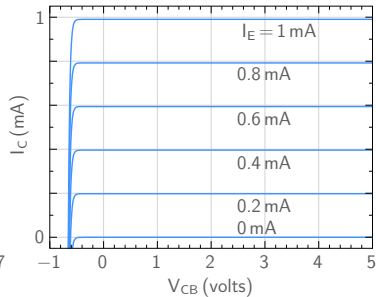
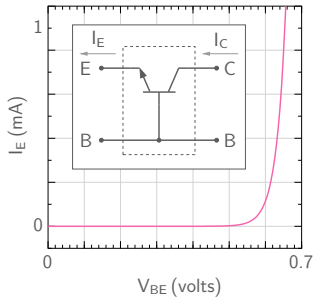
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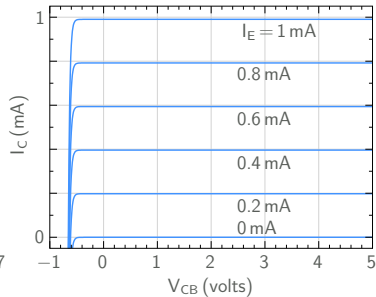
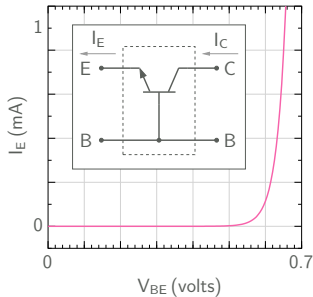
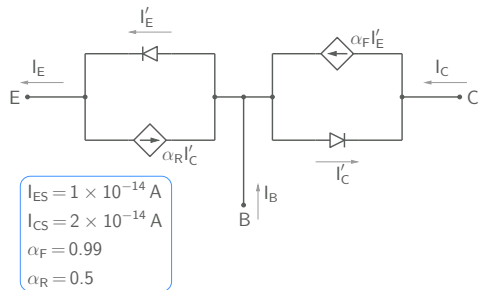
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On the input side, the I_C versus V_{BE} curve (for a positive V_{CB} value) is like a diode I - V relationship.

Common-base configuration



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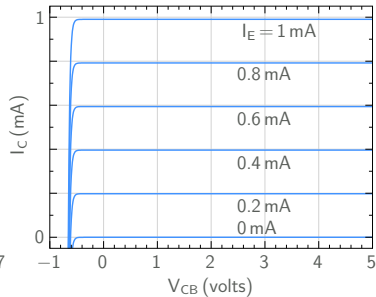
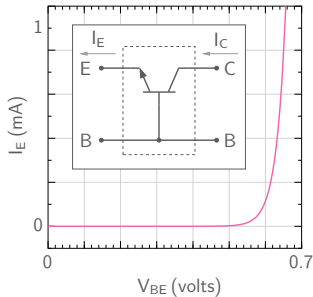
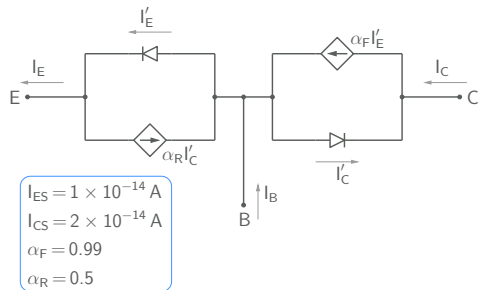
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* $V_{CB} < 0\text{V}$:

C-B junction is forward biased, but I'_C becomes substantial only when $V_{CB} \approx -0.5\text{V}$.

Common-base configuration



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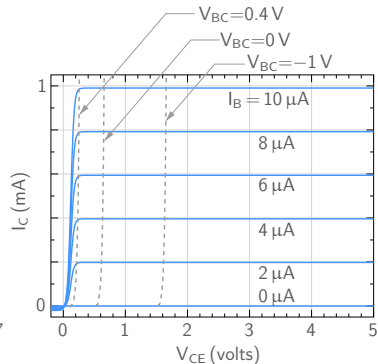
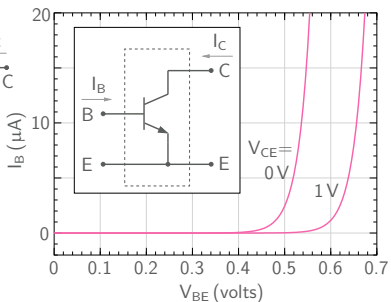
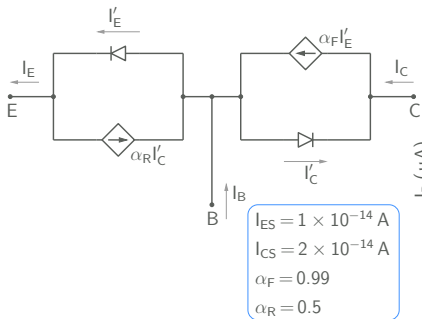
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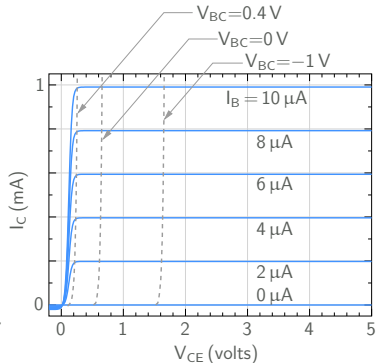
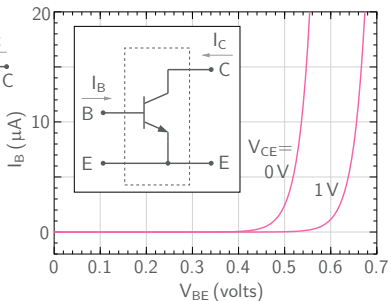
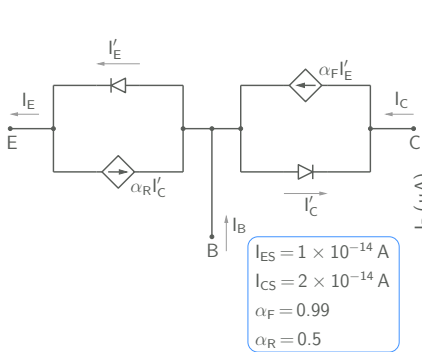
C-B junction is forward biased, but I'_C becomes substantial only when $V_{CB} \approx -0.5 \text{ V}$.

Beyond this point, I_C drops sharply since $I_C = \alpha_F I'_E - I'_C \rightarrow$ saturation mode.

Common-emitter configuration

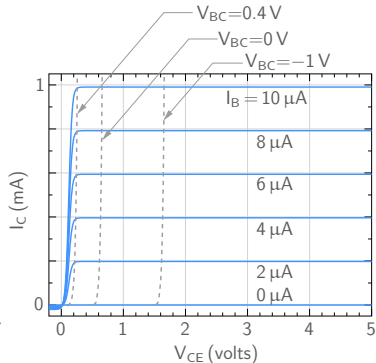
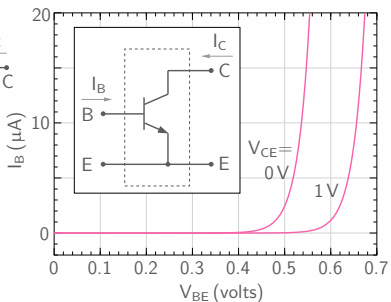
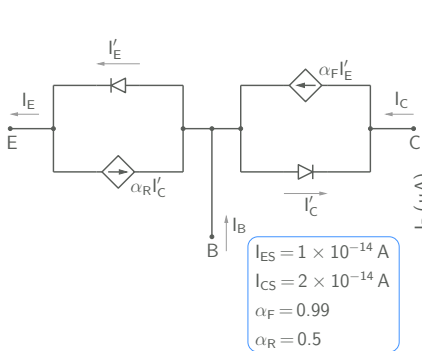


Common-emitter configuration



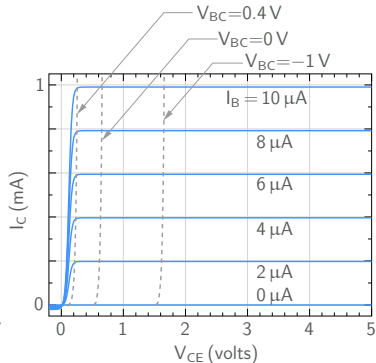
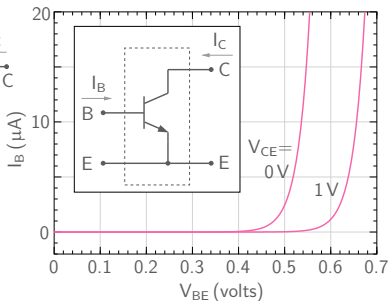
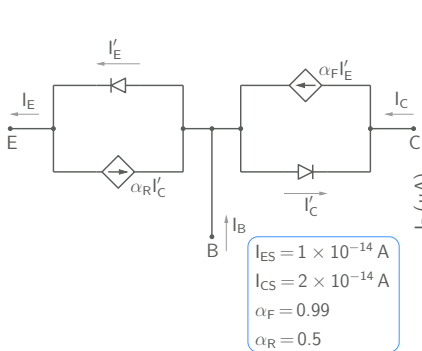
- * In the active region (where I_C is constant for a given I_B), the B-C junction is reverse biased.
 $\rightarrow I'_C \approx 0 \rightarrow I_C = \alpha_F I_E = \beta I_B$, irrespective of V_{CE} .

Common-emitter configuration



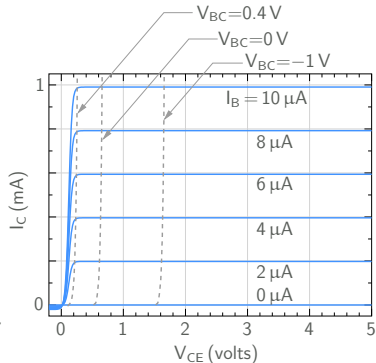
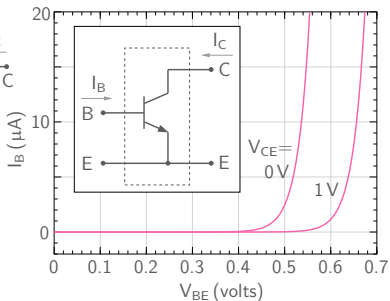
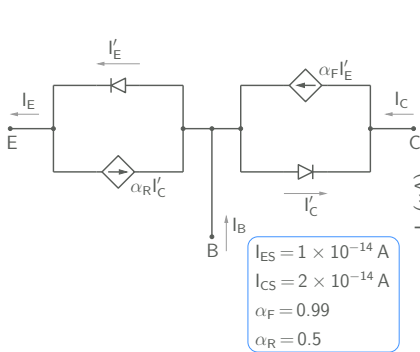
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- * When V_{BC} becomes greater than about 0.4 V, I'_C becomes significant, and $I_C = \alpha_F I'_E - I'_C$ decreases $\rightarrow I_C < \beta I_B$.

Common-emitter configuration



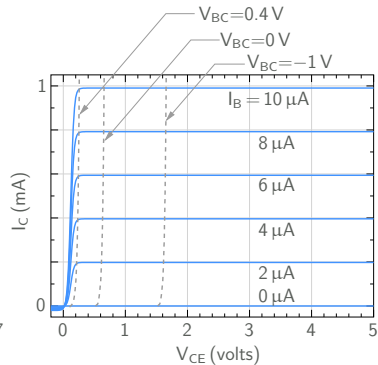
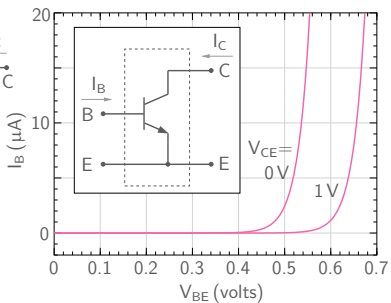
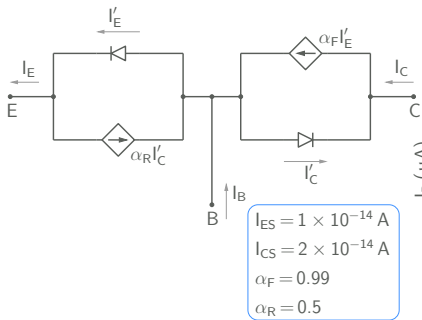
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- * In the active region (e.g., $V_{CE} = 1 \text{ V}$), V_{BE} is nearly constant ($\sim 0.65 \text{ V}$).

Common-emitter configuration

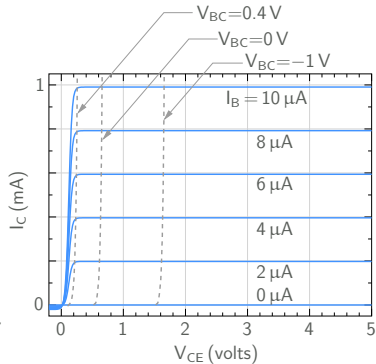
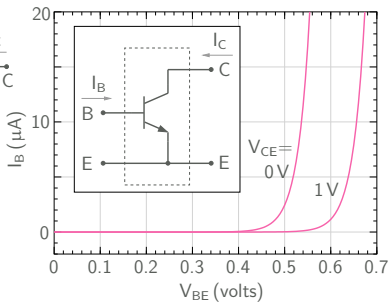
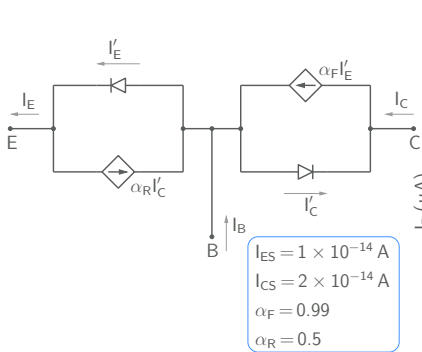


- * In the active region (where I_C is constant for a given I_B), the B-C junction is reverse biased.
 $\rightarrow I'_C \approx 0 \rightarrow I_C = \alpha_F I'_E = \beta I_B$, irrespective of V_{CE} .
- * When V_{BC} becomes greater than about 0.4 V, I'_C becomes significant, and $I_C = \alpha_F I'_E - I'_C$ decreases $\rightarrow I_C < \beta I_B$.
- * In the active region (e.g., $V_{CE} = 1 \text{ V}$), V_{BE} is nearly constant ($\sim 0.65 \text{ V}$).
- * In the saturation region, V_{CE} is 0.2 V or smaller. This is generally true for all low-power BJTs.

Common-emitter configuration

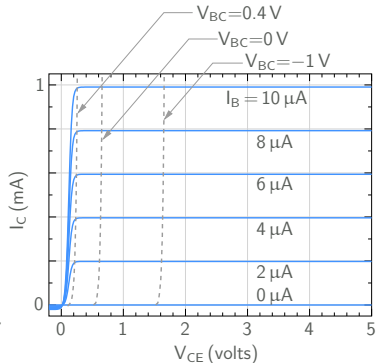
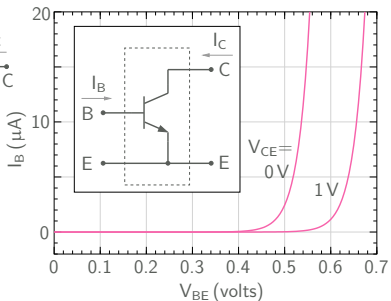
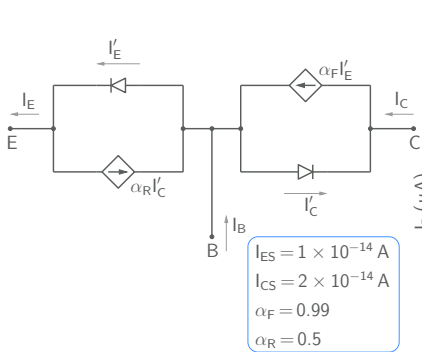


Common-emitter configuration



* Comparison of I_B versus V_{BE} for $V_{CE} = 0\text{V}$ and $V_{CE} = 1\text{V}$:

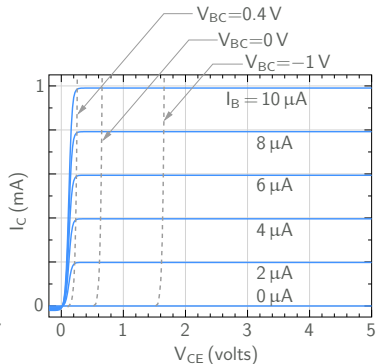
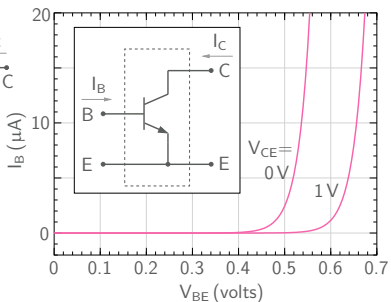
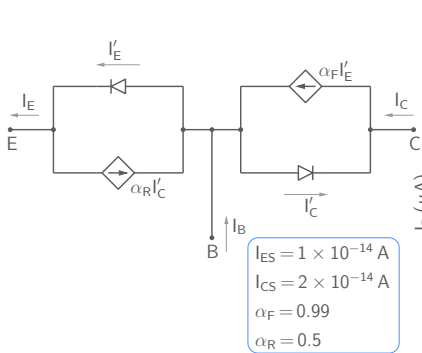
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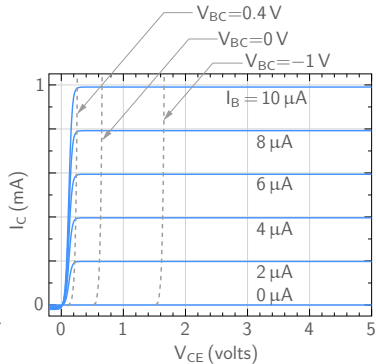
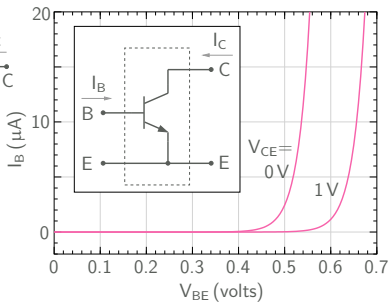
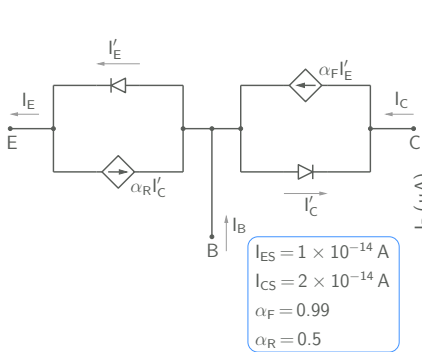


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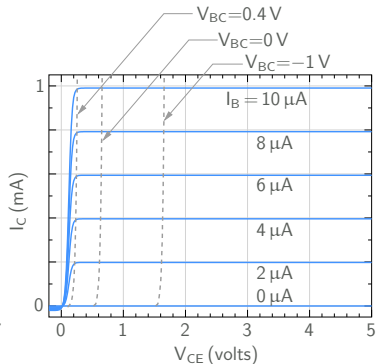
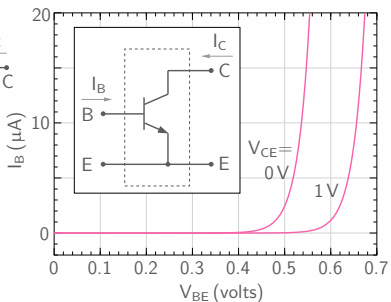
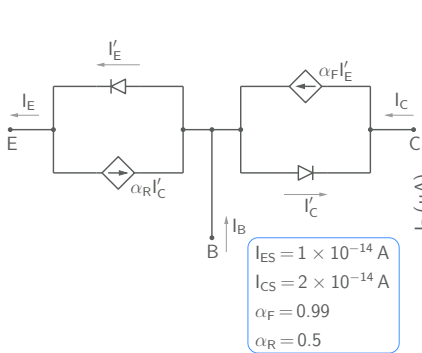


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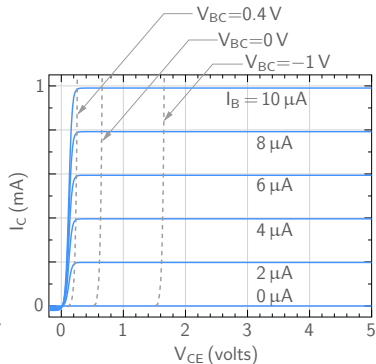
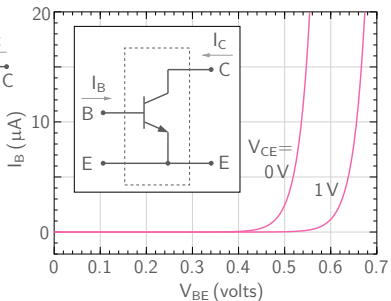
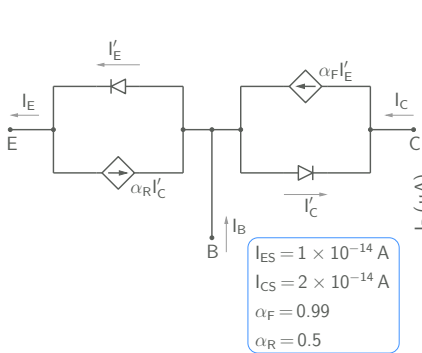


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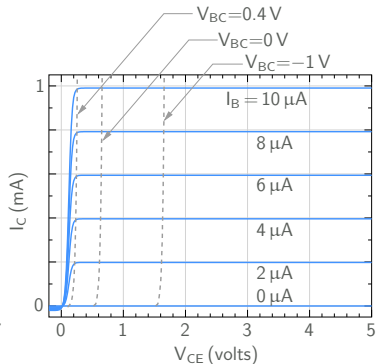
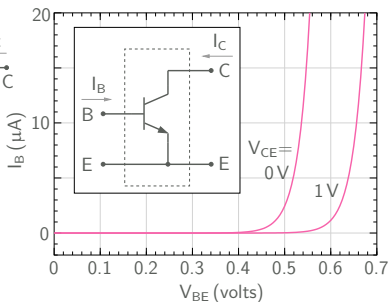
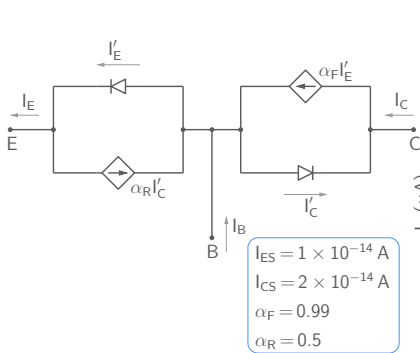


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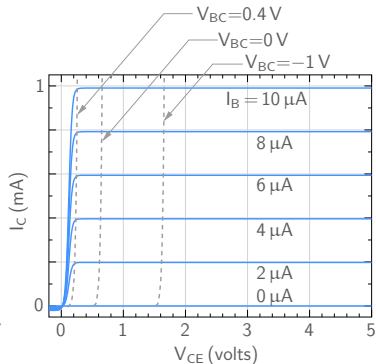
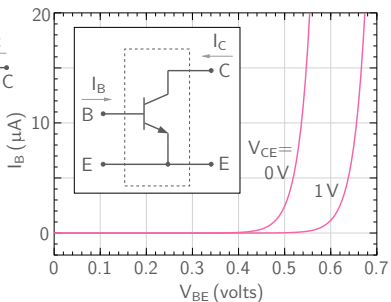
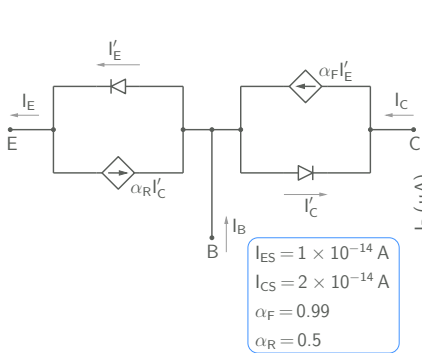
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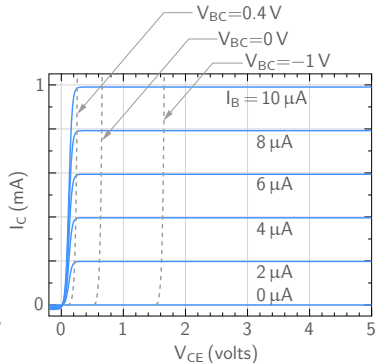
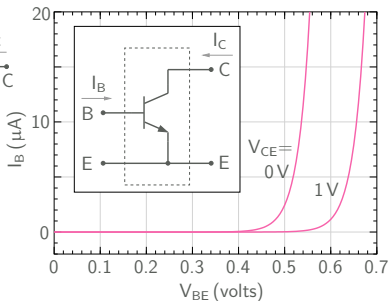
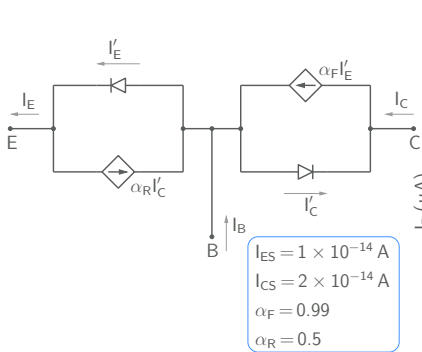
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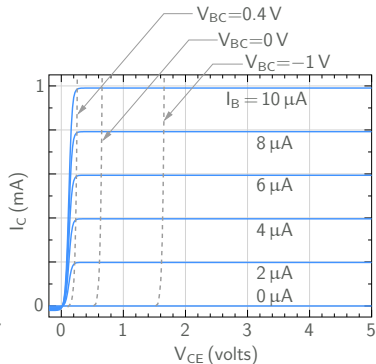
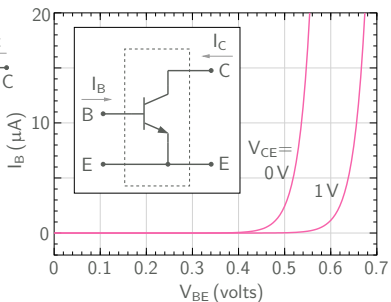
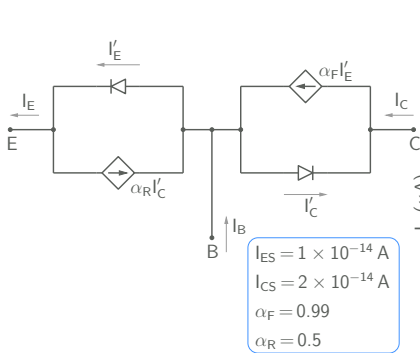
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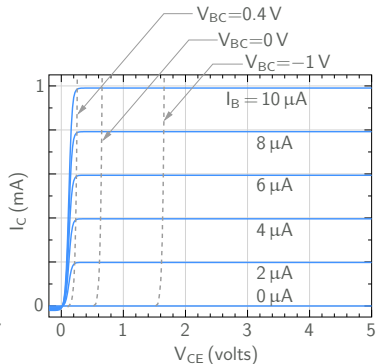
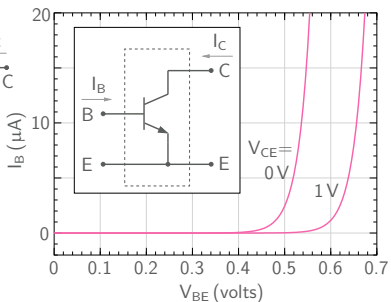
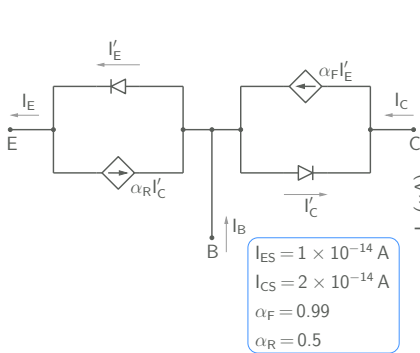
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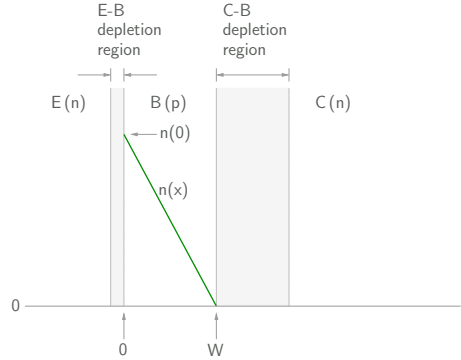
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- * We will consider
 - base width modulation
 - breakdown phenomena

Base width modulation

We have assumed so far that the width of the neutral base region (in the active mode) is independent of V_{BE} and V_{BC} . This is a reasonable assumption for the following reasons.

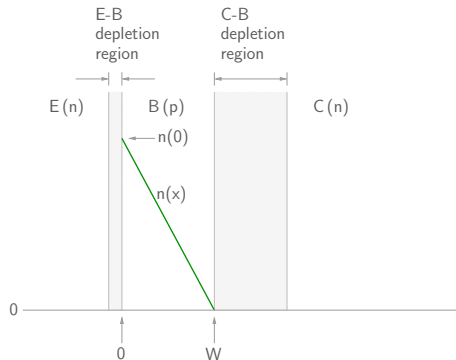


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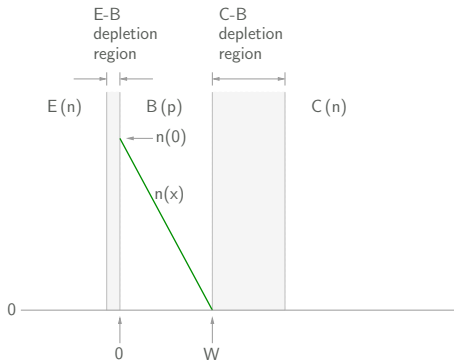
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However, the change occurs mostly on the collector side since $N_a(B) \gg N_d(C)$.



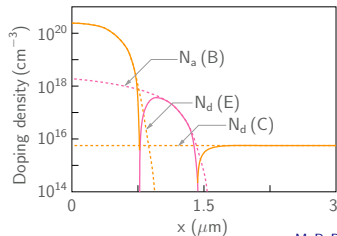
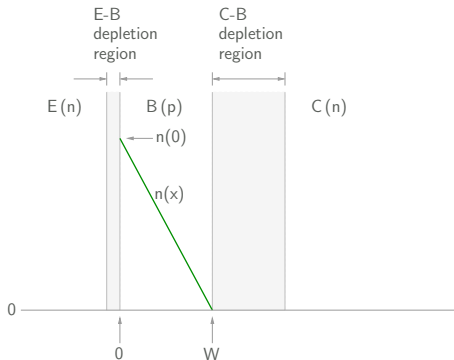
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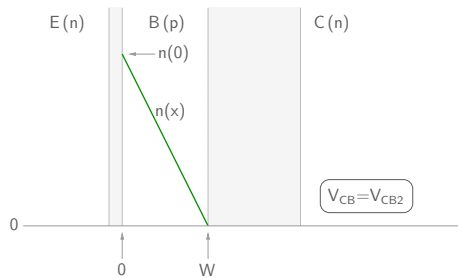
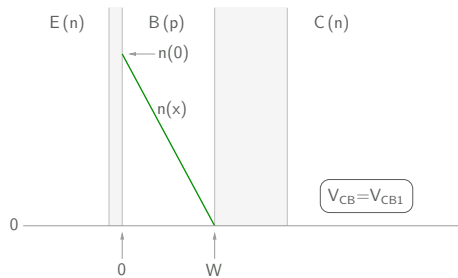
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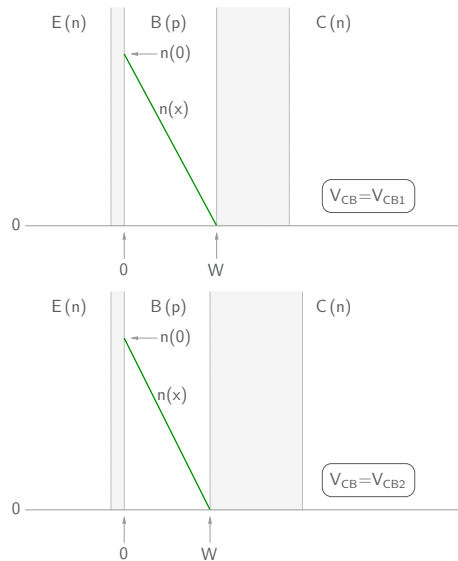


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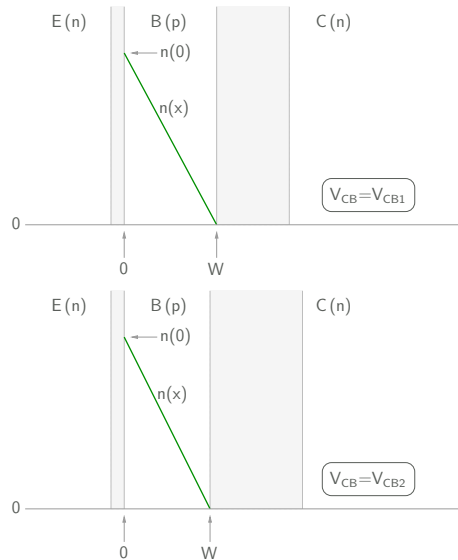
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Base width modulation

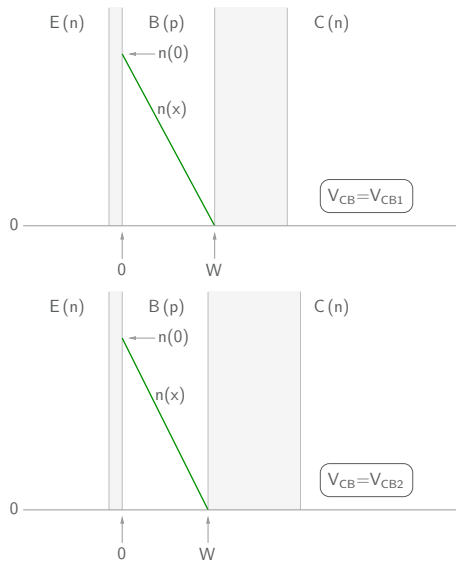
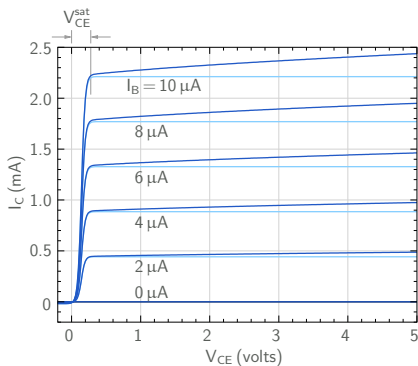
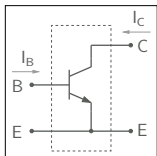
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 $(V_{CE} \uparrow \rightarrow V_{CB} (= V_{CE} - V_{BE}) \uparrow \rightarrow W \downarrow \rightarrow I_C \uparrow)$



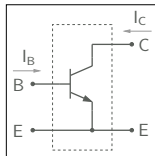
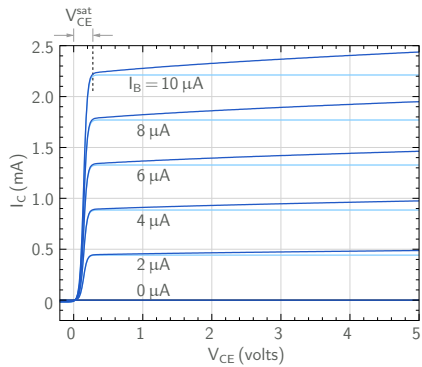
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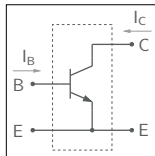
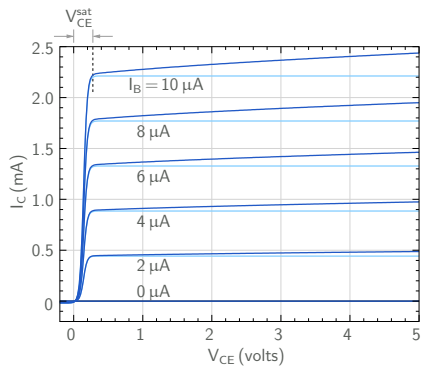
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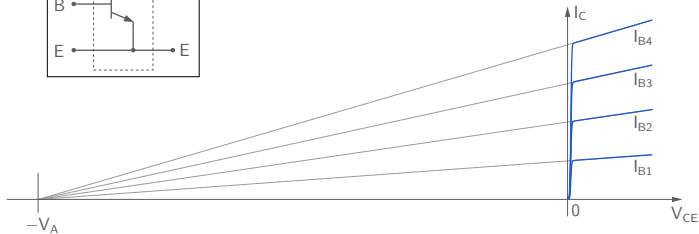
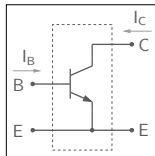
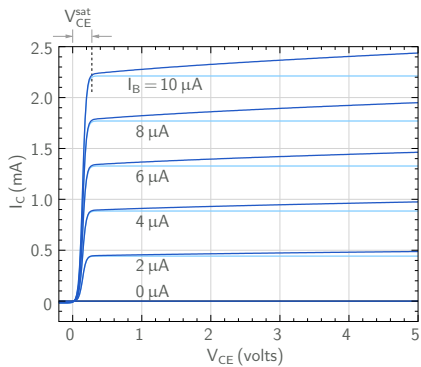


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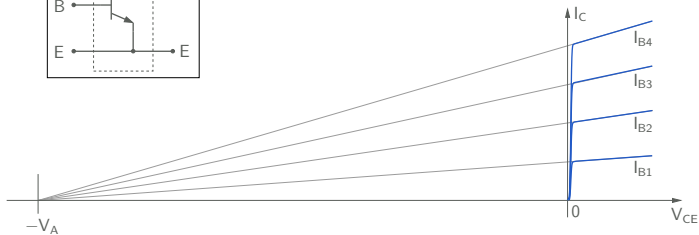
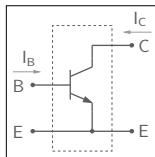
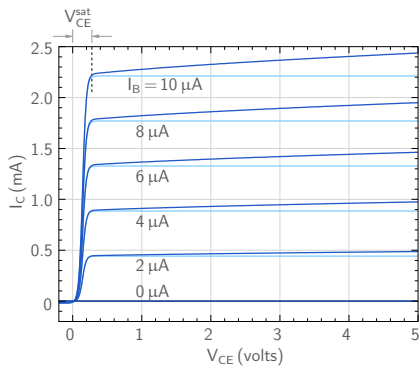
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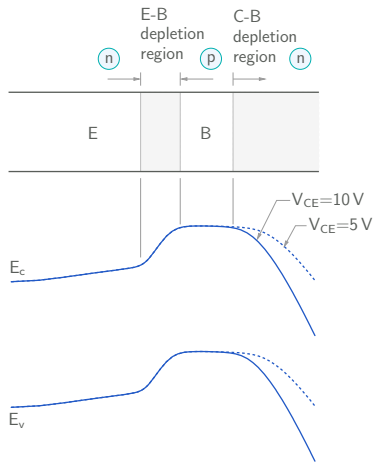
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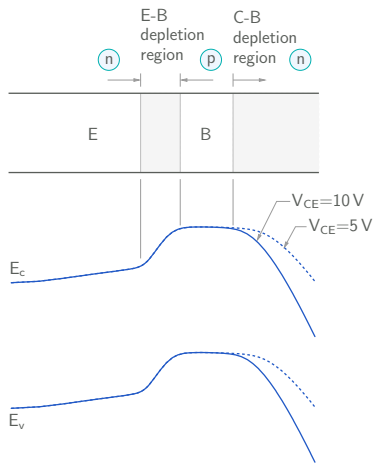
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- * We will look at two breakdown mechanisms:
 - punchthrough
 - avalanche breakdown

Punchthrough



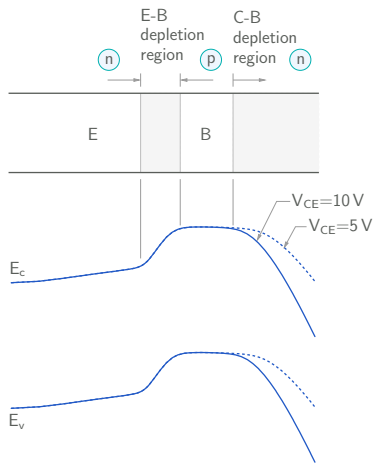
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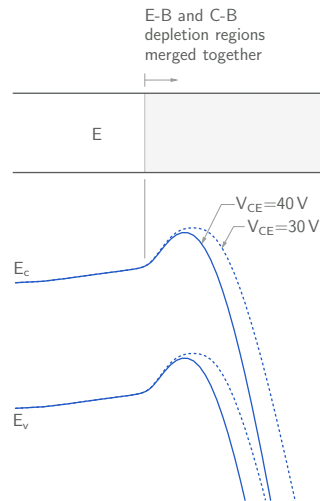
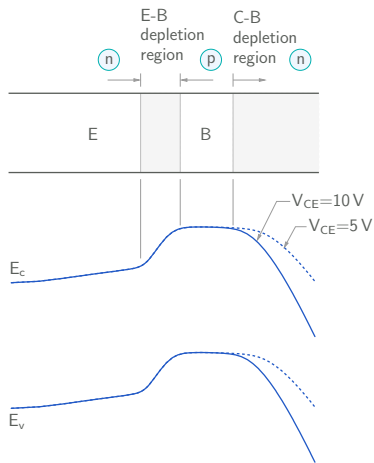
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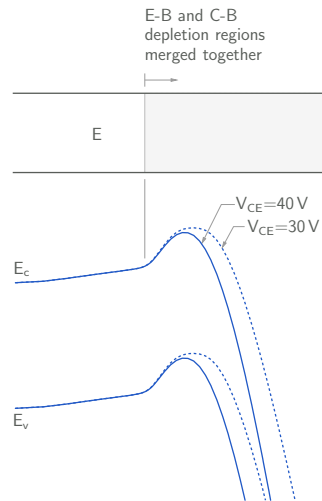
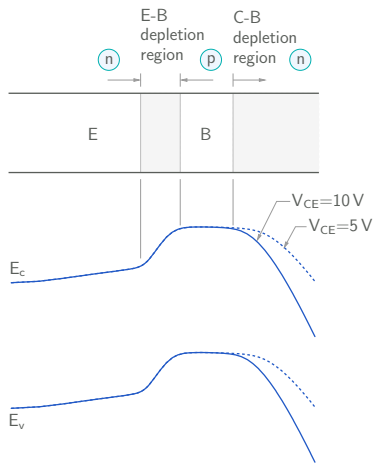
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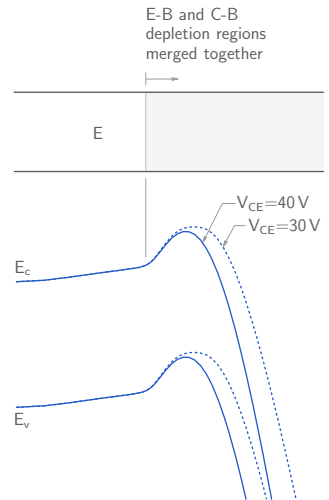
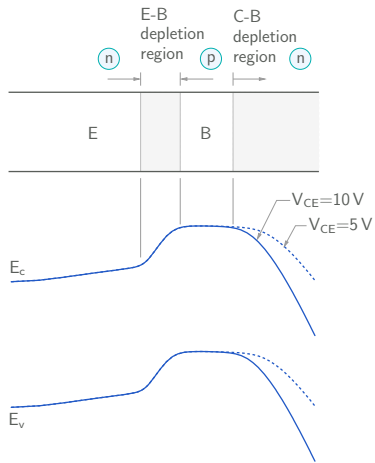


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(The band bending in the emitter region is due to non-uniform doping in the simulated structure.)

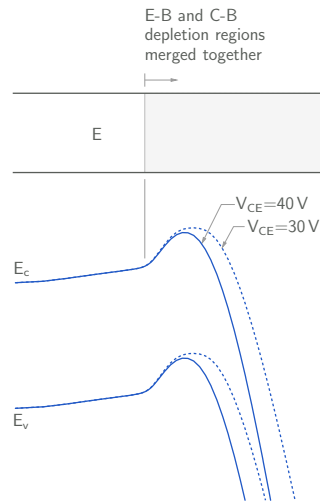
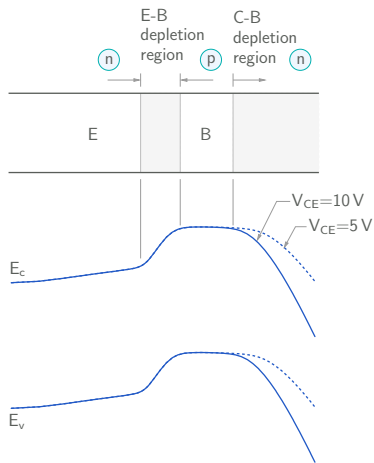


Punchthrough



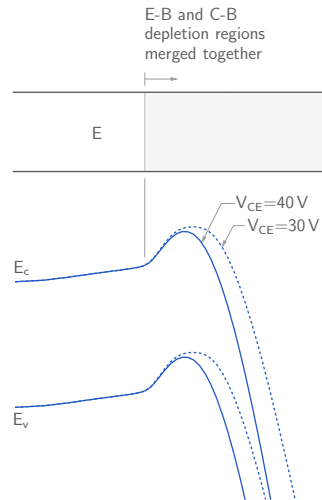
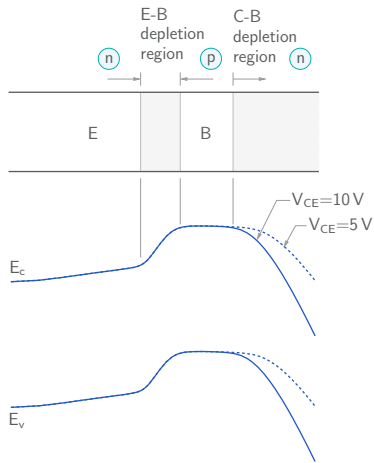
Punchthrough

- * Prior to punchthrough, an increase in the C-B reverse bias only affects the bands in the base and collector regions, leaving the E-B barrier (for electron flow) unchanged.

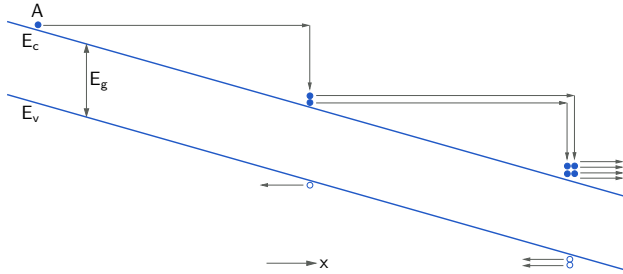


Punchthrough

- * Prior to punchthrough, an increase in the C-B reverse bias only affects the bands in the base and collector regions, leaving the E-B barrier (for electron flow) unchanged.
- * After punchthrough, any further increase in V_{CB} lowers the E-B potential barrier. The number of electrons injected from the emitter increases dramatically. They get swept away toward the collector, resulting in a large collector current.

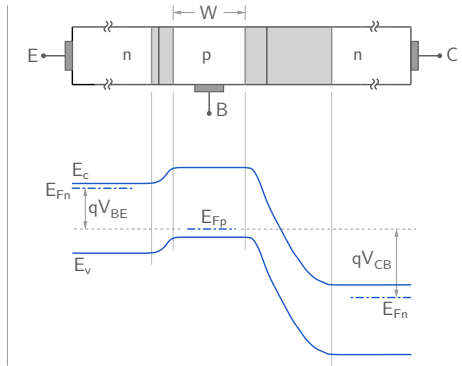
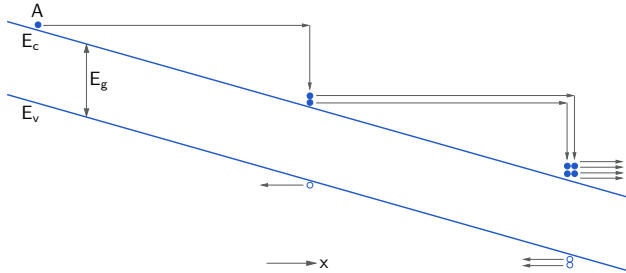


Avalanche breakdown



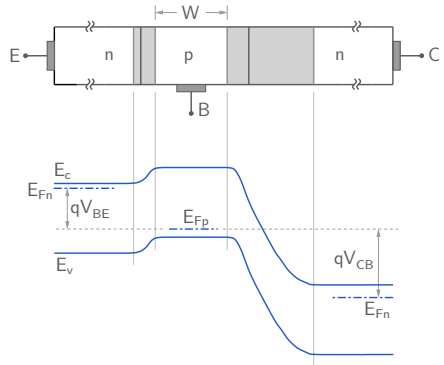
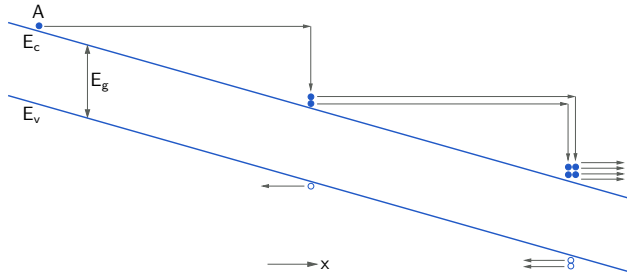
- * Avalanche multiplication because of impact ionisation can take place in a semiconductor if the electric field is high (\sim critical field \mathcal{E}_c).

Avalanche breakdown



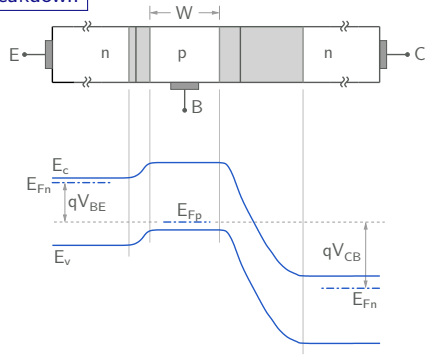
- * Avalanche multiplication because of impact ionisation can take place in a semiconductor if the electric field is high (\sim critical field \mathcal{E}_c).

Avalanche breakdown



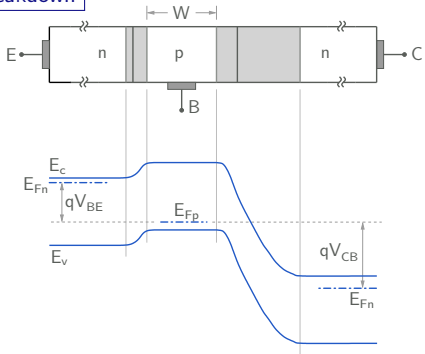
- * Avalanche multiplication because of impact ionisation can take place in a semiconductor if the electric field is high (\sim critical field \mathcal{E}_c).
- * In a BJT operating in the active mode, the C-B junction is under reverse bias. If the reverse voltage is sufficiently large, avalanche breakdown can take place.

Avalanche breakdown



* The avalanche multiplication process is characterised by a multiplication factor M .

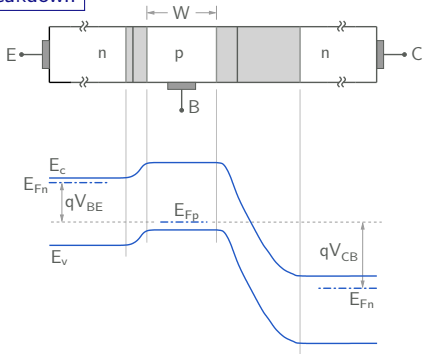
Avalanche breakdown



- * The avalanche multiplication process is characterised by a multiplication factor M .
- * Let $I_0 =$ current through the high-field region without multiplication
 $I =$ current through the high-field region with multiplication

$$\text{Then, } M = \frac{I}{I_0}.$$

Avalanche breakdown



* The avalanche multiplication process is characterised by a multiplication factor M .

* Let I_0 = current through the high-field region without multiplication

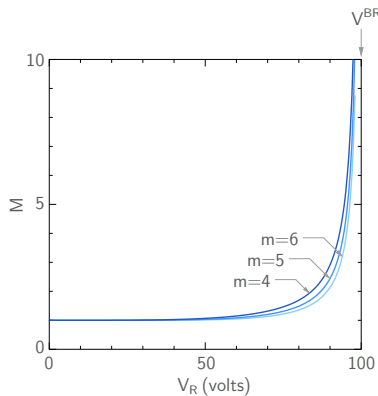
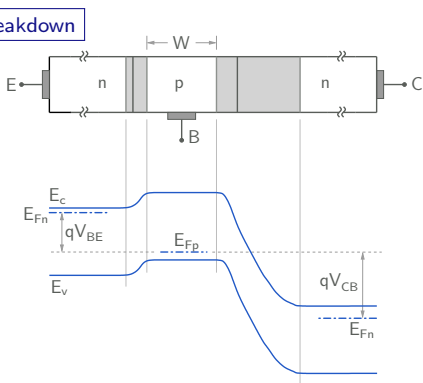
I = current through the high-field region with multiplication

$$\text{Then, } M = \frac{I}{I_0}.$$

* Empirically, it is observed that $M = \frac{1}{1 - \left(\frac{V_R}{V^{BR}}\right)^m}$, where $3 < m < 6$ (depending on the semiconductor),

V_R is the reverse bias, and V^{BR} is the breakdown voltage.

Avalanche breakdown



* The avalanche multiplication process is characterised by a multiplication factor M .

* Let I_0 = current through the high-field region without multiplication

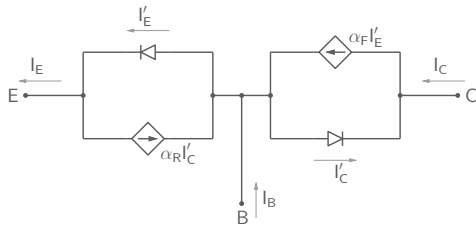
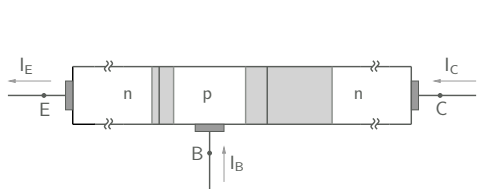
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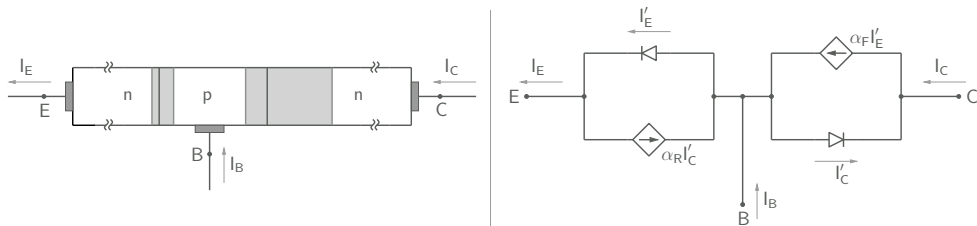
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Avalanche breakdown

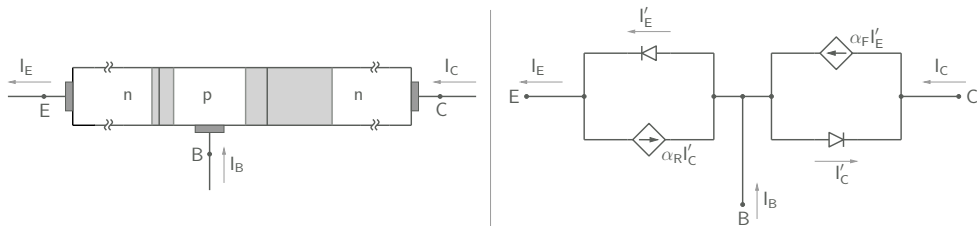


Avalanche breakdown



* Collector current without multiplication is $\alpha_F I'_E - I'_C$.

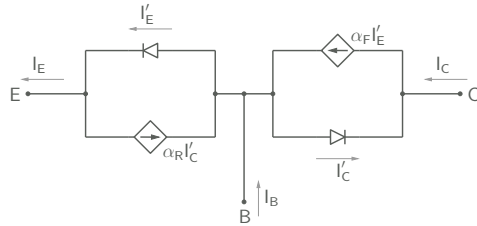
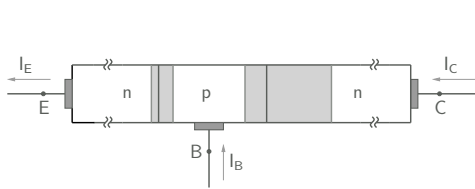
Avalanche breakdown



- * Collector current without multiplication is $\alpha_F I'_E - I'_C$.
- * Collector current with multiplication is $M (\alpha_F I'_E - I'_C)$, i.e.,

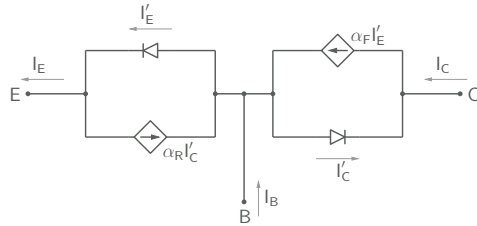
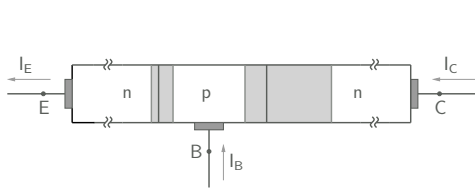
$$\begin{aligned} I_C &= M (\alpha_F I'_E + I_{CS}) \quad \because I'_C \approx -I_{CS} \\ &= M [\alpha_F (I_E + \alpha_R I'_C) + I_{CS}] \\ &= M \alpha_F I_E + M I_{CS} (1 - \alpha_F \alpha_R) \\ &= M \alpha_F I_E + M I_{CBO}. \end{aligned}$$

Avalanche breakdown



$$I_C = M \alpha_F I_E + M I_{CBO}$$

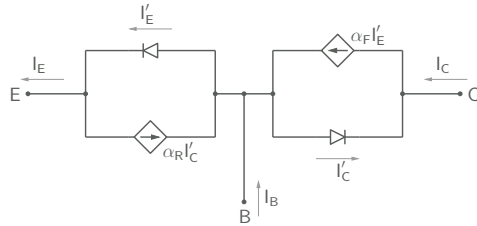
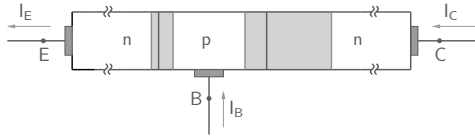
Avalanche breakdown



$$I_C = M \alpha_F I_E + M I_{CBO}$$

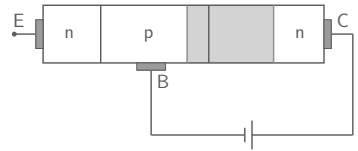
* Emitter open:

Avalanche breakdown

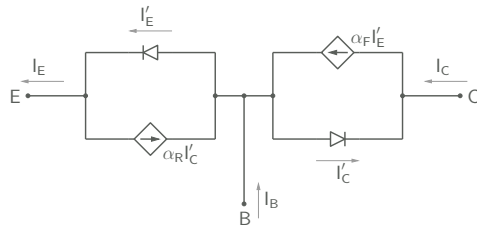
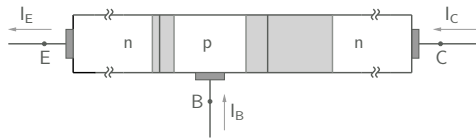


$$I_C = M \alpha_F I_E + M I_{CBO}$$

* Emitter open:



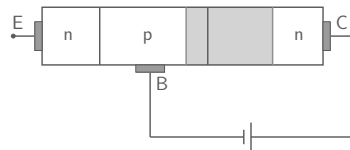
Avalanche breakdown



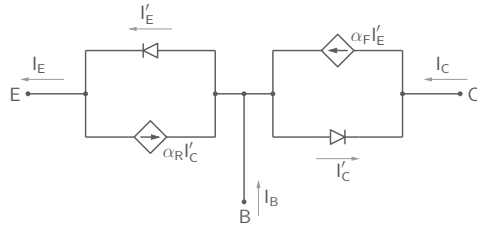
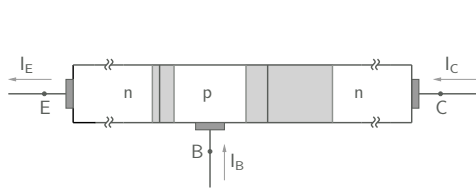
$$I_C = M \alpha_F I_E + M I_{CBO}$$

* Emitter open:

$$I_C = M I_{CBO} = I_{CBO} \times \frac{1}{1 - \left(\frac{V_R}{V_{BR_{BC}}} \right)^m}$$



Avalanche breakdown

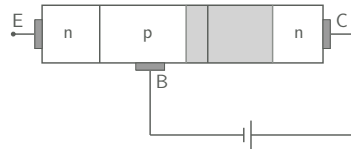


$$I_C = M \alpha_F I_E + M I_{CBO}$$

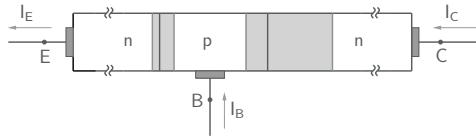
* Emitter open:

$$I_C = M I_{CBO} = I_{CBO} \times \frac{1}{1 - \left(\frac{V_R}{V_{BC}^{BR}} \right)^m}$$

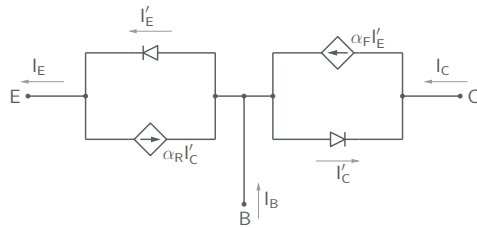
Breakdown voltage: As $V_R \rightarrow V_{BC}^{BR}$, $I_C \rightarrow \infty$, and therefore the breakdown voltage with the emitter open (denoted by V_{CBO}) is simply $V_{CBO} = V_{BC}^{BR}$.



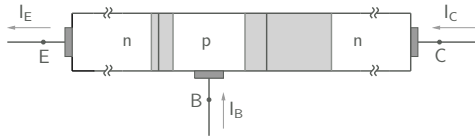
Avalanche breakdown



$$I_C = M \alpha_F I_E + M I_{CBO}$$

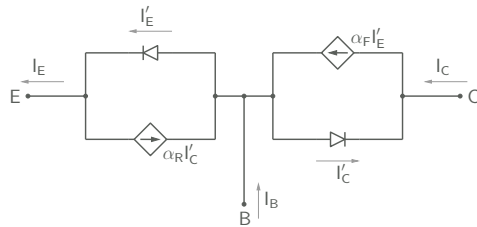


Avalanche breakdown

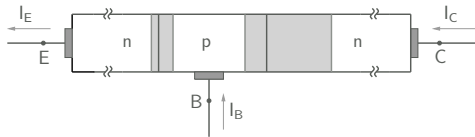


$$I_C = M \alpha_F I_E + M I_{CBO}$$

* Base open:

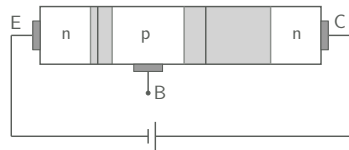
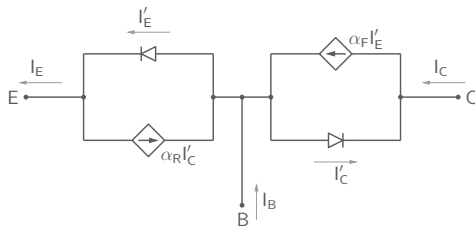


Avalanche breakdown

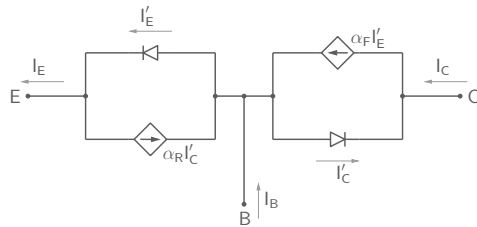
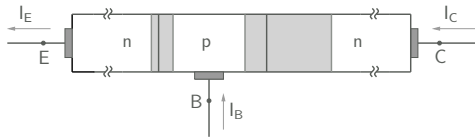


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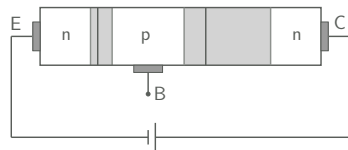
Avalanche breakdown



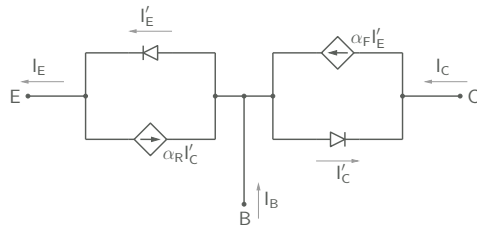
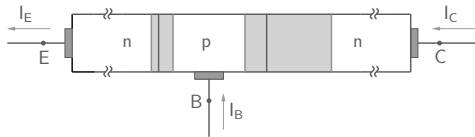
$$I_C = M \alpha_F I_E + M I_{CBO}$$

* Base open:

$$\begin{aligned} I_C &= M \alpha_F (I_C + I_B) + M I_{CBO} \\ &= M \alpha_F I_C + M I_{CBO} \end{aligned}$$



Avalanche breakdown

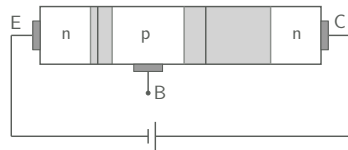


$$I_C = M \alpha_F I_E + M I_{CBO}$$

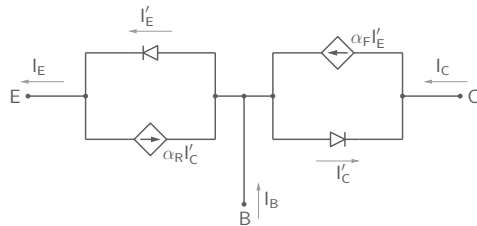
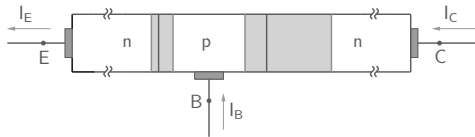
* Base open:

$$\begin{aligned} I_C &= M \alpha_F (I_C + I_B) + M I_{CBO} \\ &= M \alpha_F I_C + M I_{CBO} \end{aligned}$$

$$\rightarrow I_C (1 - M \alpha_F) = M I_{CBO} \rightarrow I_C = \frac{M I_{CBO}}{1 - M \alpha_F}$$



Avalanche breakdown



$$I_C = M \alpha_F I_E + M I_{CBO}$$

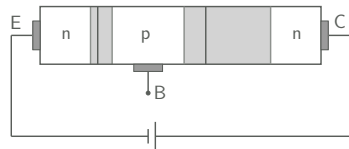
* Base open:

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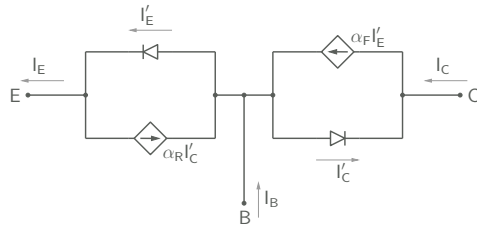
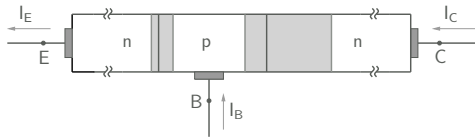
$$= M \alpha_F I_C + M I_{CBO}$$

$$\rightarrow I_C (1 - M \alpha_F) = M I_{CBO} \rightarrow I_C = \frac{M I_{CBO}}{1 - M \alpha_F}$$

$$\text{Breakdown condition: } M \alpha_F \rightarrow 1 \text{ or } M \rightarrow \frac{1}{\alpha_F}$$



Avalanche breakdown



$$I_C = M \alpha_F I_E + M I_{CBO}$$

* Base open:

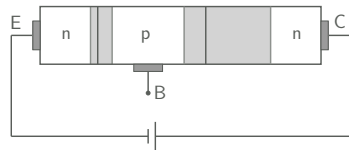
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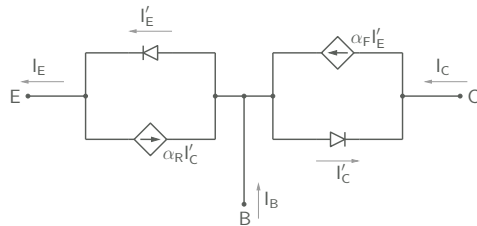
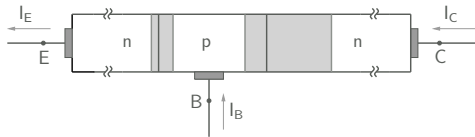
$$\rightarrow I_C (1 - M \alpha_F) = M I_{CBO} \rightarrow I_C = \frac{M I_{CBO}}{1 - M \alpha_F}$$

Breakdown condition: $M \alpha_F \rightarrow 1$ or $M \rightarrow \frac{1}{\alpha_F}$.

$$\rightarrow \frac{1}{1 - \left(\frac{V_R}{V_{BC}^{BR}} \right)^m} = \frac{1}{\alpha_F} = \frac{\beta_F + 1}{\beta_F}$$



Avalanche breakdown



$$I_C = M \alpha_F I_E + M I_{CBO}$$

* Base open:

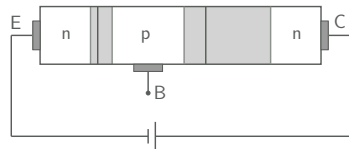
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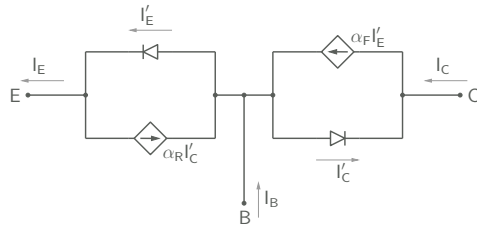
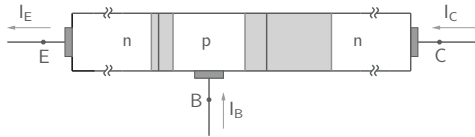
$$\text{Breakdown condition: } M \alpha_F \rightarrow 1 \text{ or } M \rightarrow \frac{1}{\alpha_F}$$

$$\rightarrow \frac{1}{1 - \left(\frac{V_R}{V_{BC}^{BR}} \right)^m} = \frac{1}{\alpha_F} = \frac{\beta_F + 1}{\beta_F}$$

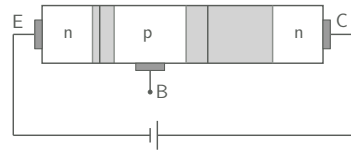
$$\rightarrow V_R \equiv V_{CEO} = \frac{V_{BC}^{BR}}{(\beta_F + 1)^{1/m}} \approx \frac{V_{CBO}}{\beta_F^{1/m}}$$



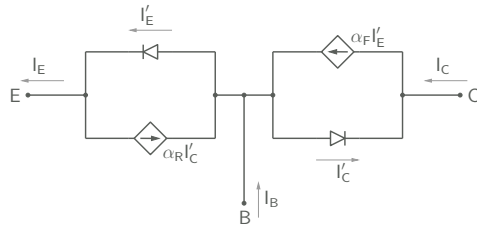
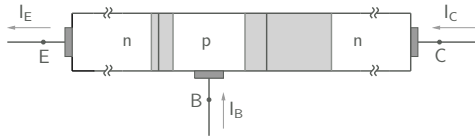
Avalanche breakdown



* Base open: $V_{CEO} = \frac{V_{BC}^{BR}}{(\beta_F + 1)^{1/m}} \approx \frac{V_{CBO}}{\beta_F^{1/m}}$.

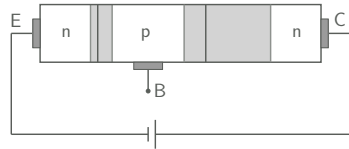


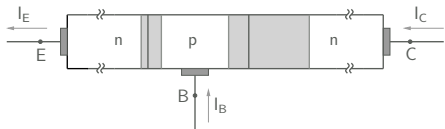
Avalanche breakdown



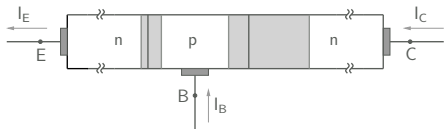
* Base open:
$$V_{CEO} = \frac{V_{BC}^{BR}}{(\beta_F + 1)^{1/m}} \approx \frac{V_{CBO}}{\beta_F^{1/m}}.$$

As an example, for $\beta_F = 200$ and $m = 4$, $V_{CEO} = V_{CBO}/3.8$, which is significantly smaller than V_{CBO} .



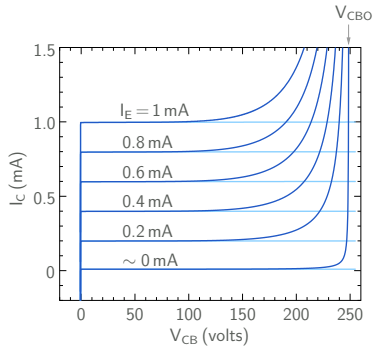


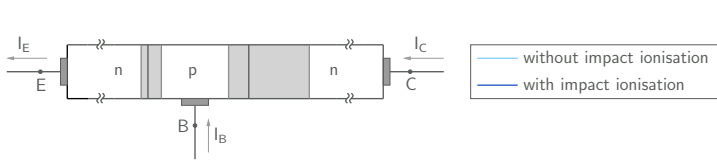
Significance of V_{CBO} and V_{CEO} :



— without impact ionisation
 — with impact ionisation

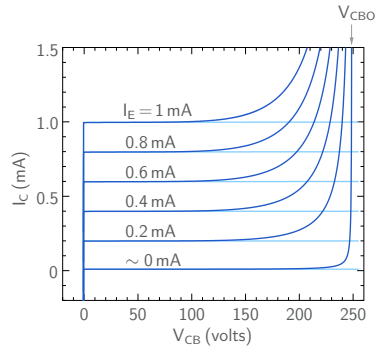
Significance of V_{CBO} and V_{CEO} :

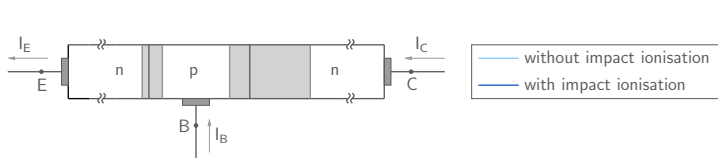




Significance of V_{CBO} and V_{CEO} :

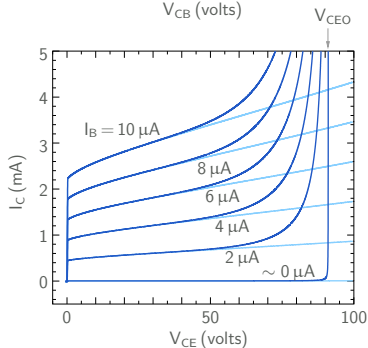
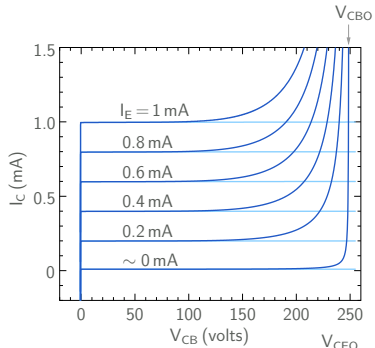
- * When $I_E = 0$, the breakdown voltage (V_{CB}) is given by V_{CBO} .
In the above example, it is ~ 230 V.

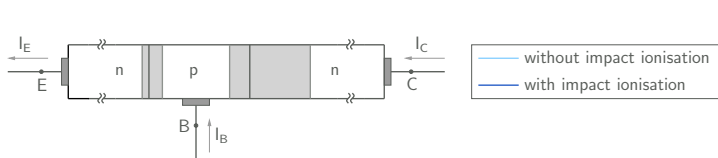




Significance of V_{CBO} and V_{CEO} :

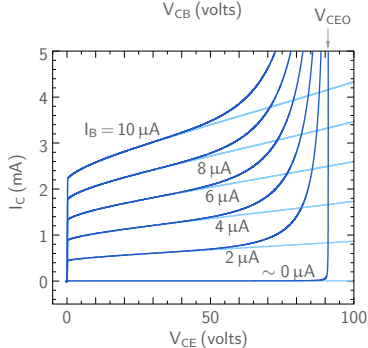
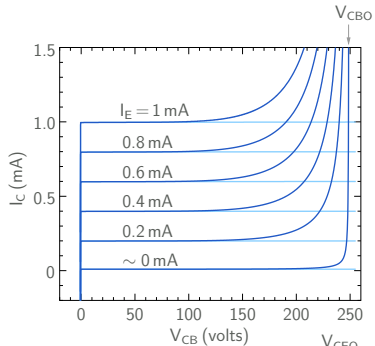
- * When $I_E = 0$, the breakdown voltage (V_{CB}) is given by V_{CBO} . In the above example, it is ~ 230 V.

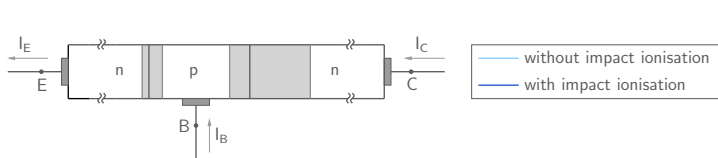




Significance of V_{CBO} and V_{CEO} :

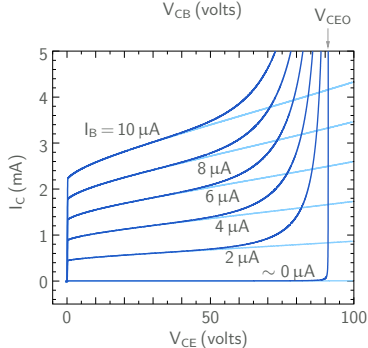
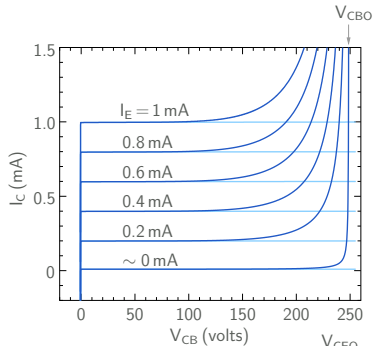
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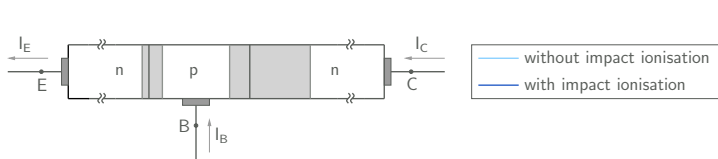




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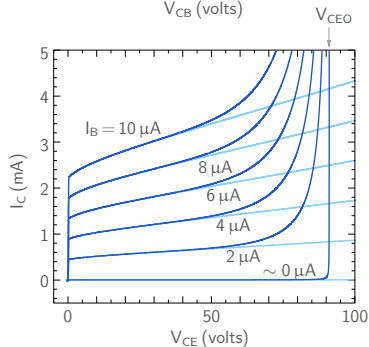
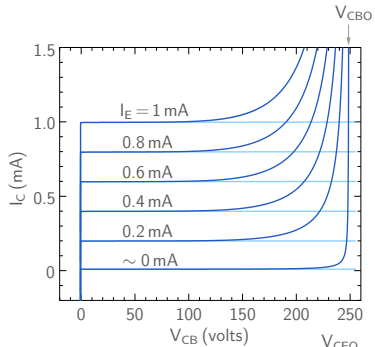
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- * Note that the slope of the I_C - V_{CE} curves in the linear region without impact ionisation is because of base width modulation.

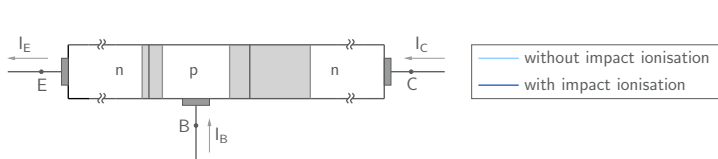




$$I_C = M \alpha_F I_E + M I_{CBO}$$

$$= \frac{M \alpha_F I_B}{1 - M \alpha_F} + \frac{M I_{CBO}}{1 - M \alpha_F}, \text{ with } M = \frac{1}{1 - \left(\frac{V_R}{V_{BR}^{BC}} \right)^m}$$

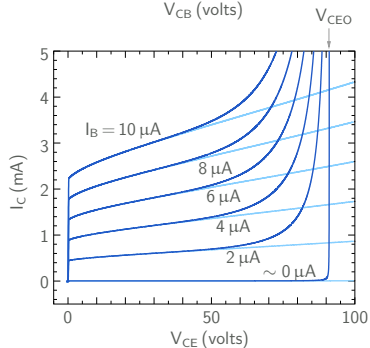
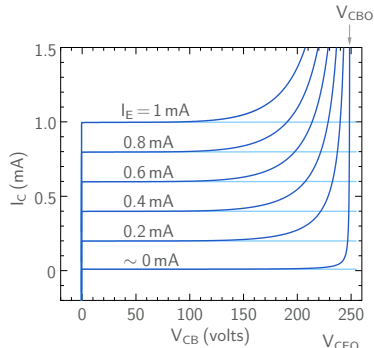


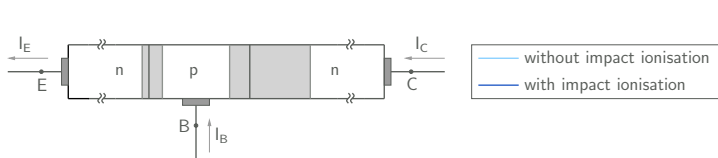


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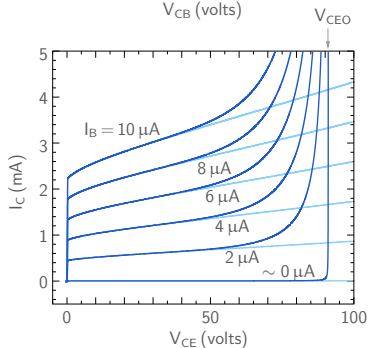
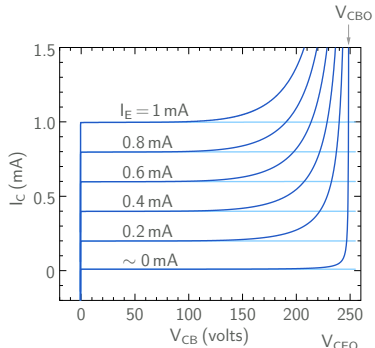




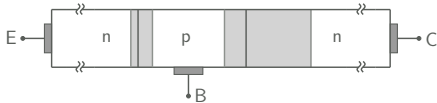
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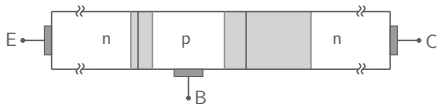


$V_{CEO} < V_{CBO}$: qualitative explanation



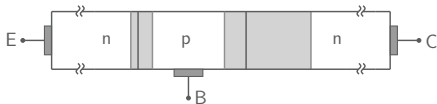
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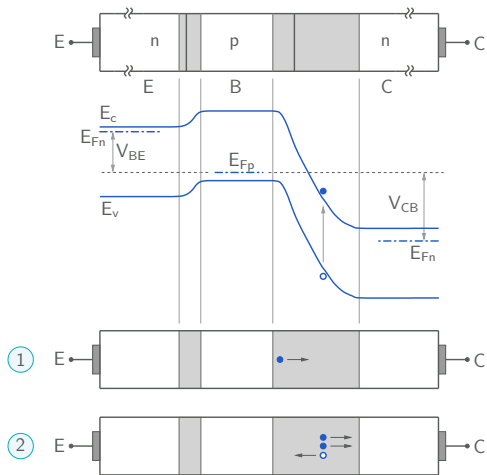
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- * With the base open, the situation is different.

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Consider an electron undergoing impact ionisation with the base open.

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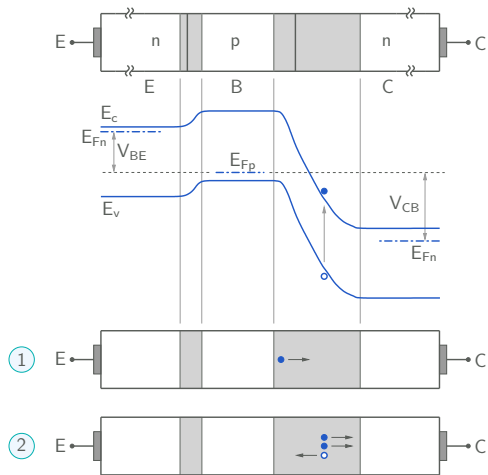
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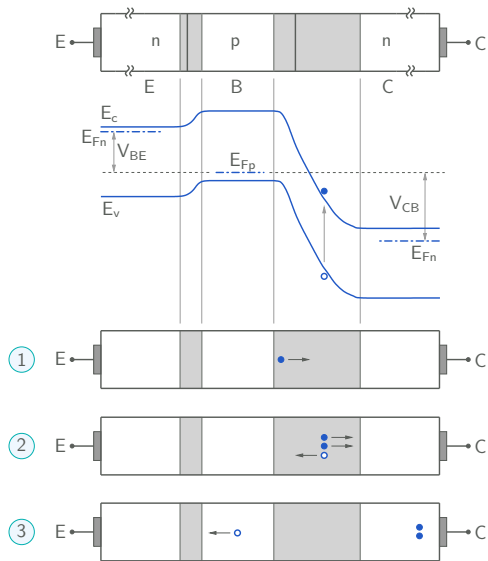
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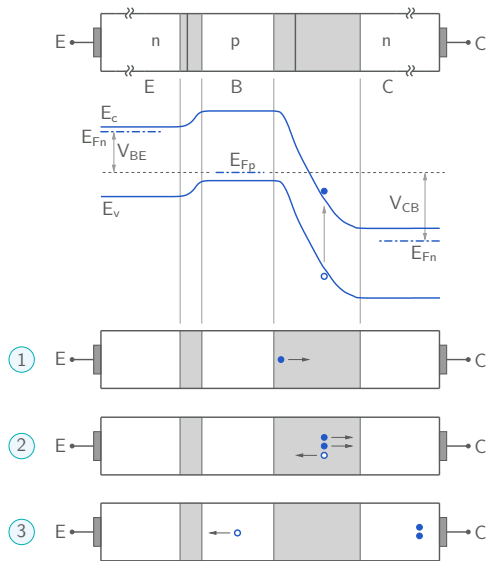
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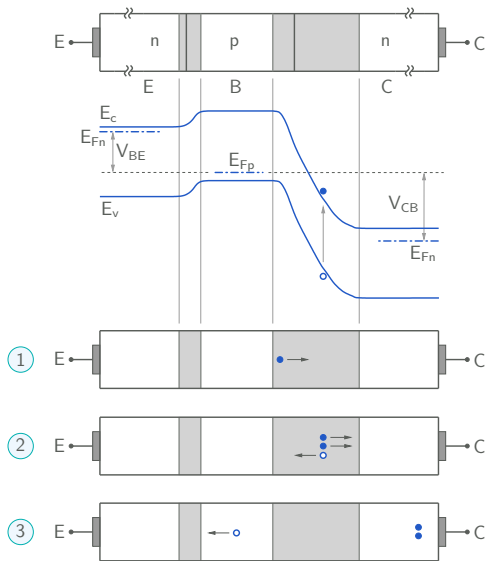


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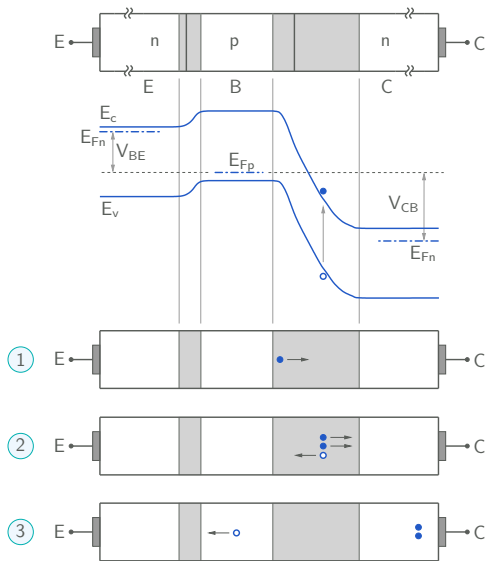


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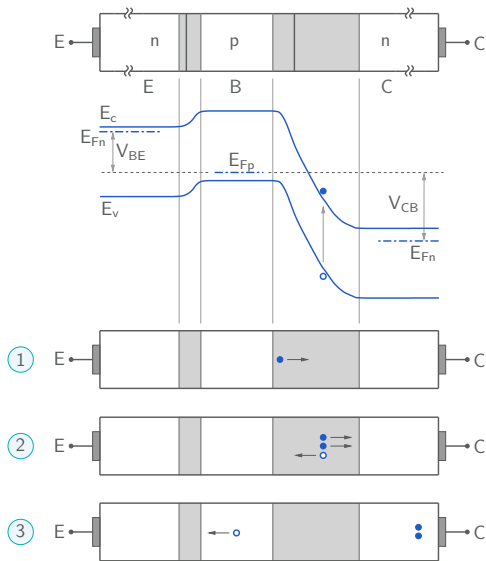
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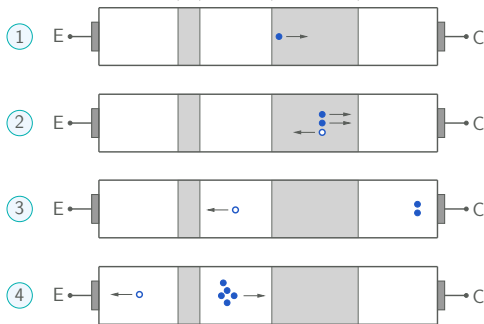
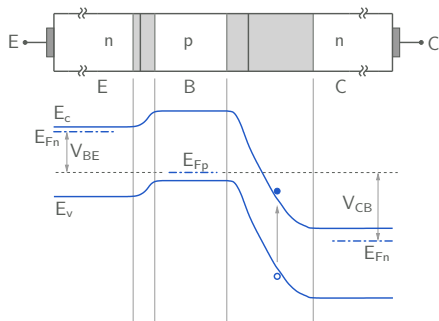
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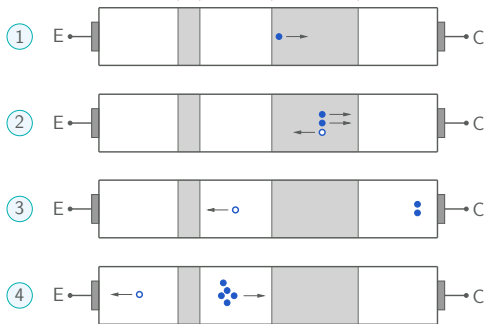
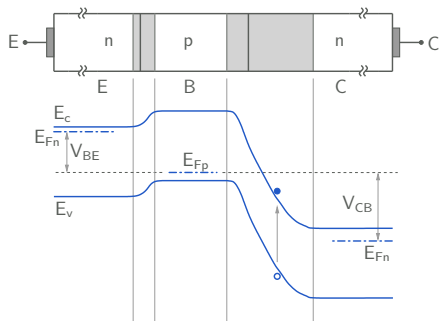
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→ multiplication of carriers is enhanced → lower breakdown voltage [Pierret].



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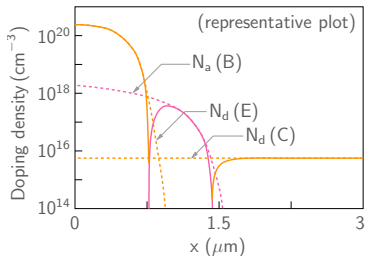
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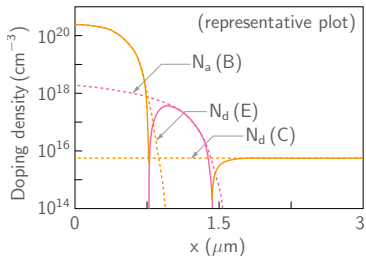


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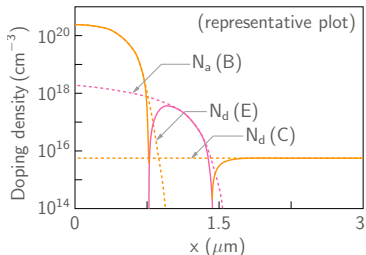
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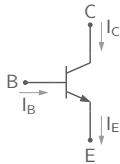
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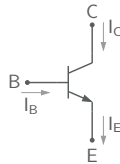
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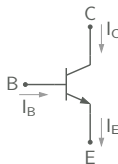
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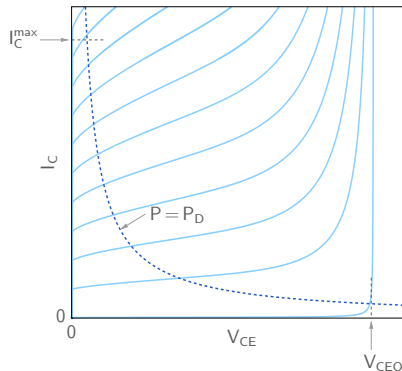
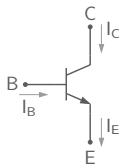
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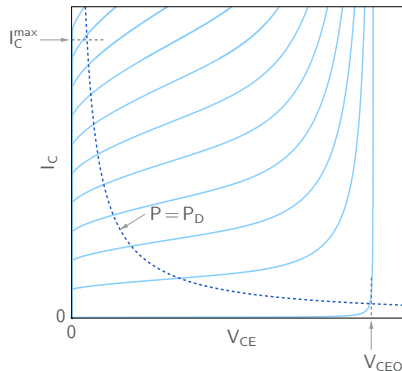
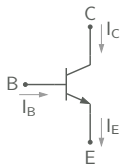
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In designing a BJT amplifier, the DC bias values are subject to the constraints due to I_C^{\max} , V_{CE0} , and P_D .



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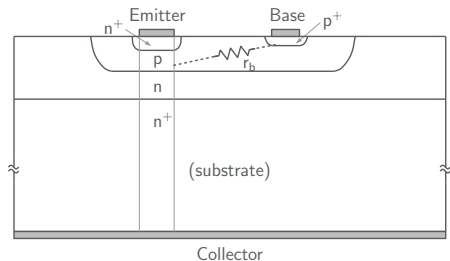
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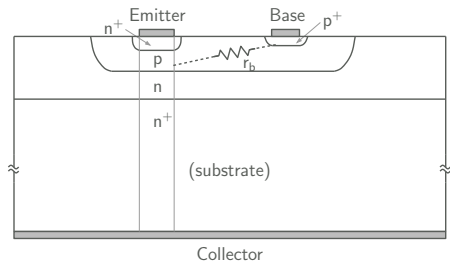


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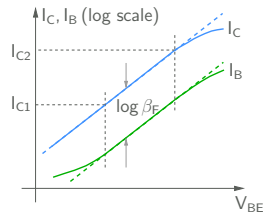
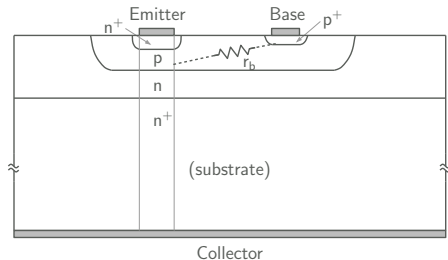


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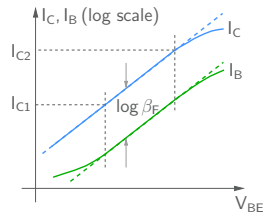
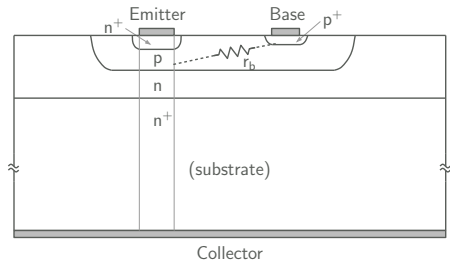
A typical discrete transistor: 2N3904 (*npn*)

Ebers-moll model (active mode):

$$I_C = \alpha_F I_{ES} e^{V_{BE}/V_T}, \quad I_B = (1 - \alpha_F) I_{ES} e^{V_{BE}/V_T}.$$

- * At lower values of V_{BE} , the diffusion component of the E - B diode current becomes small, and recombination in the emitter-depletion region, which adds to the base current, cannot be neglected any more. This causes I_B to be larger than that predicted by the above equation.
- * At high values of V_{BE} (large I_C),
 - The voltage drop across the base resistance r_b becomes significant.
 - The minority carrier concentration in the base becomes comparable to the majority carrier concentration (high-level injection)

As a result, $\beta_F = \frac{I_C}{I_B}$ is constant only for a range of I_C values.



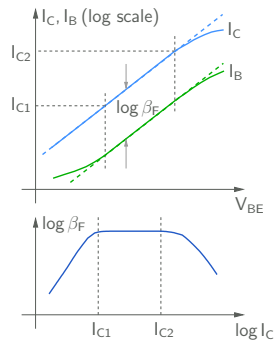
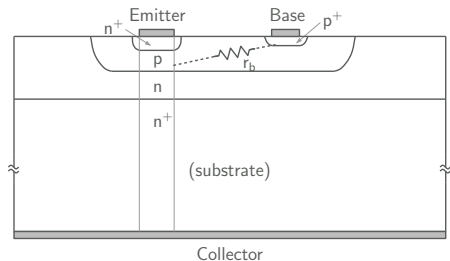
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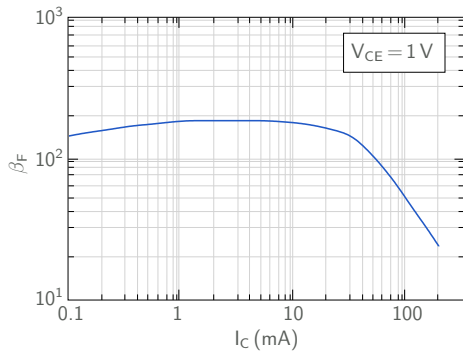
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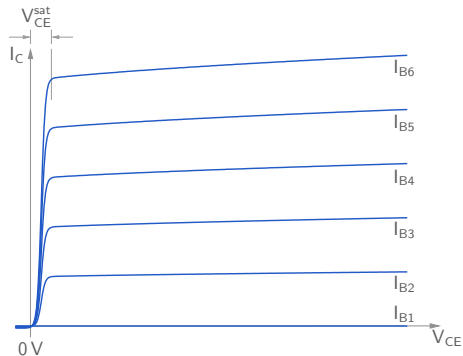
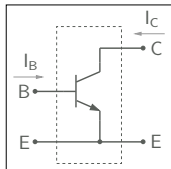
A typical discrete transistor: 2N3904 (*n*pn)



(Note: β_F varies from device to device.)

A typical discrete transistor: 2N3904 (*npn*)

* $V_{CE}^{\text{sat}} = 0.2\text{ V}$ at $I_C = 10\text{ mA}$, $V_{CE}^{\text{sat}} = 0.3\text{ V}$ at $I_C = 50\text{ mA}$.



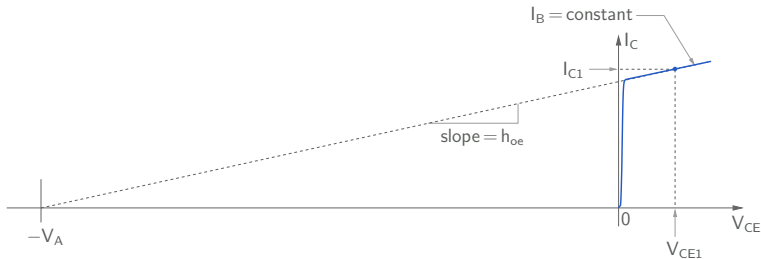
A typical discrete transistor: 2N3904 (*npn*)

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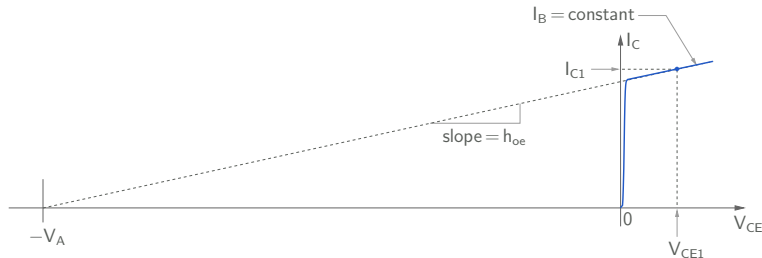
The slope of the I_C versus V_{CE} curve at a constant I_B is defined as the output conductance h_{oe} .



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The slope of the I_C versus V_{CE} curve at a constant I_B is defined as the output conductance h_{oe} .



From h_{oe} , we can get an idea of the Early voltage V_A of the device. For example, with $h_{oe} = 10 \mu\Omega$, $I_{C1} = 1$ mA, $V_{CE1} = 10$ V, we get

$$\frac{I_{C1}}{V_A + V_{CE1}} = h_{oe} \rightarrow V_A = \frac{1 \times 10^{-3}}{10 \times 10^{-6}} - 10 = 90 \text{ V.}$$