clock_1b.xbe

Attributes

```
xbe name=clock_1b evaluate=yes limit_tstep=yes allow_ssw=yes allow_elex=yes
+  elex_limit_tstep=yes
# clock source, with f_hz and duty cycle specified
Jacobian: constant
input_vars:
output_vars: y
aux_vars:
iparms:
sparms:
rparms:
+  f_hz=1e3
+  D=0.5
+  y_high=1
+  delta1=0.01
+  delta2=0.01
+  elex_dt=1.0e-10
+  t0=0
stparms:
igparms:
outparms: y
```

Description

clock_1b.xbe is similar to clock_1.xbe except that it allows D and f_hz to be changed during simulation using the set_rparm statement. y is made available as an output variable.