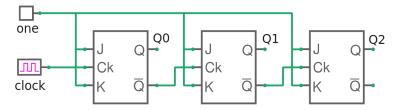
ee101_counter_2a.sqproj



(Flip-flops are positive-edge triggered)

This example illustrates the working of a counter made up of JK flip-flops. Note that J=K=1 for all flip-flops.

Exercise Set

- 1. Assuming that all flip-flops are originally reset, sketch the outputs Q_0 , Q_1 , Q_2 as a function of time. Verify with simulation.
- 2. How would the plots change if negative edge-triggered flip-flops are used?