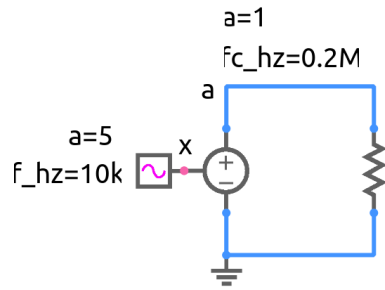


test_vsrc_fm.sqproj



The purpose of the simulation set-up shown in the figure is to demonstrate frequency modulation. $x(t)$ is the modulating signal, and $V_a(t)$ is the output of the FM source.

Exercise Set

1. Run the simulation. Plot x and V_a versus time, and observe how the frequency of $V_a(t)$ is modulated by the input signal $x(t)$.