

Sr. No.	Title of the books	Author's Name	Tag No.	Publisher
1.	Digital Design with CPLD Applications and VHDL	Robert K. Dueck	1	Delmar Thomson
2.	CMOS Analog Circuit 2 nd edn	Philip E. Allen, Douglas R. Holberg	2	New York Oxford
3.	CMOS Circuit Design, Layout, And Simulation	R. Jacob baker, Harry W. Li, David E. Boyce	3	Prentice Hall of India Pvt. Ltd.
4.	CMOS Circuit Design, Layout, And Simulation	R. Jacob baker, Harry W. Li, David E. Boyce	4	Prentice Hall of India Pvt. Ltd.
5.	VHDL 3 rd edn	Douglas Perry	5	
6.	Algorithms 3 rd edn in C++ Parts 1-4	Robert Sedgewick	6	Princeton University
7.	VHDL Coding and Logic Synthesis with Synopsys	Weng Fook Lee	7	
8.	A VHDL Synthesis Primer 2 nd edn	J. Bhasker	8	BS Publication
9.	Verilog HDL A Guide to Digital Design and Synthesis 2 nd edn	Samir Palnitkar	9	Pearson Education
10.	Power Aware Computing	Robert Graybill, Rami Melhem	10	Kluwer Academic/ Plenum Publishers
11.	VHDL 3 rd Edn	Douglas L. Perry	11	Mc Graw Hill
12.	VHDL and AHDL Digital System Implementation	Frank A. Scarpino,	12	Prentice Hall `
13.	Verilog HDL Synthesis A Practical Primer	J. Bhasker	13	BS Publication
14.	Introductory VHDL form Simulation to Synthesis	Sudhakar Yalamanchili	14	Pearson Education Asia
15.	Data Structures using C and C ++ 2 nd edn	Yedidyah Langsam, Moshe J. Augenstein, Aaron M. Tenebaum	15	Pearson Education
16.	The Designer's Guide to VHDL 2 nd edn	Peter J. Ashenden	16	Morgan Kaufmann
17.	VHDL Starter's Guide	Sudhakar Yalamanchili	17	Prentice Hall
18.	Design of High-Performance Microprocessor Circuit	Anantha Chandrakasan, William J. Bowhill, Frank Fox	18	IEEE Press
19.	A Verilog HDL Primer 2 nd edn	J. Bhasker	19	BS Publication
20.	VHDL Designer's Reference	Jean Michel, Berge Alain, Fonkoua, Serge Maginot, Jacques Rouillard	20	Kluwer Academic Publishers
21.	Design of Analog CMOS Integrated Circuits	Behzad Razavi	21	Tata McGraw-Hill Publishing Co. Ltd.
22.	CMOS Digital Integrated Circuits Analysis and Design 3 rd edn	Sung-Mo (Steve) Kang, Yusuf Leblebici	22	Tata McGrawHill
23.	The Verilog Hardware Description Language 4 th edn	Donald E. Thomas, Philip R. Moorby	23	
24.	VHDL Analysis and Modeling of Digital Systems 2 nd edn	Zainalabendin Navabi	24	
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27.	VLSI Testing Digital and Mixed	Stanley Hurst	27	

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29.	VLSI Memory Chip Design	Kiyoo Itoh	29	Springer
30.	VLSI RISC Architecture and Organization	Stephen B. Furber	30	Marcel Dekker Inc.

31.	The VLSI Hand Book	Wai-Kai Chen	31	CRC Press & IEEE Press
32.	The VLSI Hand Book	Wai-Kai Chen	32	CRC Press & IEEE Press
33.	VLSI for Wireless Communication	Bosco Leung	33	Pearson Education Asia

34.	Introduction to VLSI Systems	Carver Mead – Lynn Conway	34	BS Publications
35.	VLSI Design Techniques for Analog and Digital Circuits	Randall L. Geiger, Phillip E. Allen, Noel R. Strader	35	Mc Graw Hill International Editions
36.	CMOS/BiCMOS ULSI Low Voltage Low Power	Kiat – Seng Yeo Samir S. Rofail, Wang-Ling Goh	36	Pearson Education Asia
37.	Algorithms for VLSI Design Automation	Sabin H. Gerez	37	
38.	Optimal VLSI Architectural Synthesis Area, Performance and Testability	Catherine H. Gebotys, Mohamed I. Elmasry	38	Kluwer Academic Publishers
39.	Introduction to VLSI Design	Eugene D. Fabricius	39	Eugene D. Fabricius
40.	Logic Minimization Algorithms for VLSI Synthesis	Robert K. Brayton, Gary D. Hachtel, Curtis T. Mc Mullen, Alberto L. Sangiovanni-Vincentelli	40	Kluwer Academic Publishers
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44.	VLSI Design Techniques for Analog and Digital Circuits	Randall L. Geiger, Phillip E. Allen, Noel R. Strader	44	McGraw Hill International Editions
45.	VLSI Digital Signal Processing Systems Design and Implementation	Keshab K. Parhi	45	John Wiley & Sons, Inc.
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47.	Algorithms and Data Structures in VLSI Design OBDD Foundations and Applications	Christoph Meinel, Thorsten Theobald	47	Springer – Verlag
48.	An Introduction to VLSI Physical Design	M. Sarrafzadeh, C.K. Wong	48	McGraw Hill Companies, Inc.
49.	VHDL for Logic Synthesis 2 nd edn	Andrew Rushton	49	John Wiley & Sons, Inc.
50.	Fundamentals of Modern VLSI Devices	Yuantaur, Tak H. Ning	50	
51.	Digital Integrated Circuits A Design Perspective	Jan M. Rabaey	51	
52.	Digital Design 3 rd edn	M. Morris Mano	52	Pearson Education Asia
53.	Digital Integrated Circuits A Design Perspective 2 nd Edn `	Jan M. Rabaey, Anantha Chandrakasan, Borivoje Nikolic	53	Prentice Hall India Pvt. Ltd
54.	Digital Designing with Programmable Logic Devices	John W. Carter	54	Prentice Hall
55.	Digital BiCMOS Integrated Circuit Design	Sherif H.K. Embabi, Abdellatif Bellaouar, Mohamed I. Elmasry	55	Kluwer Academic Publishers
56.	Digital Integrated Circuits A Design Perspective	Jan M. Rabaey	56	Prentice Hall India Pvt. Ltd
57.	Fundamentals of Digital Logic with verilog Design	Stephen Brown, Zvonko Vranesic	57	TATA McGrawHill
58.	Digital Systems Principles & Applications	Ronald J. Tocci	58	Prentice Hall India Pvt. Ltd

59.	High –Speed Digital Design A Hand Book of Black Magic	Howard Johnson, Martin Graham	59	Pearson Education
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61.	Digital Design Principles & Practices 3 rd edn	John F. Wakerly	61	Prentice Hall International Inc.
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142.	IEEE Symposium on Fielo-Programmable custom Computing Machines FCCM '99	Napa Valley, California	142	IEEE Computer Society
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